

D

D

C

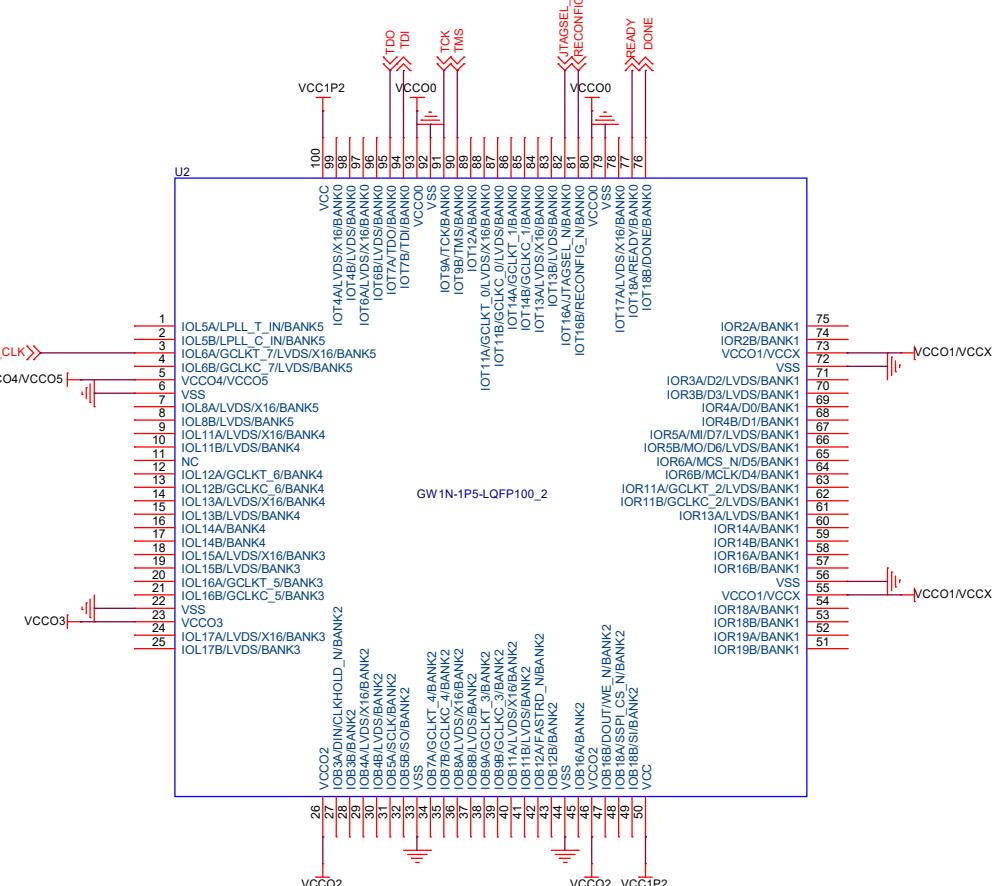
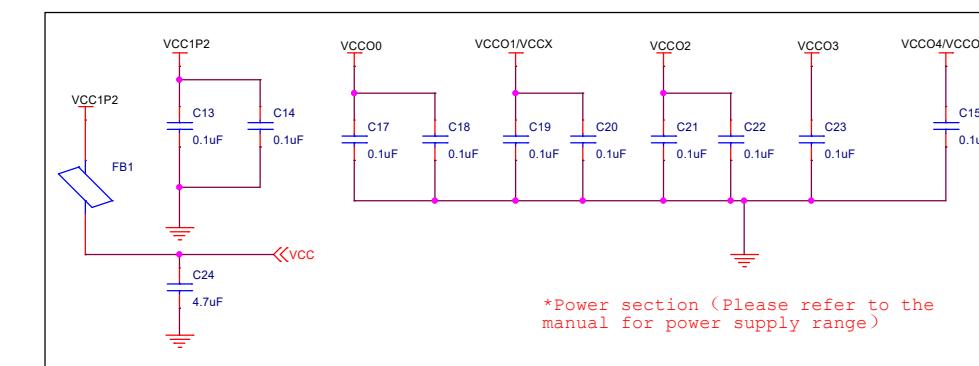
C

B

B

A

A



R10 33

JTAG mode selection signal section

R11 4.7K

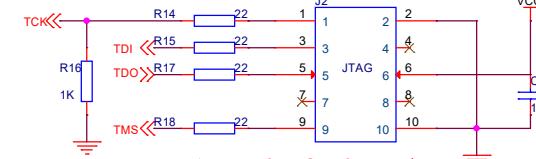
*Configuration reset section (low level)

R12 4.7K

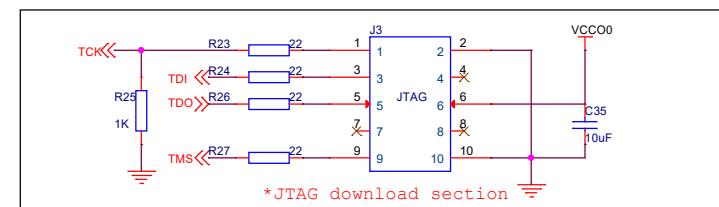
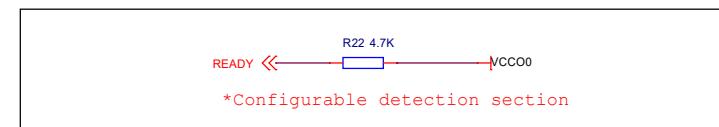
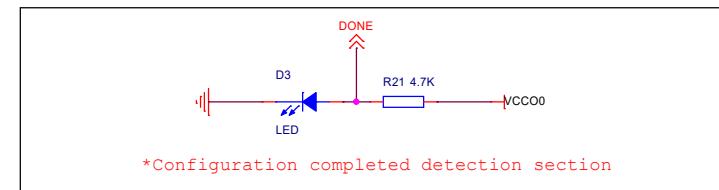
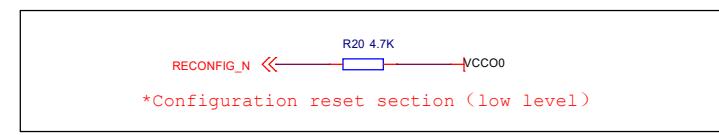
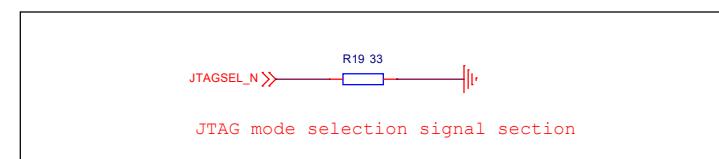
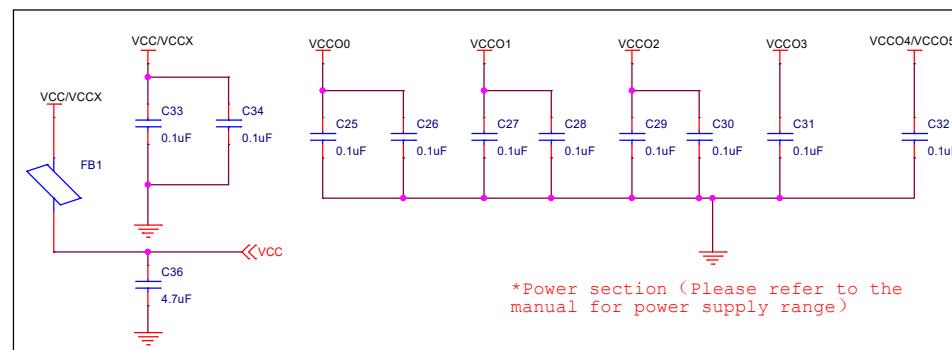
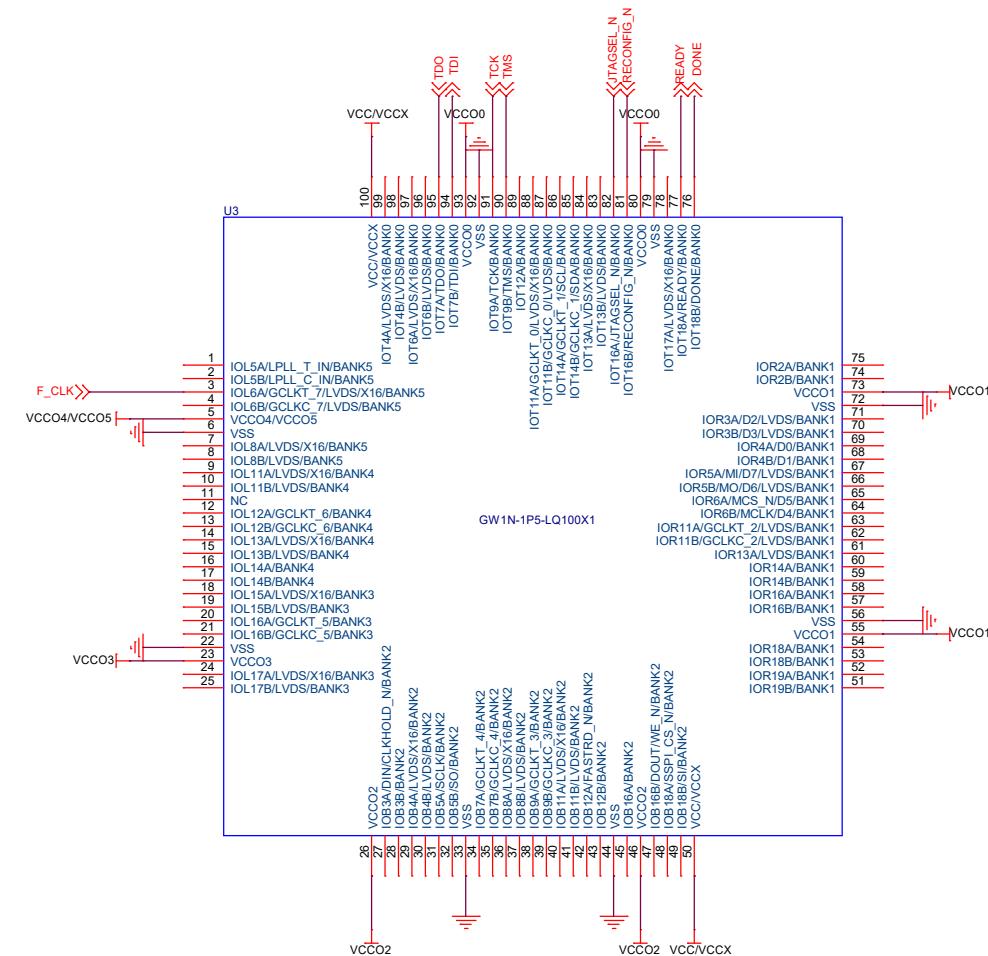
*Configuration completed detection section

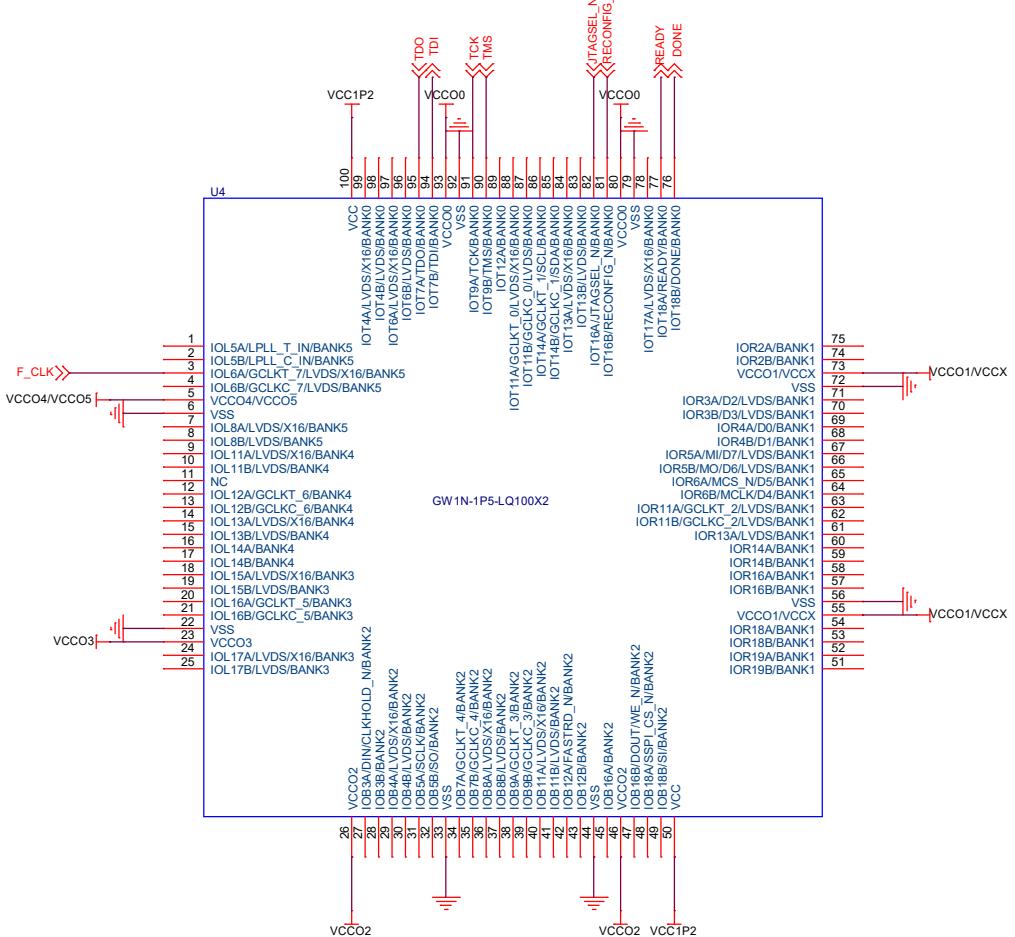
R13 4.7K

*Configurable detection section



D





JTAGSEL_N >> R28 33

JTAG mode selection signal section

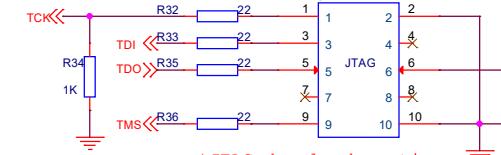
R29 4.7K << VCCO0

*Configuration reset section (low level)

LED D4 R30 4.7K VCCO0 DONE

R31 4.7K << VCCO0

*Configurable detection section



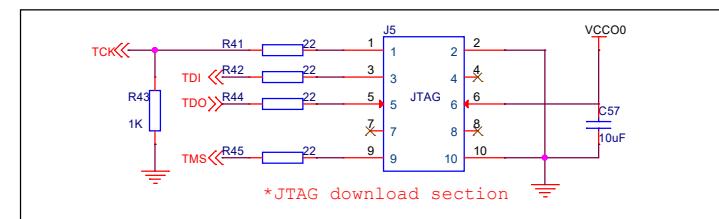
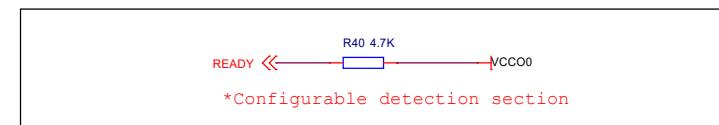
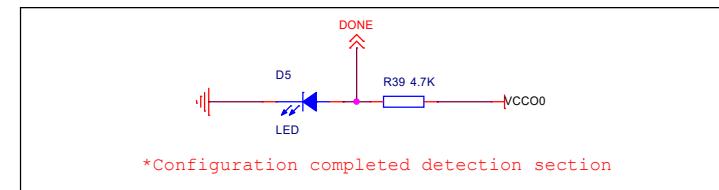
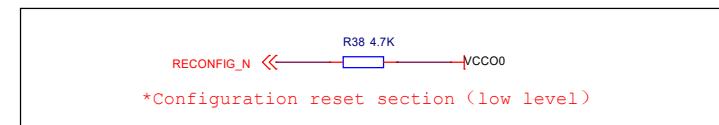
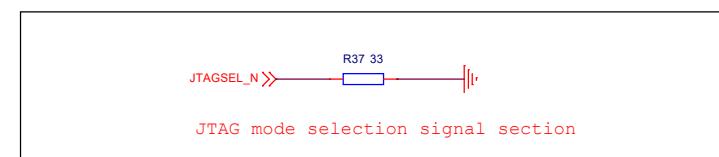
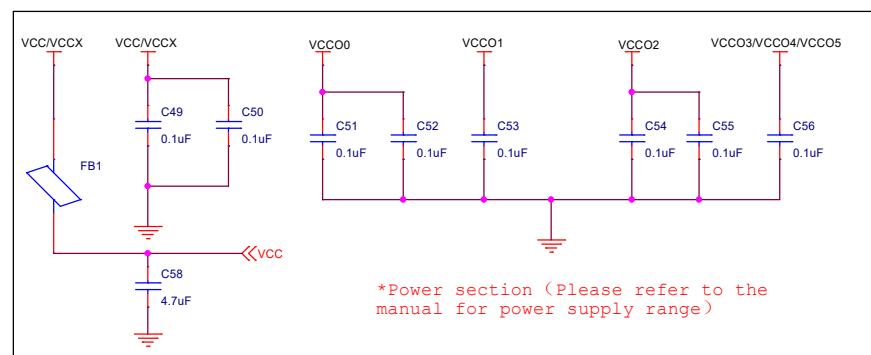
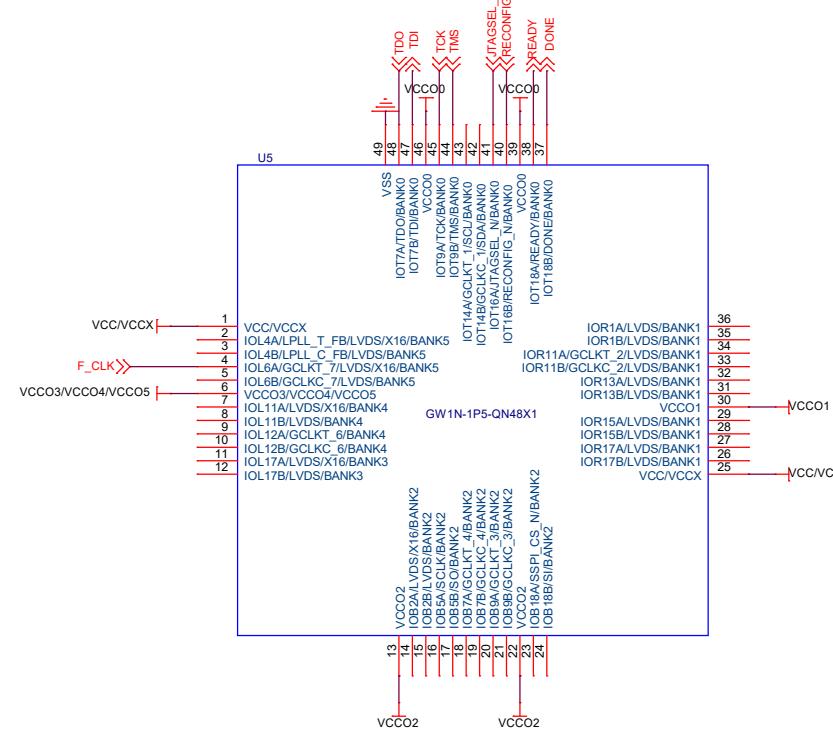
*JTAG download section

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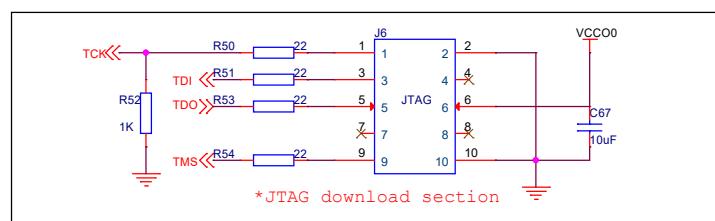
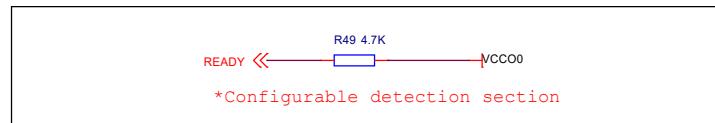
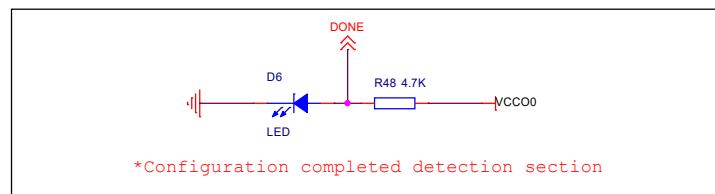
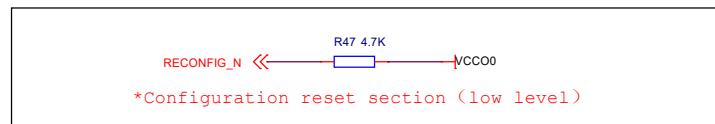
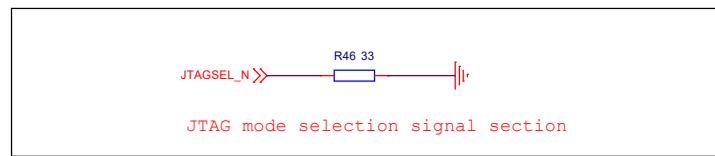
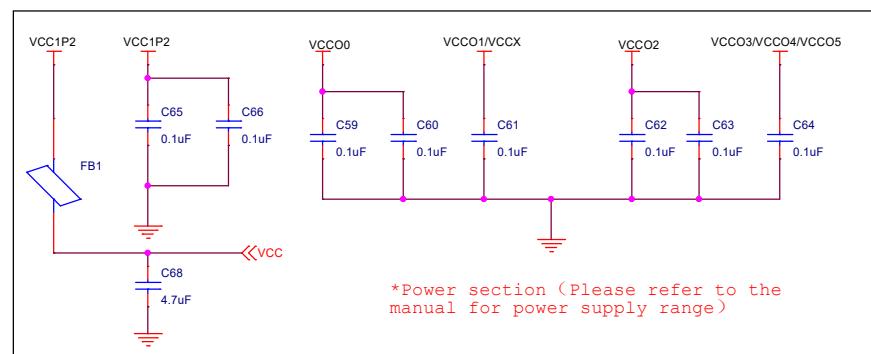
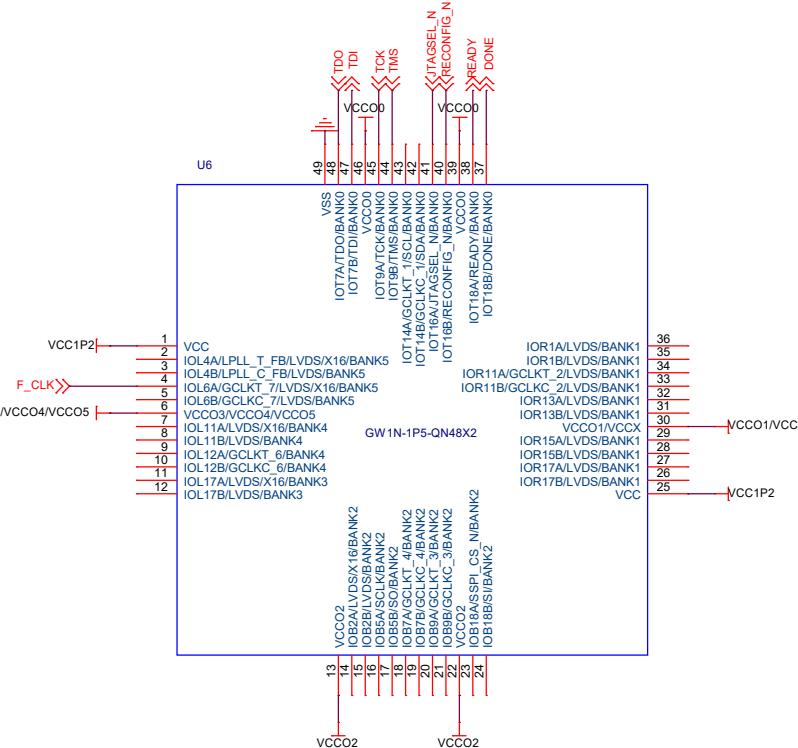
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