

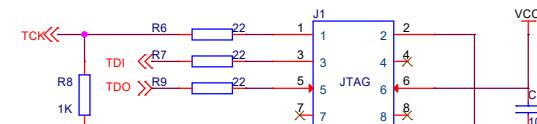
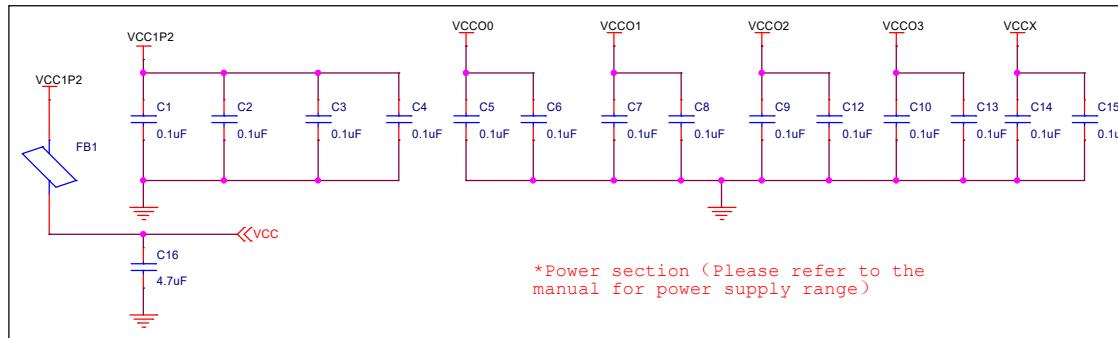
JTAG mode selection signal section

*Configuration mode signal selection section

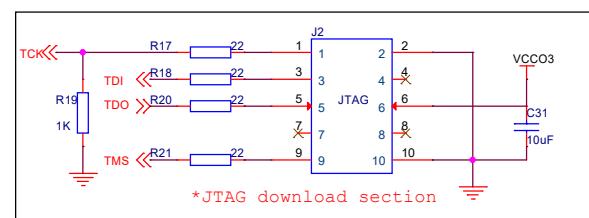
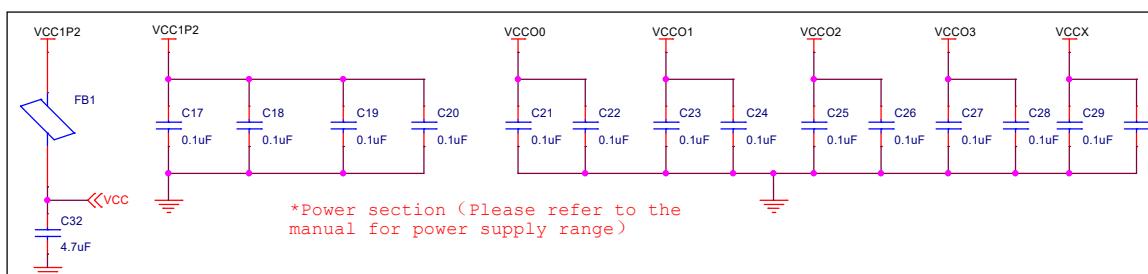
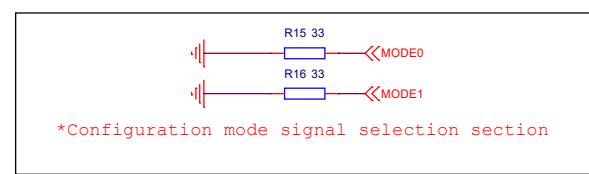
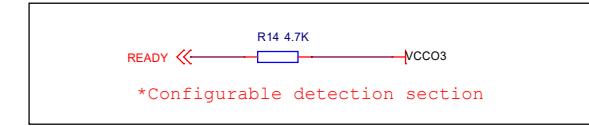
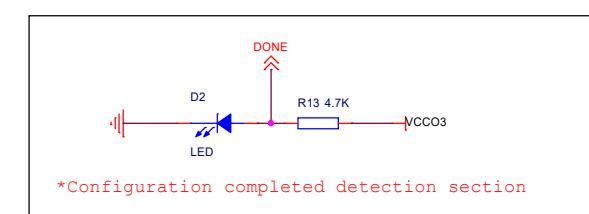
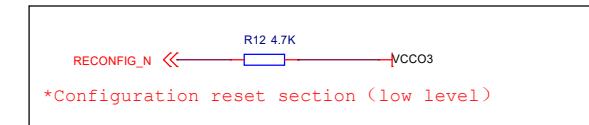
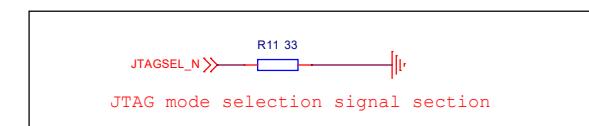
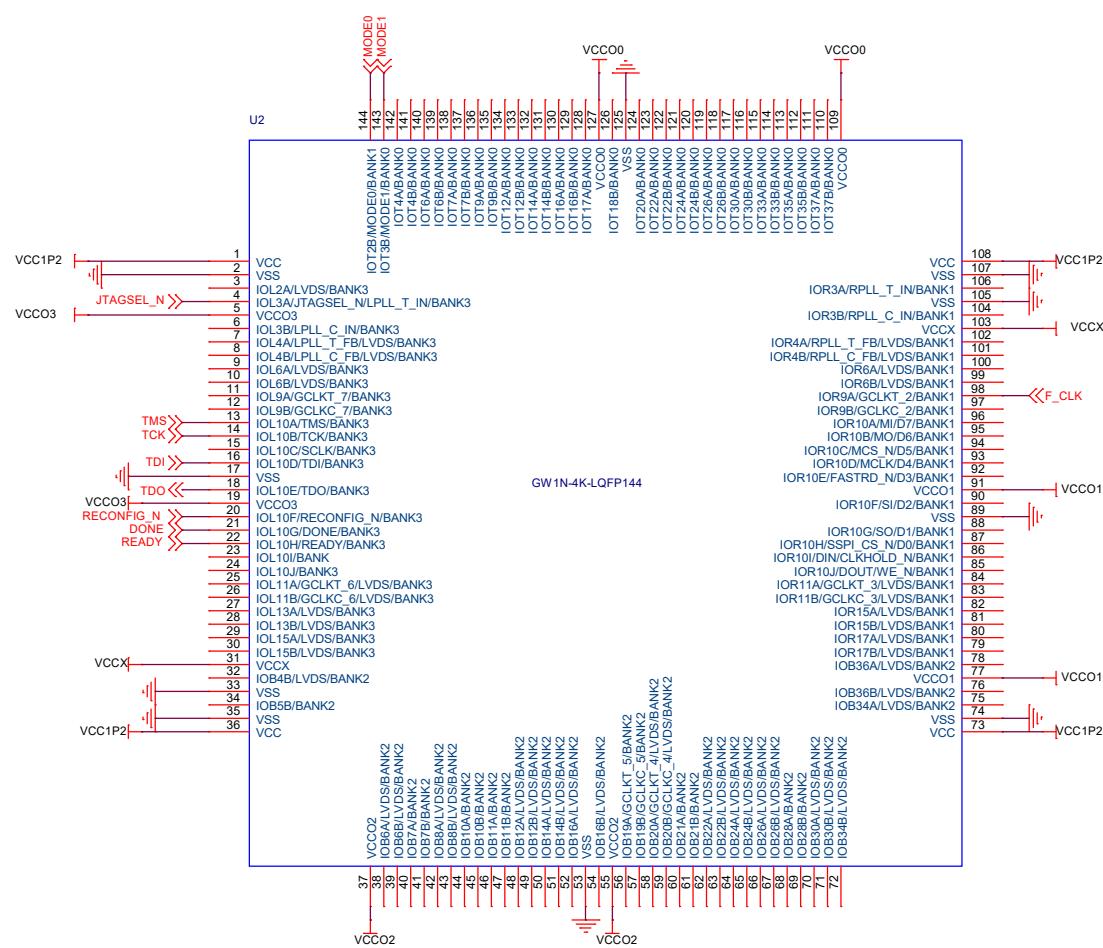
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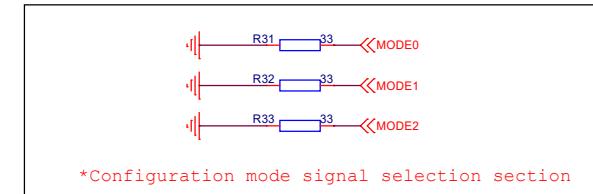
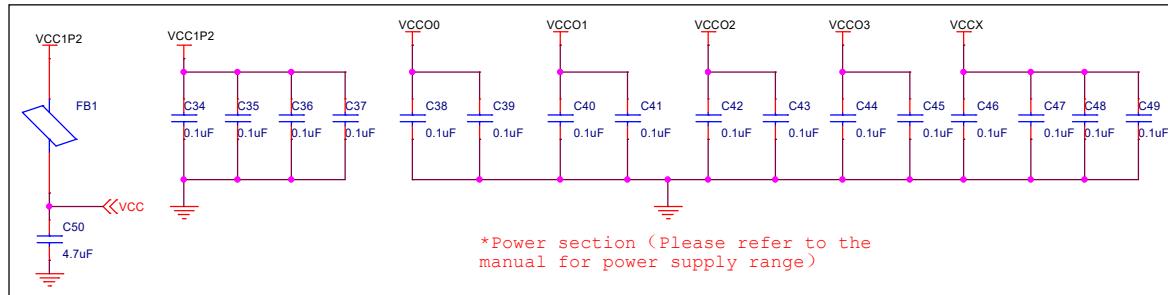
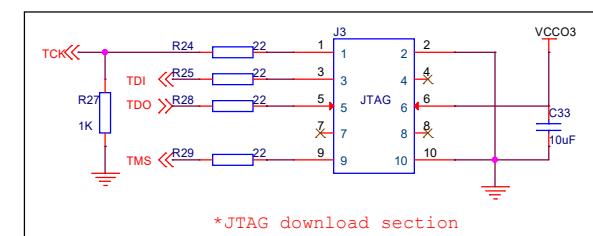
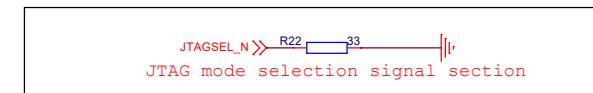
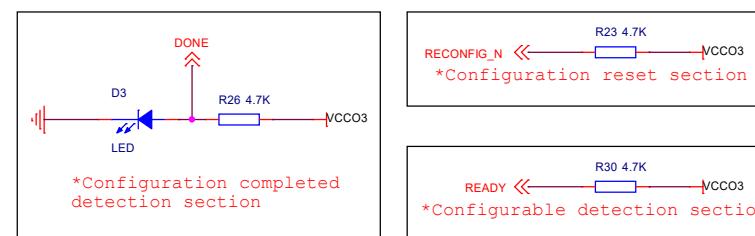
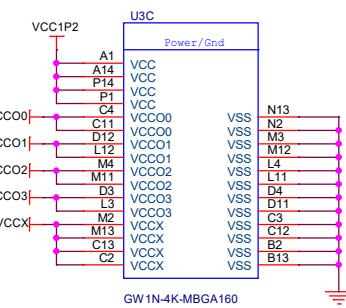
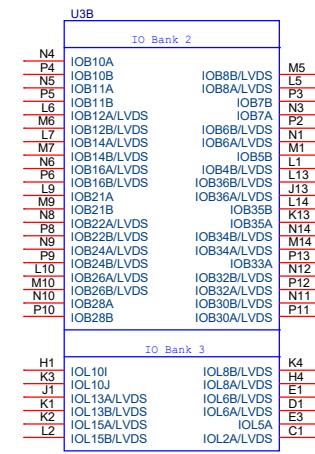
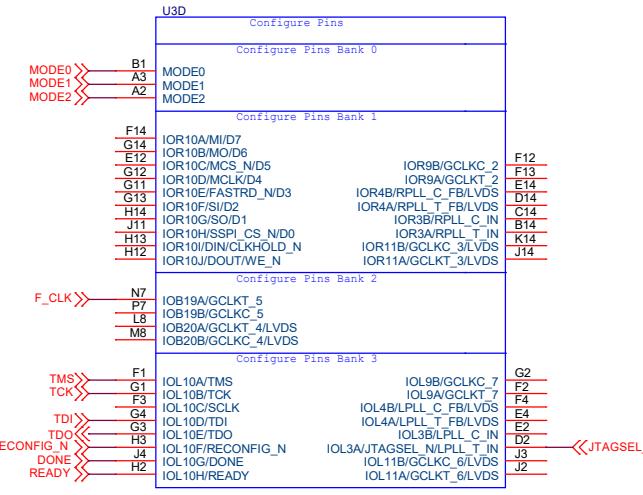
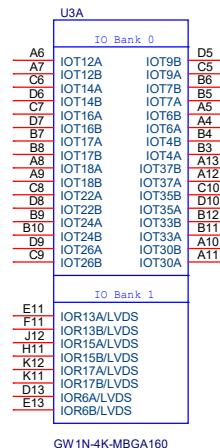
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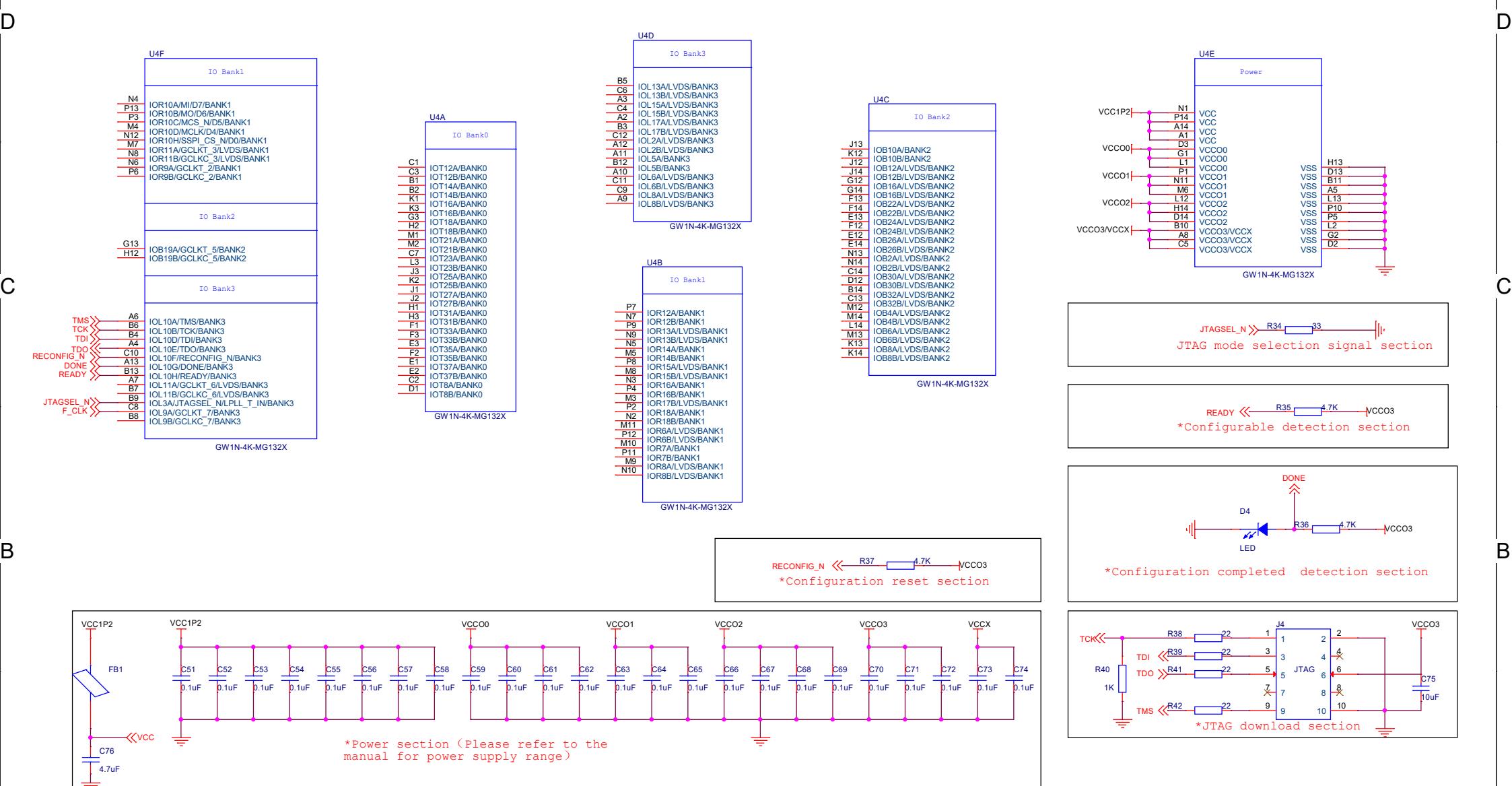
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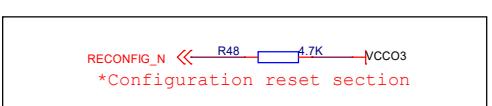
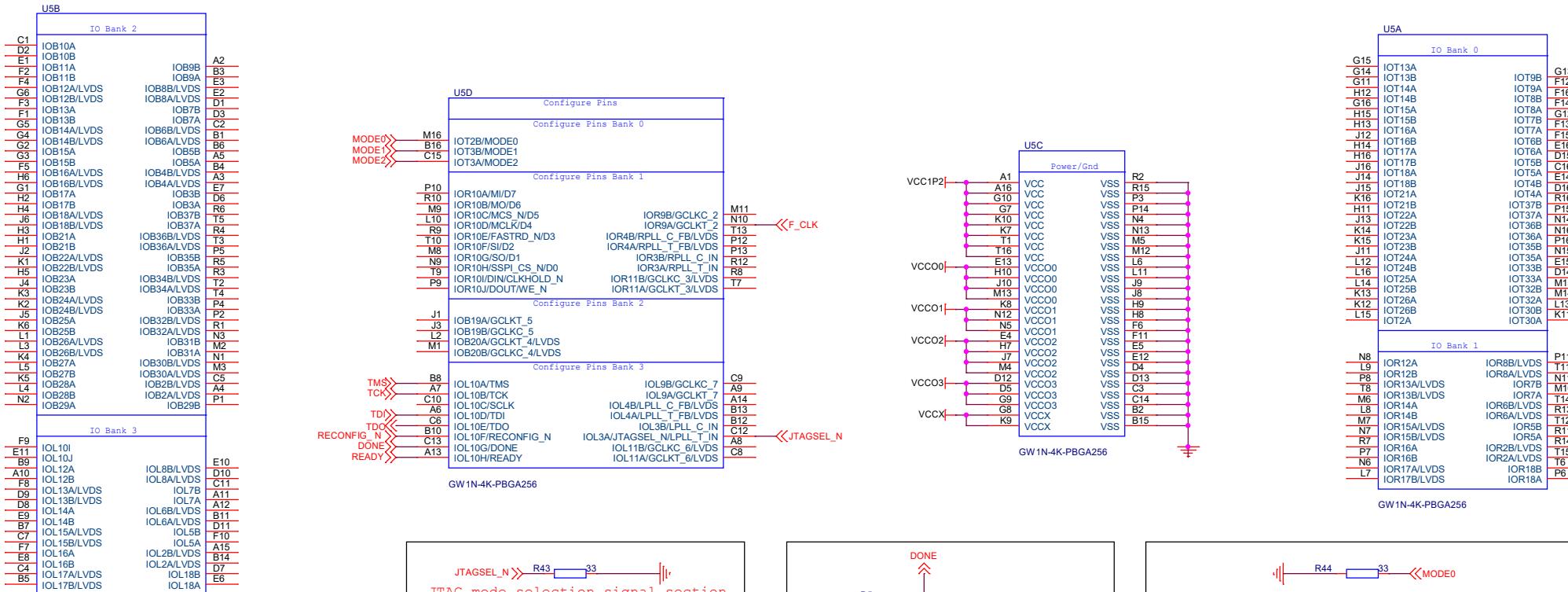


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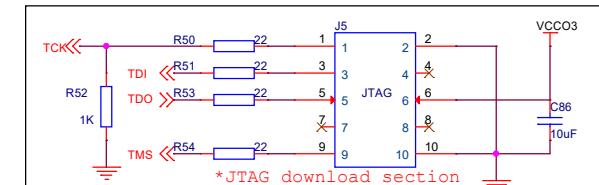
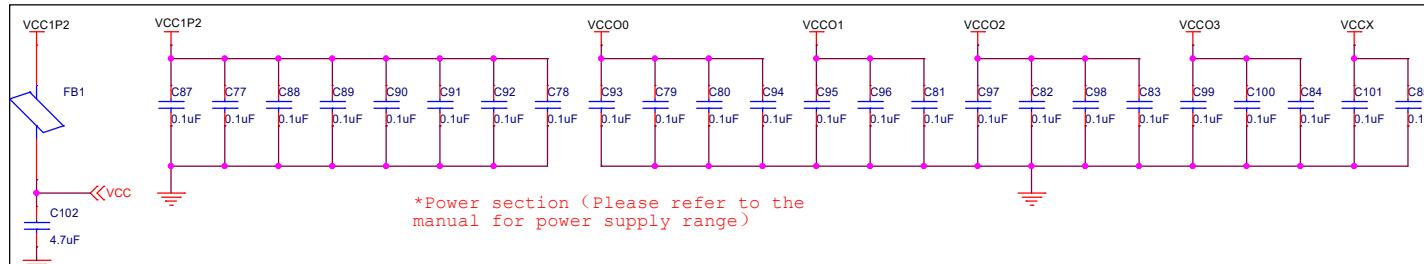
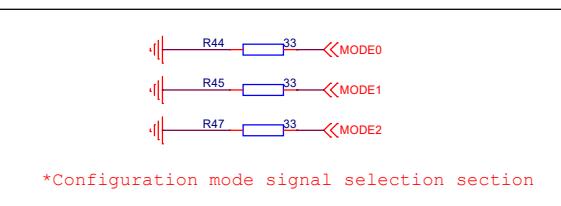
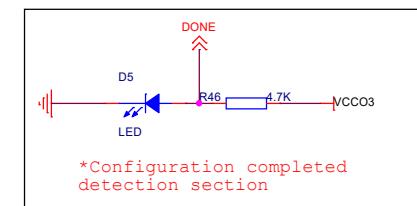




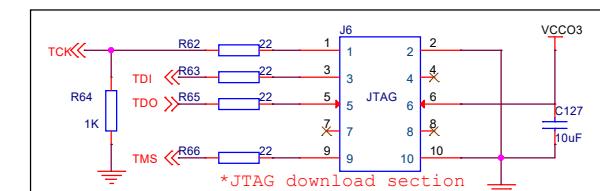
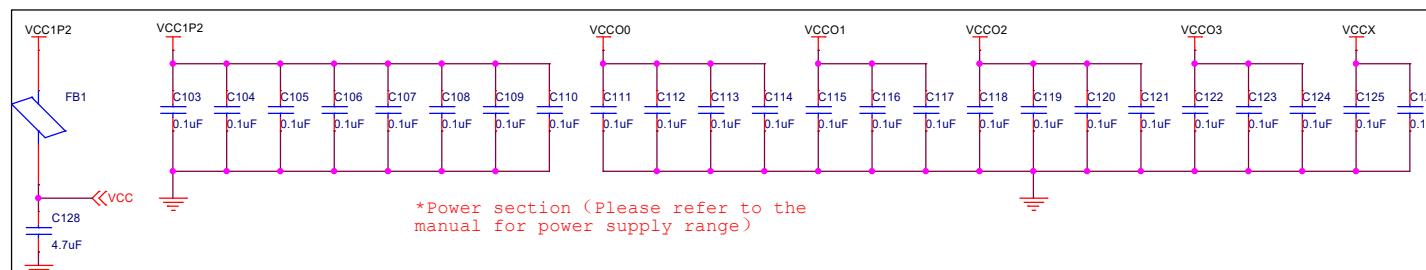
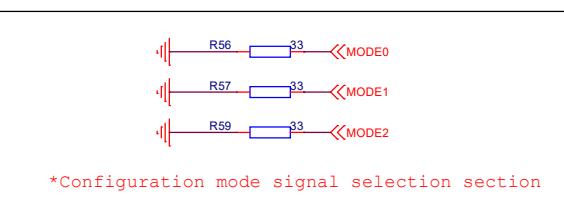
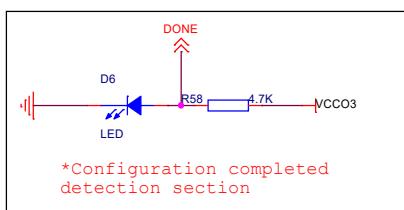
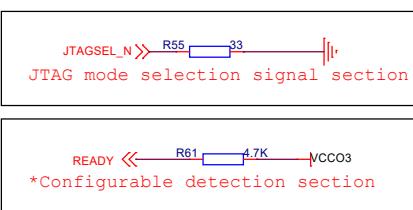
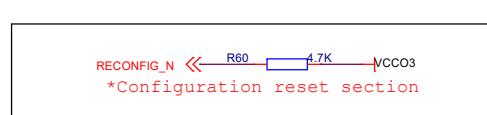
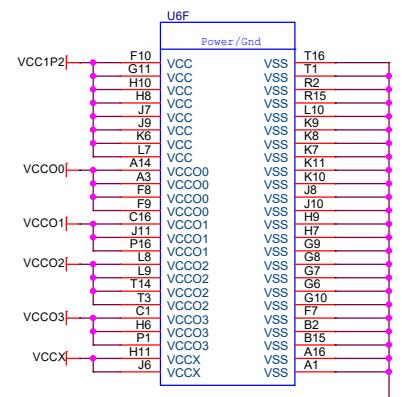
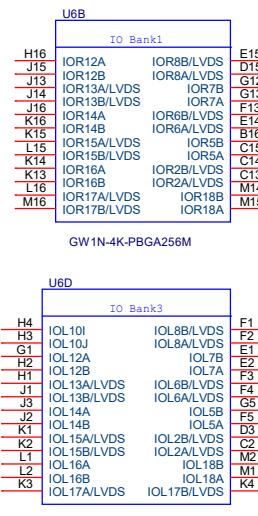
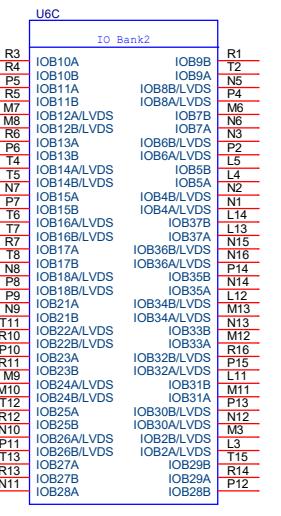
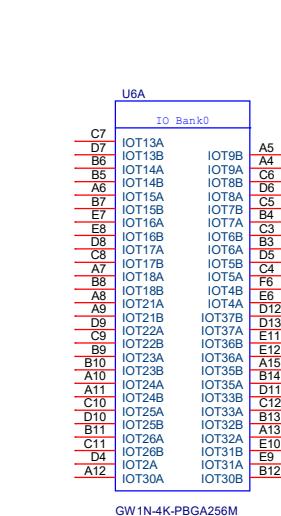
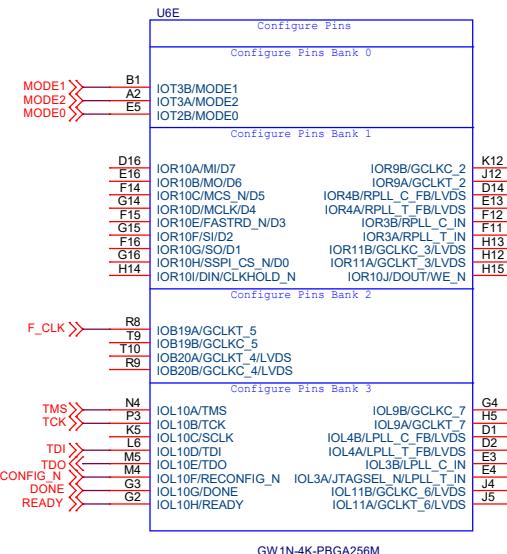


JTAGSEL_N → R43 → 33 → JTAG mode selection signal section

READY → R49 → 4.7K → VCC03 *Configurable detection section



D



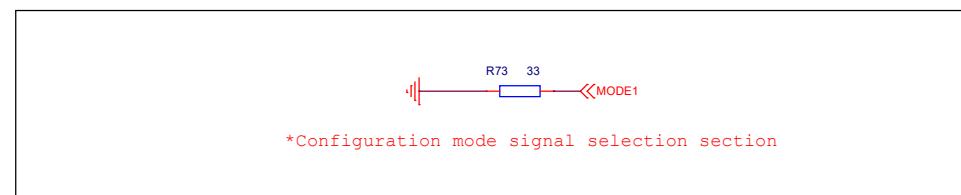
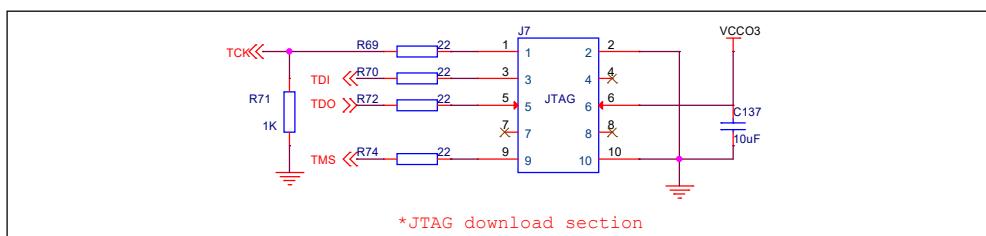
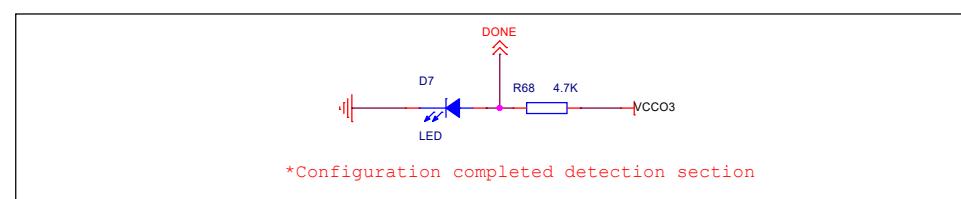
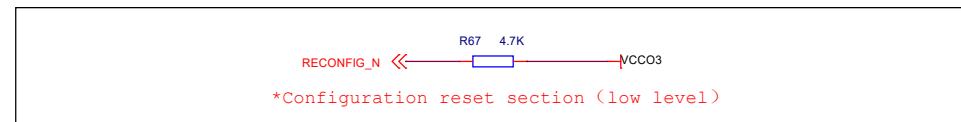
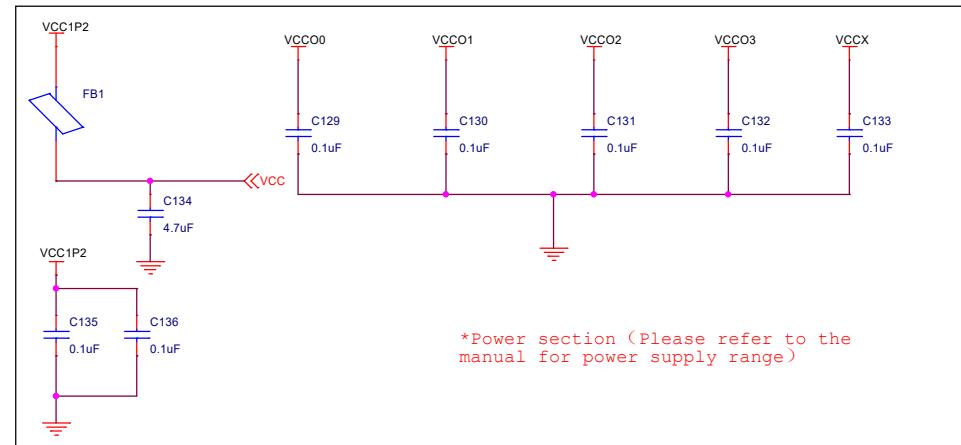
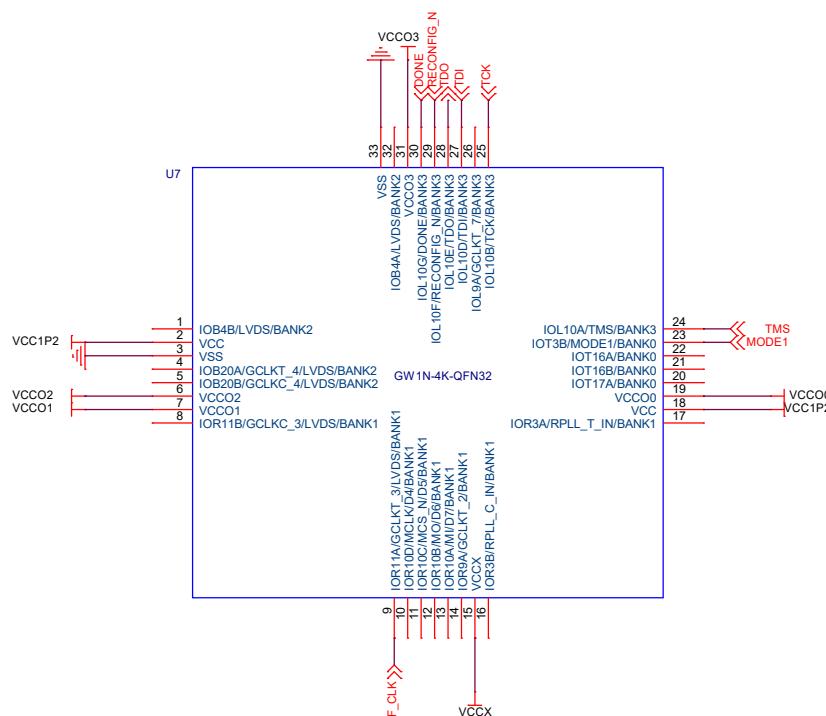
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C

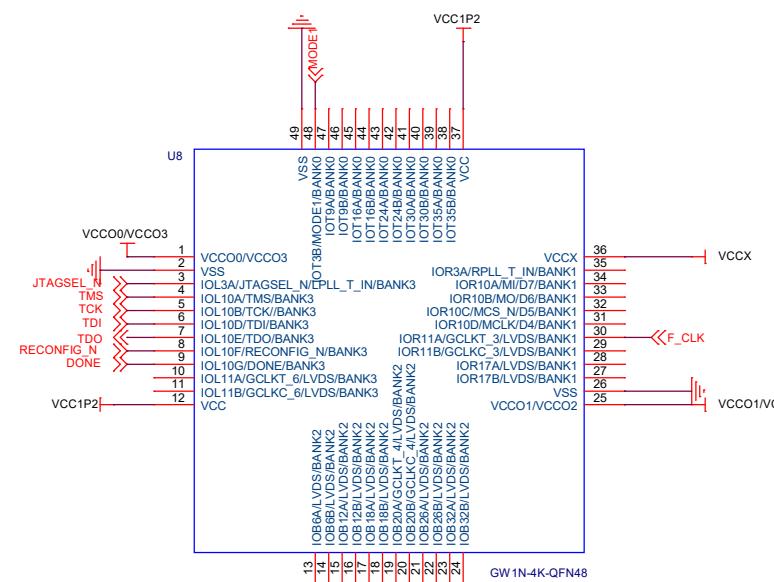
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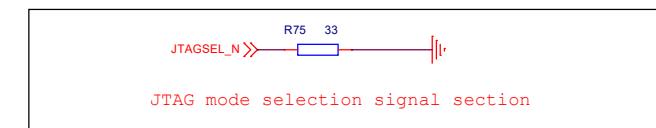
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D

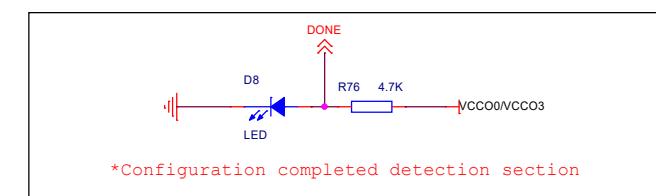


*Configuration: JTAG.AUTO BOOT.MSPI

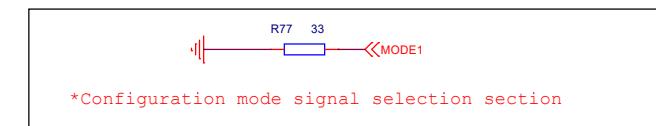
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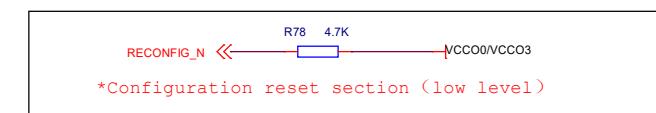
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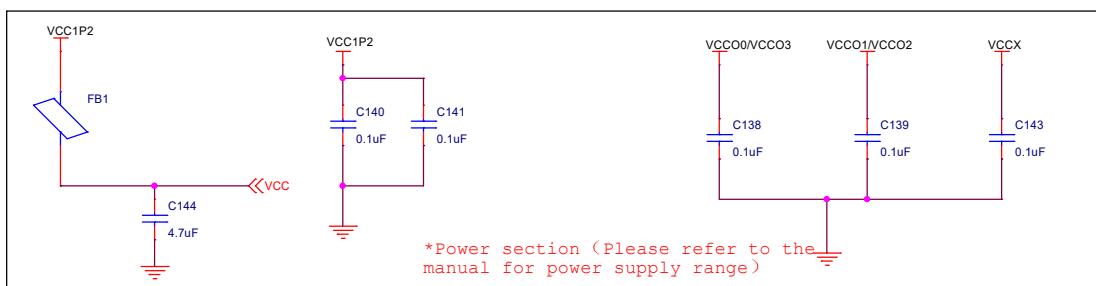
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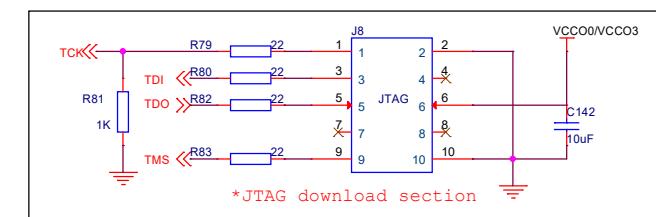
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B

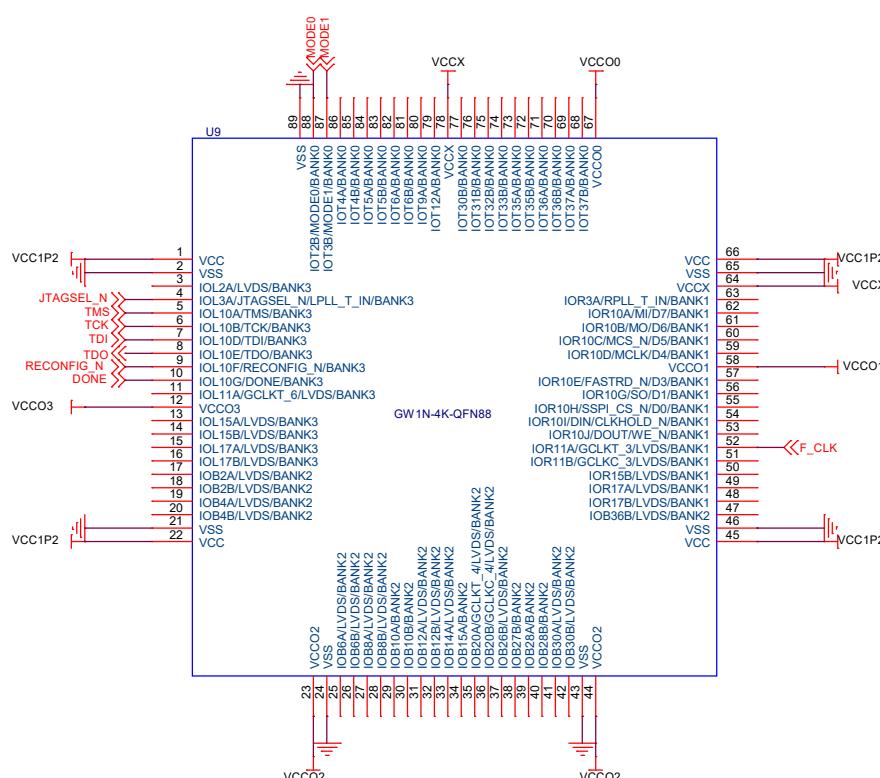


A



A

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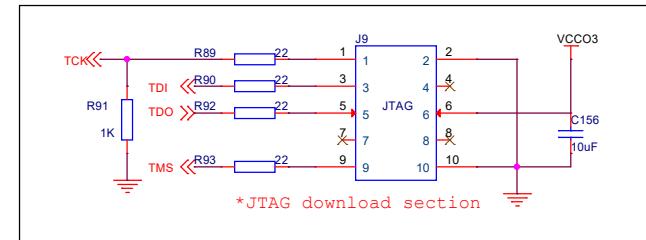
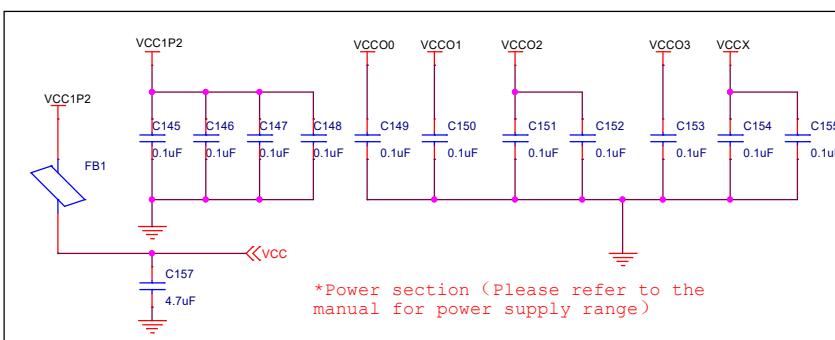


*Configuration reset section (low level)

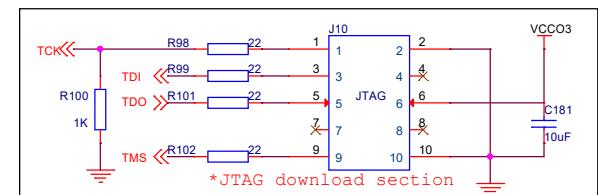
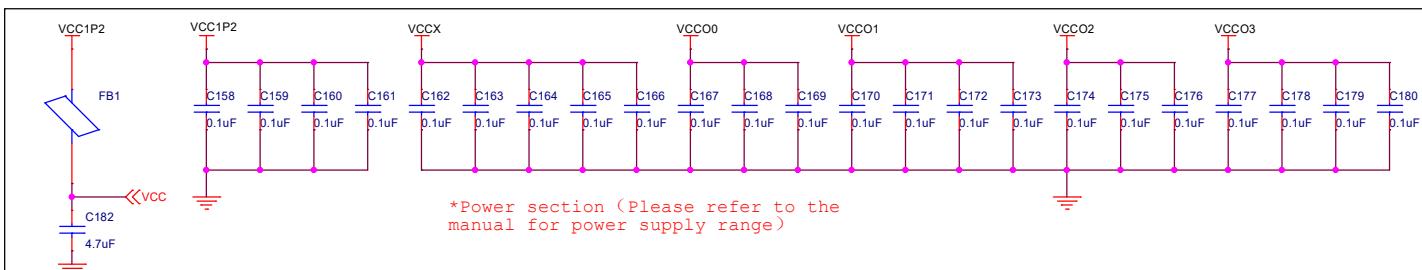
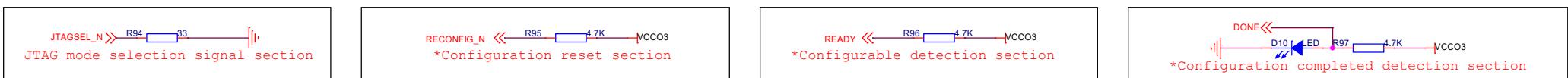
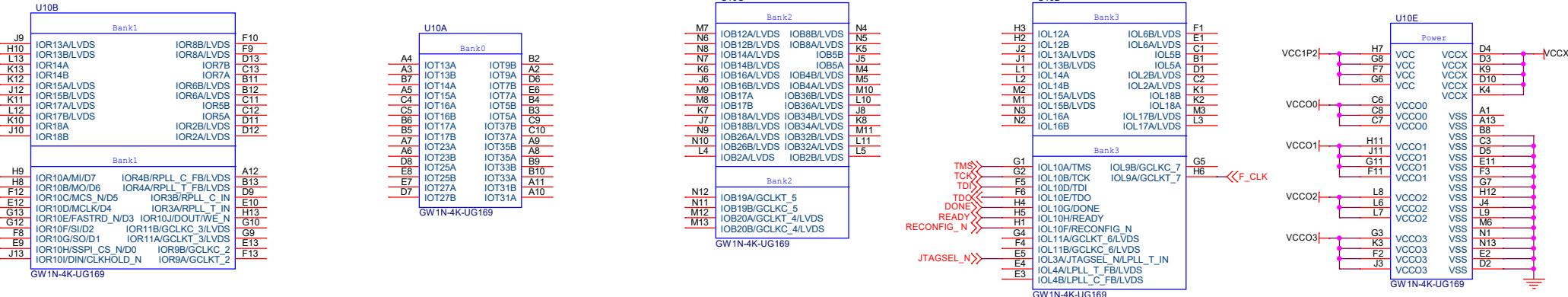
*Configuration completed detection section

*Configuration mode signal selection section

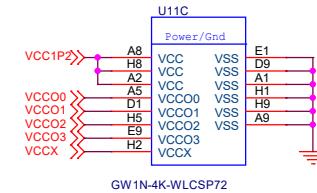
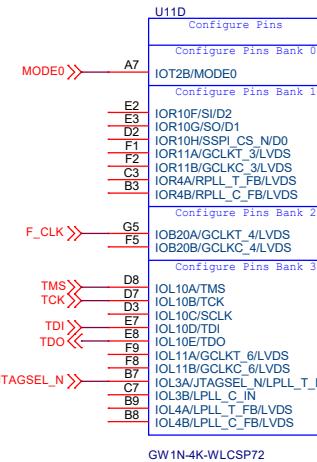
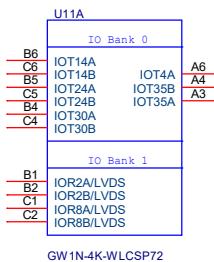
JTAG mode selection signal section



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D



GW1N-4K-WLCSP72

