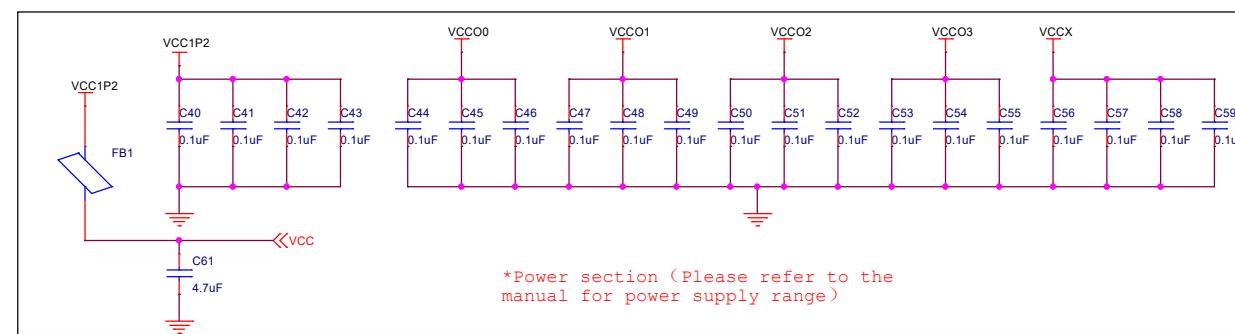
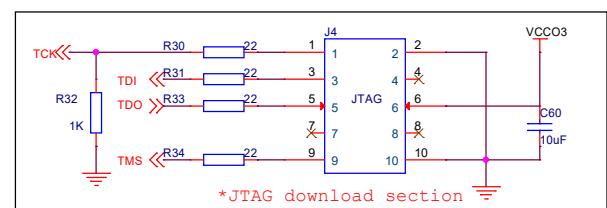
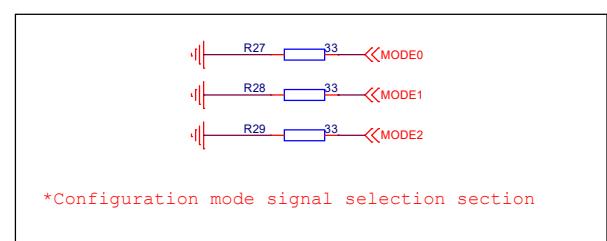
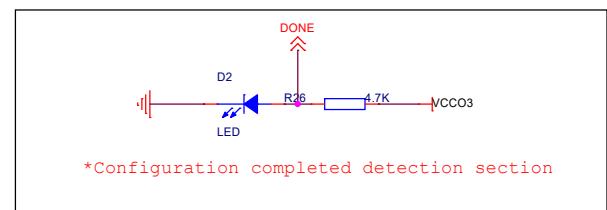
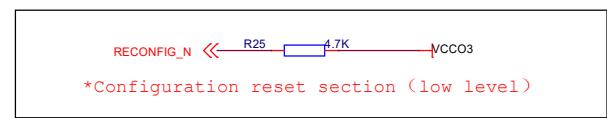
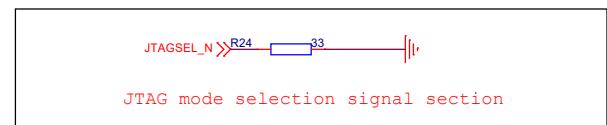
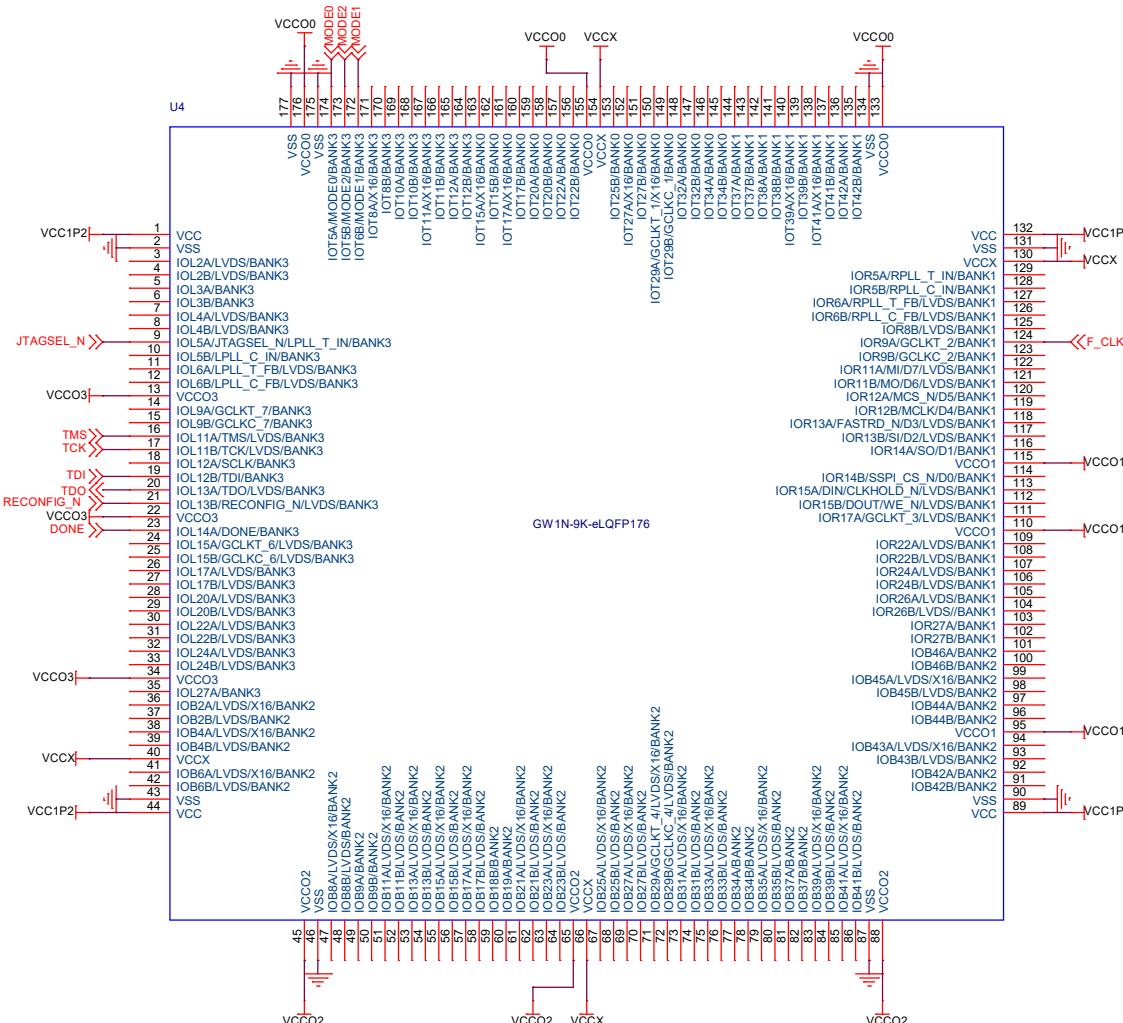
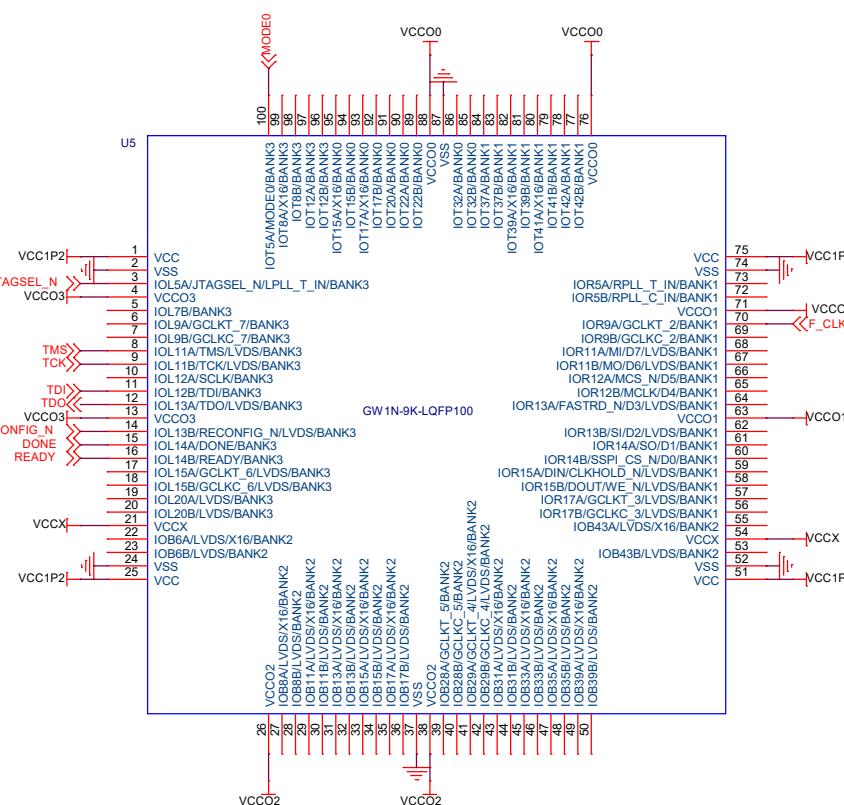


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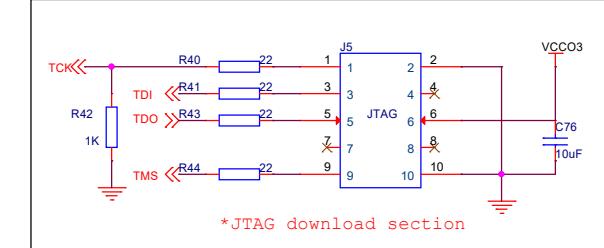
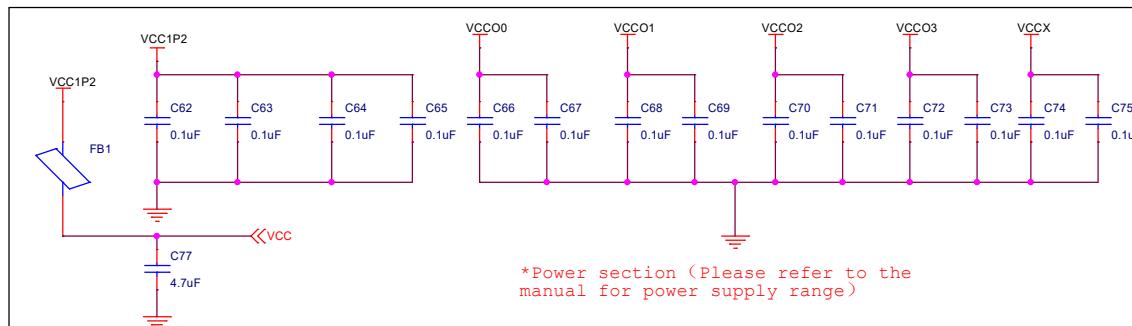
JTAG mode selection signal section

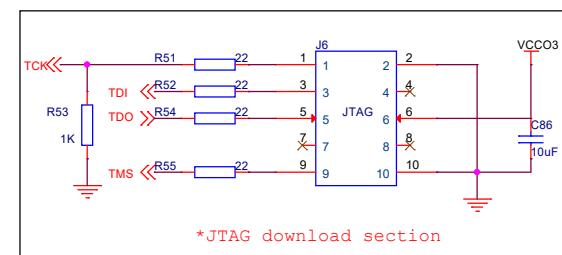
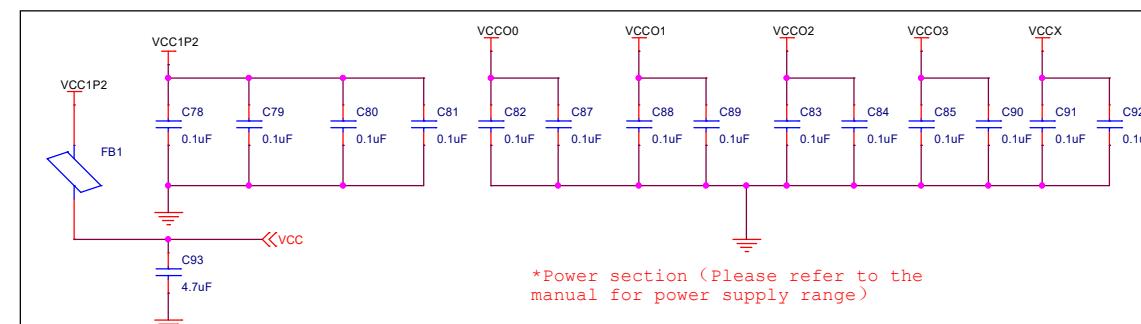
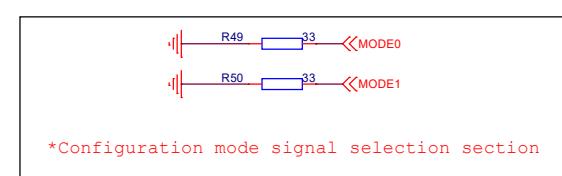
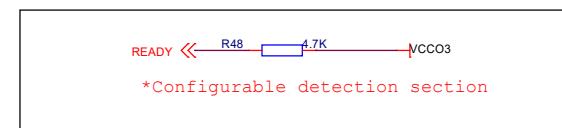
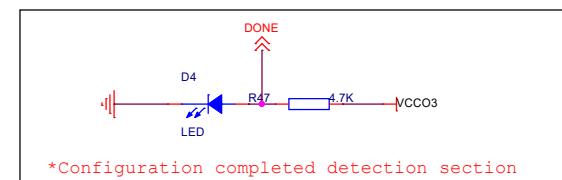
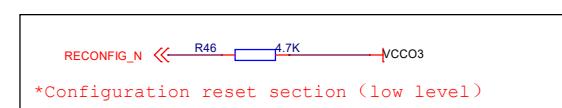
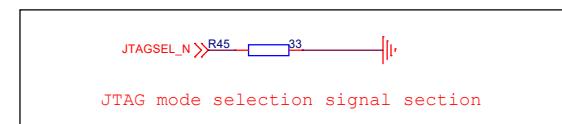
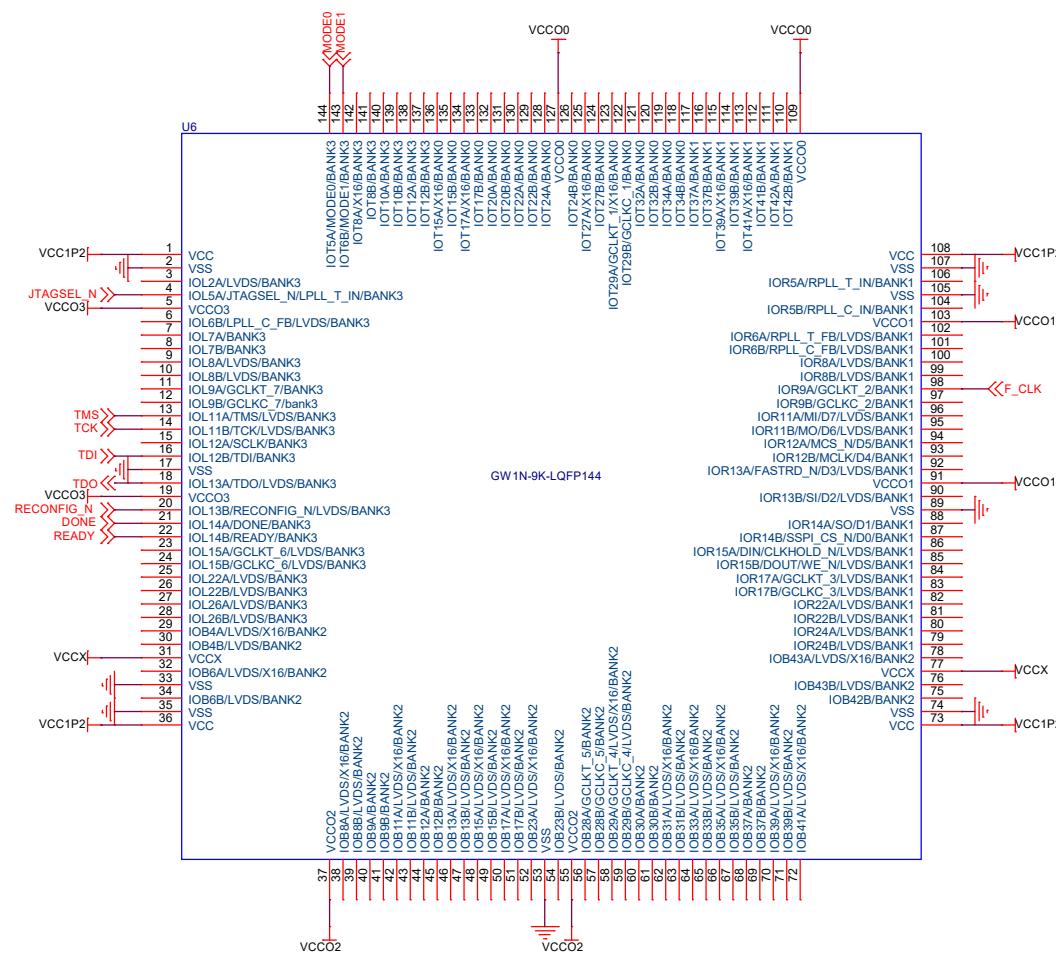
\*Configuration reset section (low level)

\*Configuration completed detection section

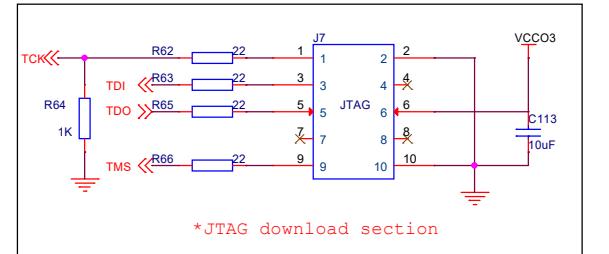
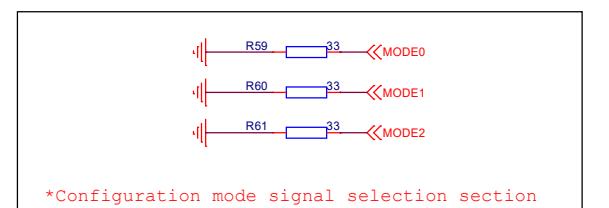
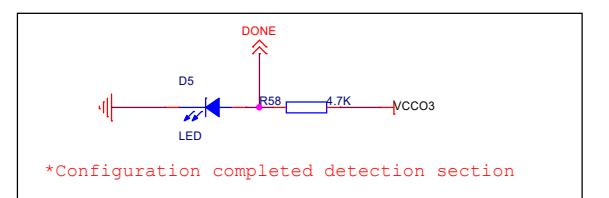
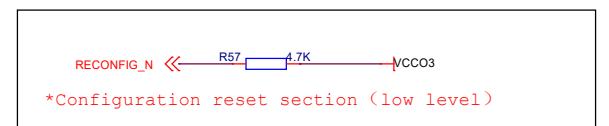
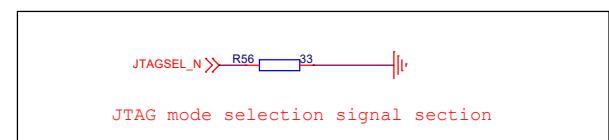
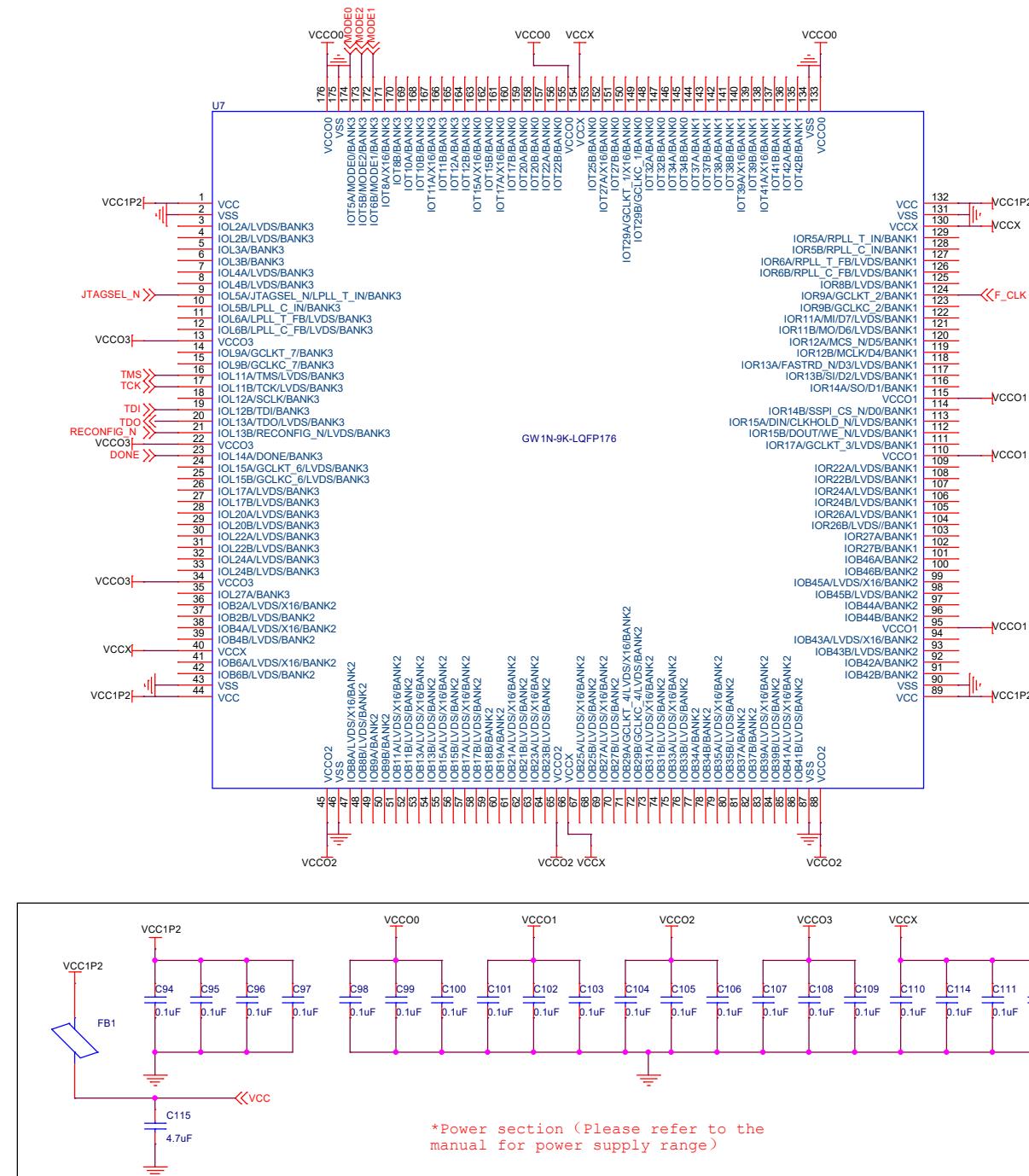
\*Configurable detection section

\*Configuration mode signal selection section



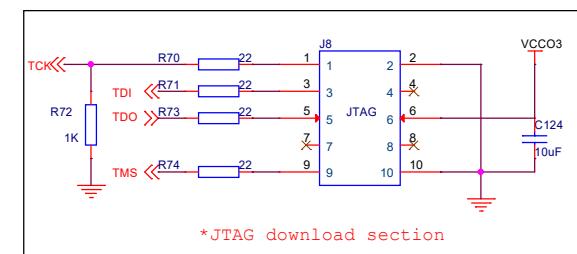
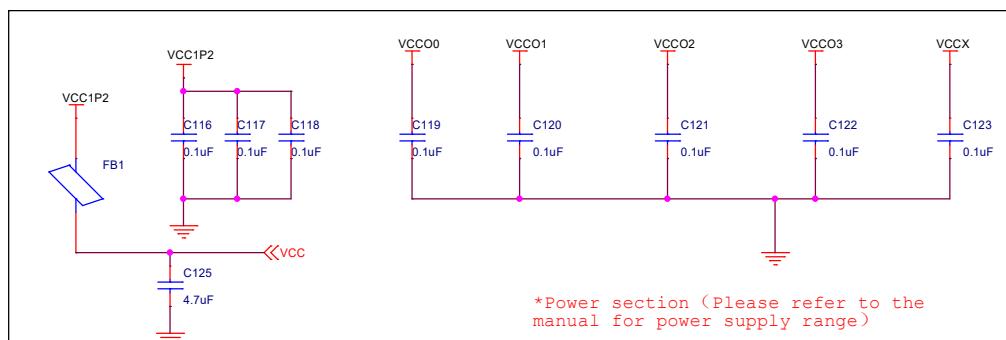
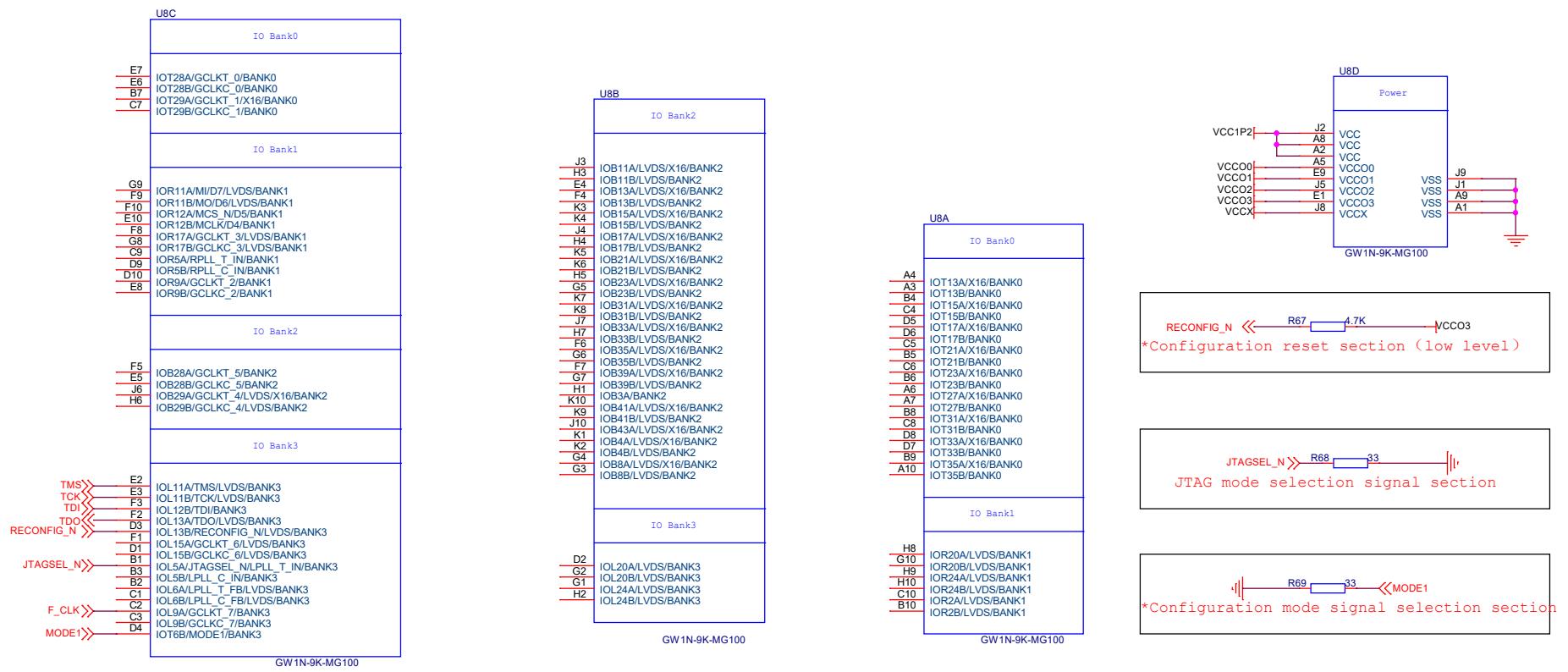


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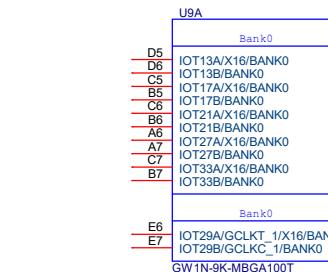
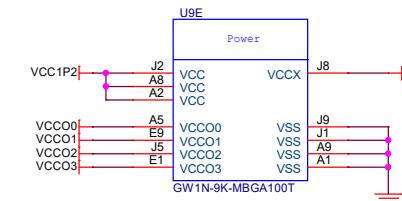
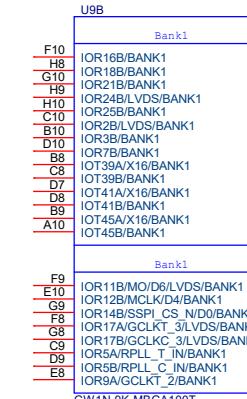
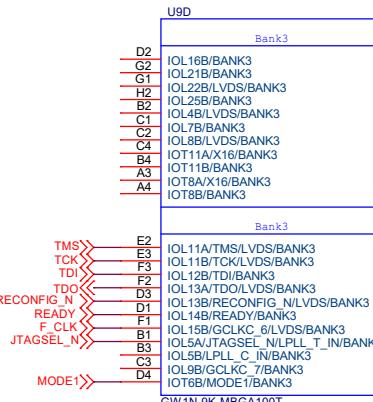
D

C

B

A

D

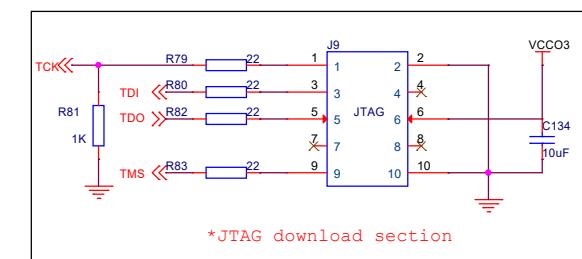
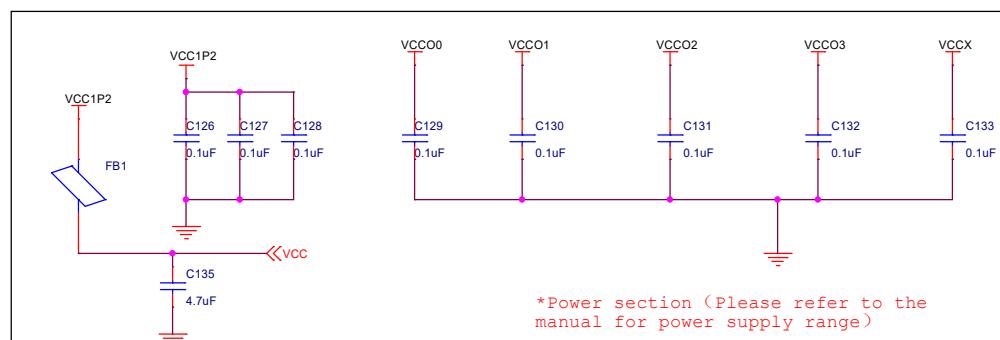


READY << R75 4.7K VCC03  
\*Configurable detection section

RECONFIG\_N << R76 4.7K VCC03  
\*Configuration reset section (low level)

JTAGSEL\_N >> R77 33 JTAG mode selection signal section

R78 23 << MODE1  
\*Configuration mode signal selection section



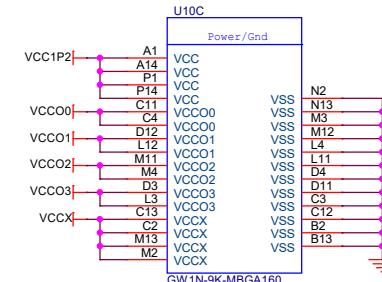
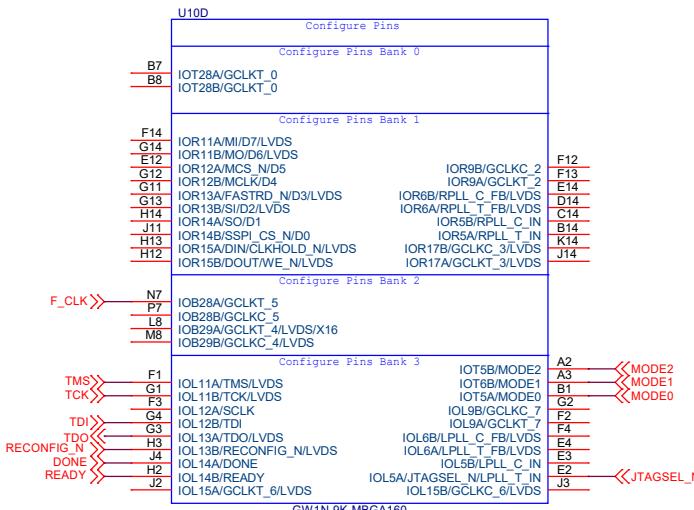
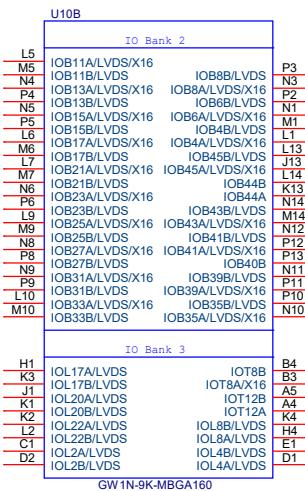
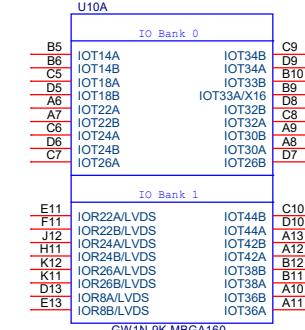
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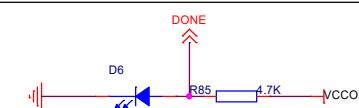
B

A



RECONFIG\_N  $\llcorner$  R84 4.7K VCCO3

\*Configuration reset section (low level)



\*Configuration completed detection section

JTAGSEL\_N  $\gg$  R86 33

JTAG mode selection signal section

READY  $\llcorner$  R90 4.7K VCCO3

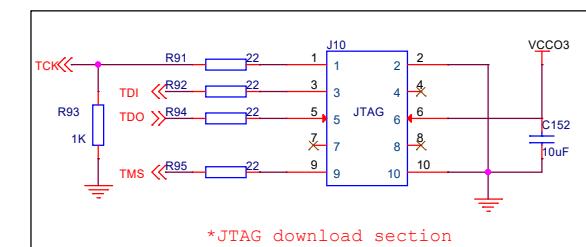
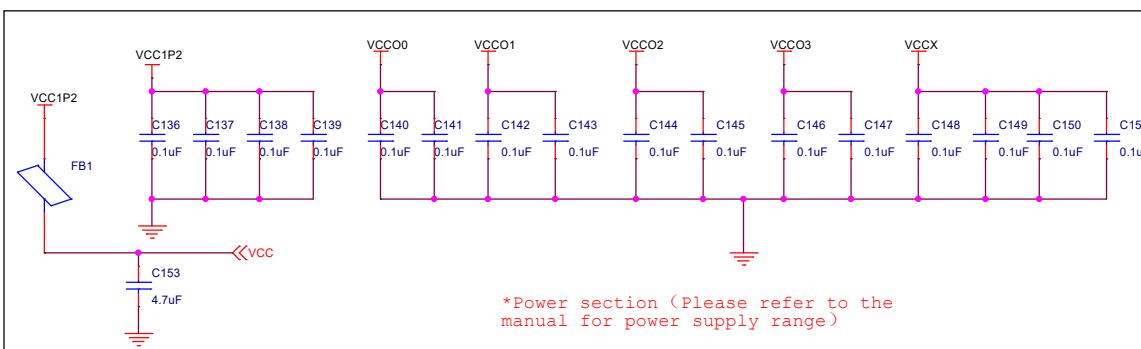
\*Configurable detection section

R87 33  $\llcorner$  MODE0

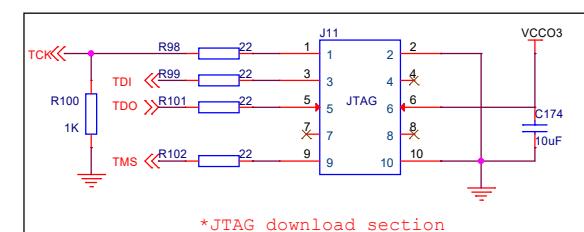
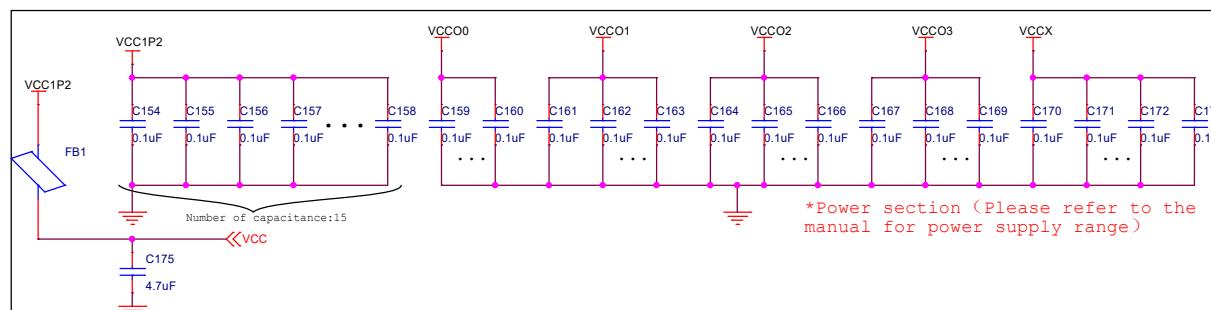
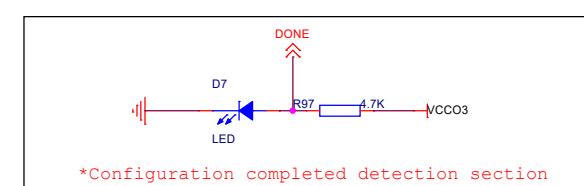
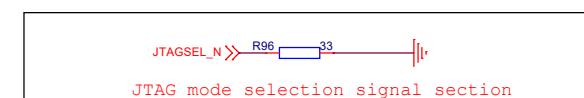
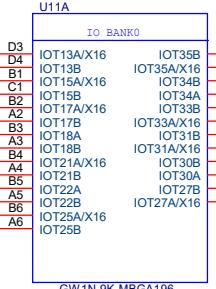
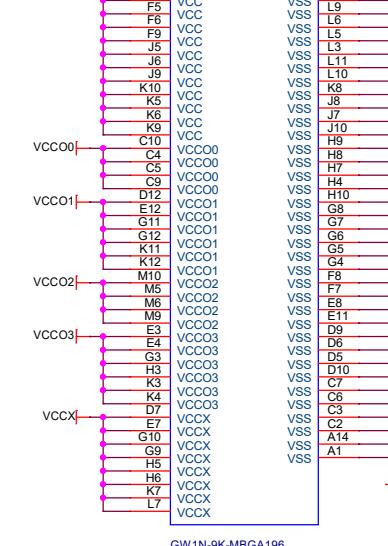
R88 33  $\llcorner$  MODE1

R89 33  $\llcorner$  MODE2

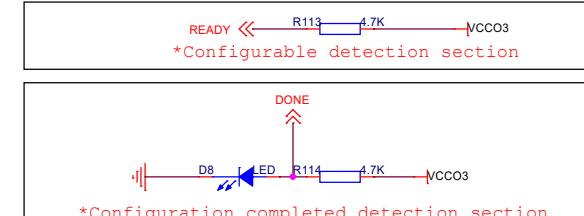
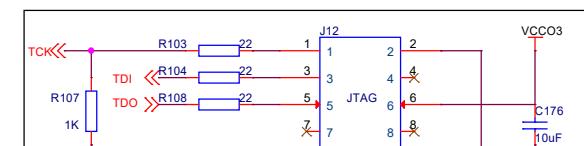
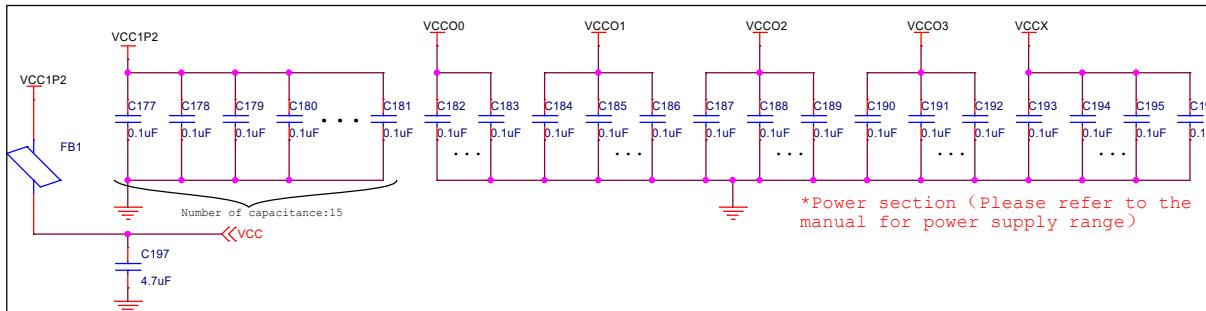
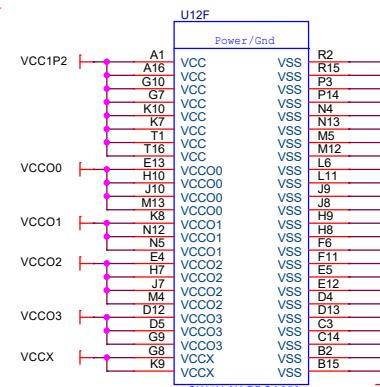
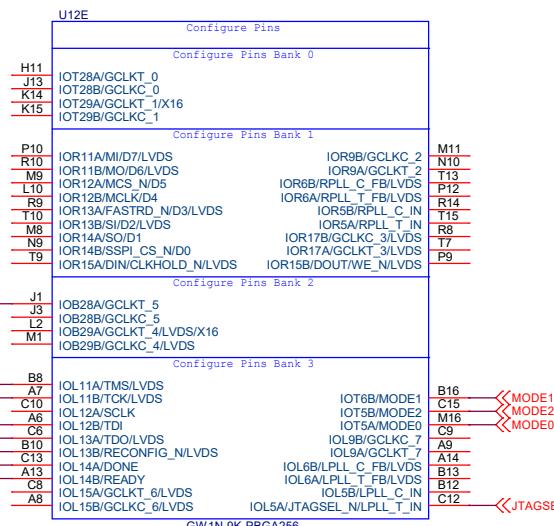
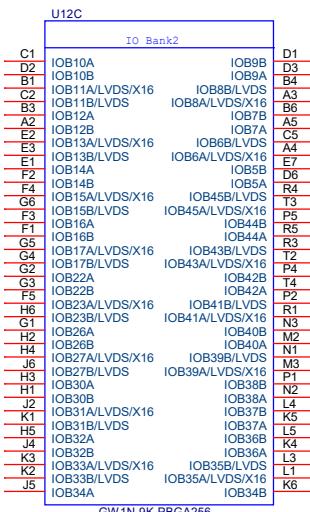
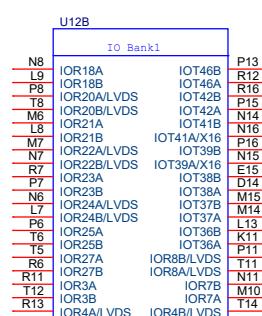
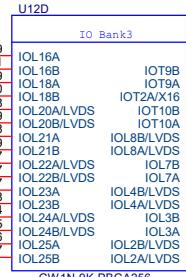
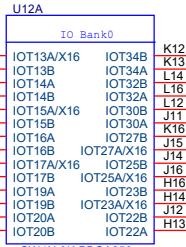
\*Configuration mode signal selection section

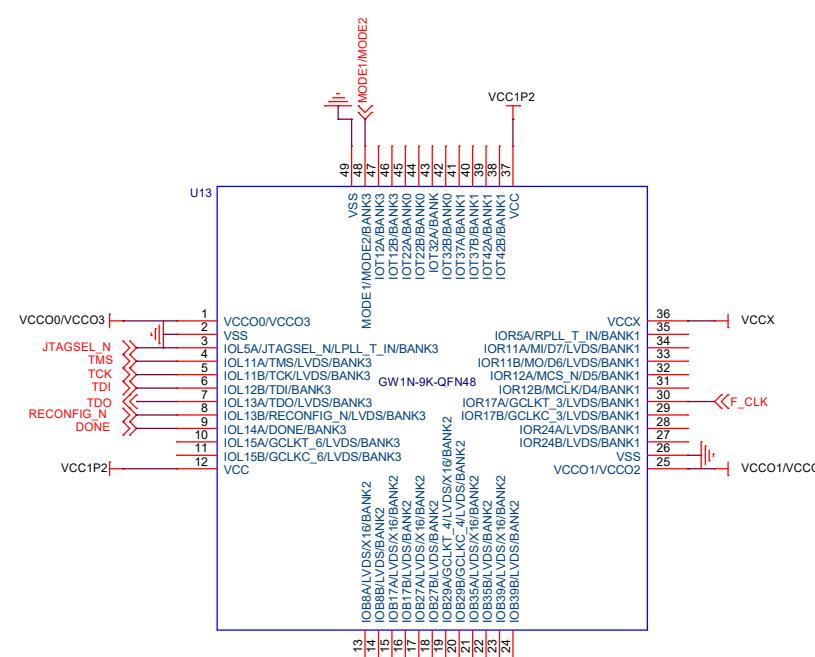


*GW1N-LV9-MG196*



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RECONFIG\_N  $\ll$  R115 4.7K  $\rightarrow$  VCC00/VCC03

\*Configuration reset section (low level)

JTAGSEL\_N  $\gg$  R116 33  $\rightarrow$  GND

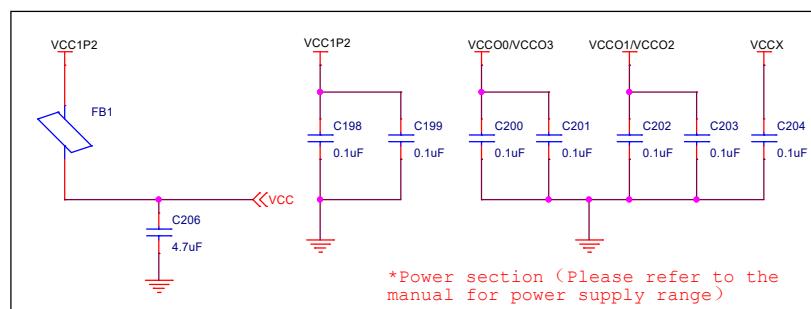
JTAG mode selection signal section

D9  $\rightarrow$  LED  $\rightarrow$  R117 4.7K  $\rightarrow$  VCC00/VCC03

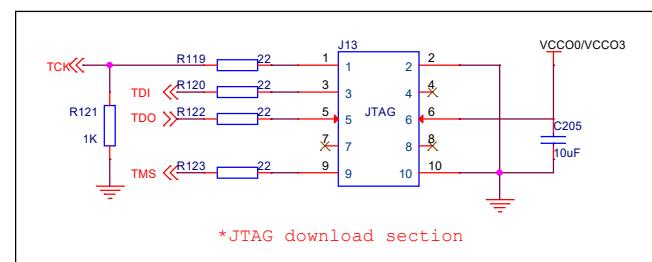
\*Configuration completed detection section

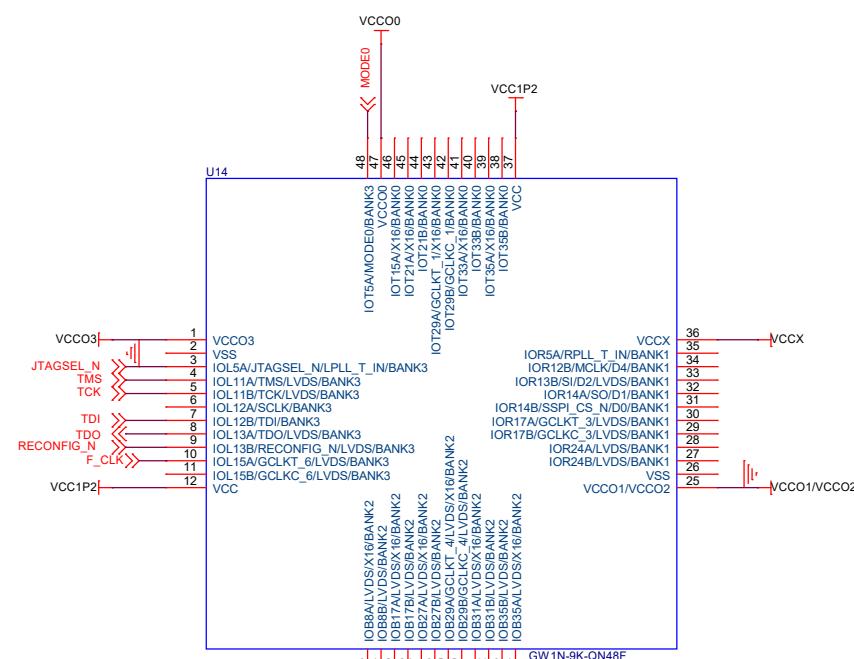
R118 33  $\rightarrow$  MODE1/MODE2

\*Configuration mode signal selection section



\*Power section (Please refer to the manual for power supply range)



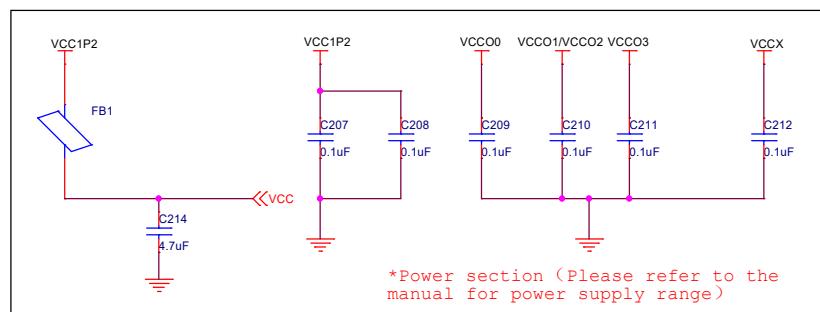


RECONFIG\_N ⇐ R124 4.7K ⇌ VCCO3  
\*Configuration reset section (low level)

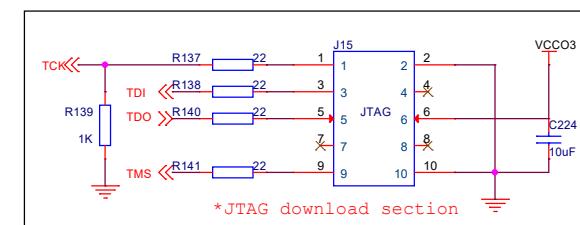
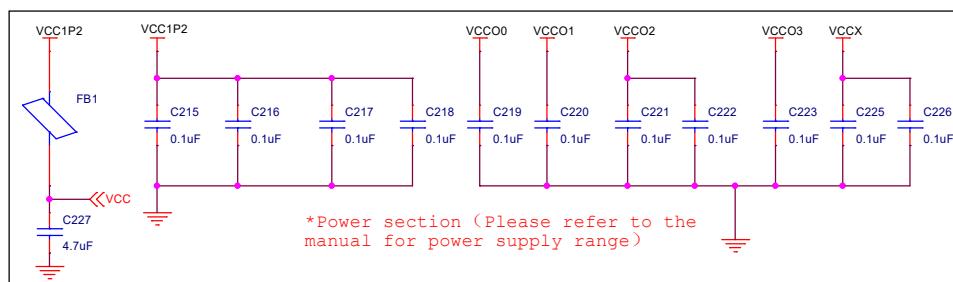
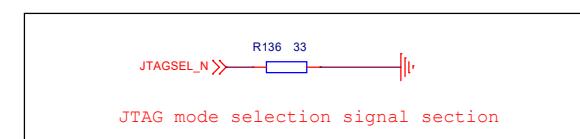
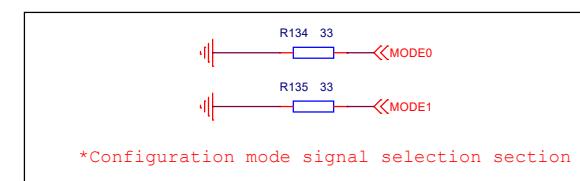
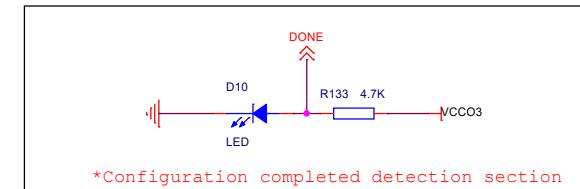
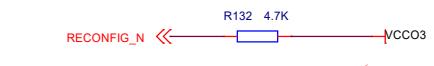
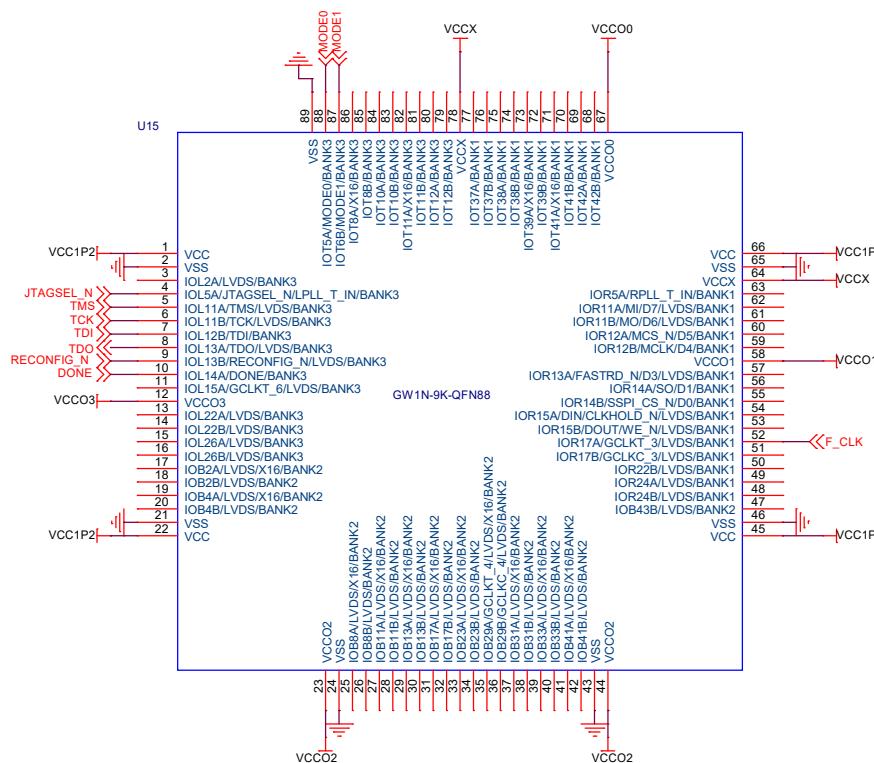
JTAGSEL\_N ⇢ R125 33 ⇌  
JTAG mode selection signal section

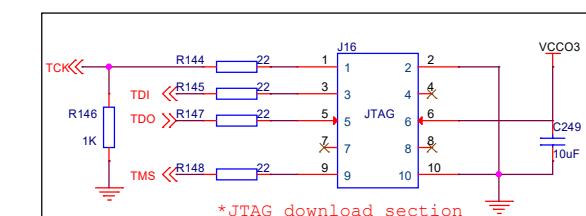
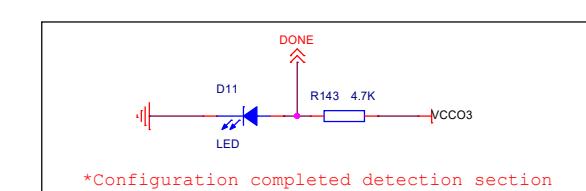
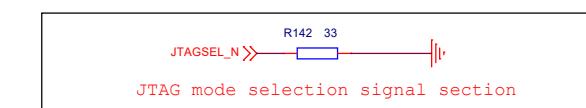
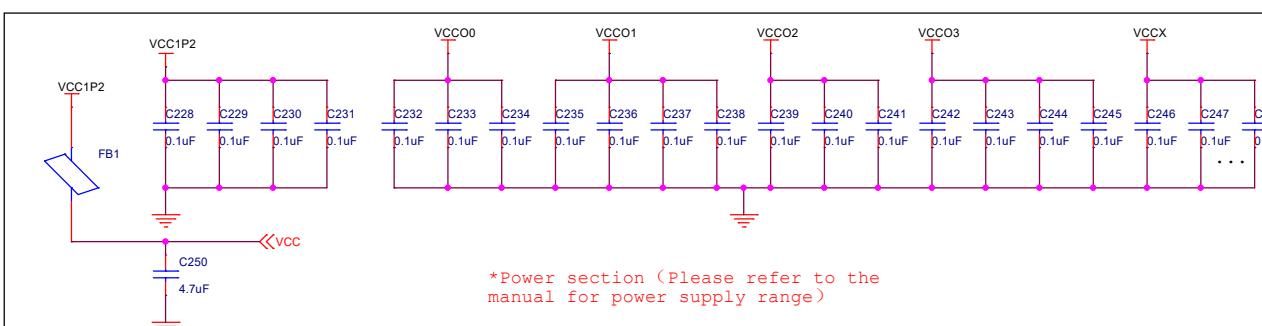
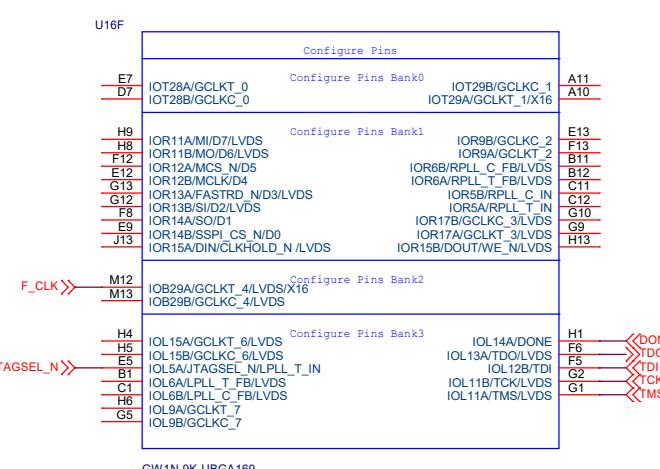
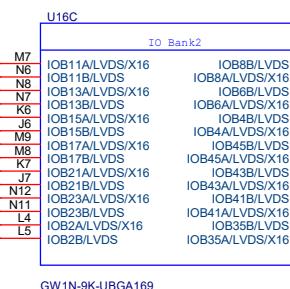
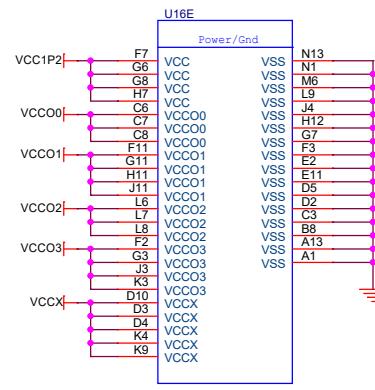
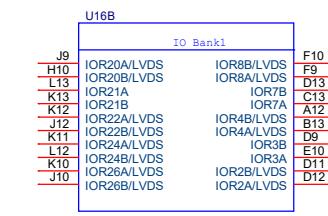
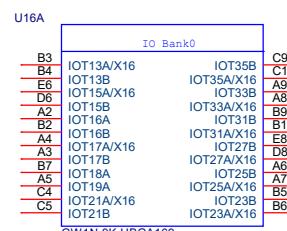
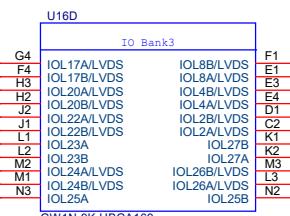
R126 33 ⇌ MODE0  
\*Configuration mode signal selection section

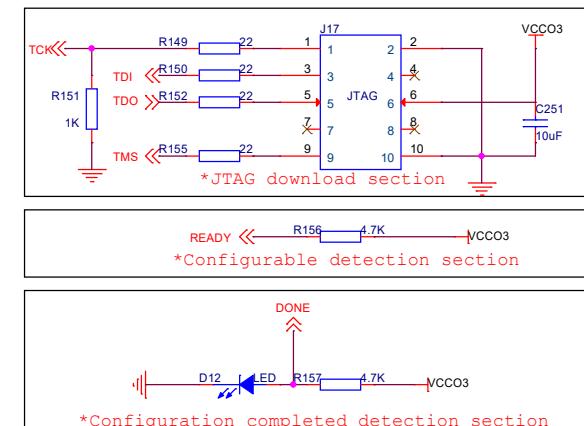
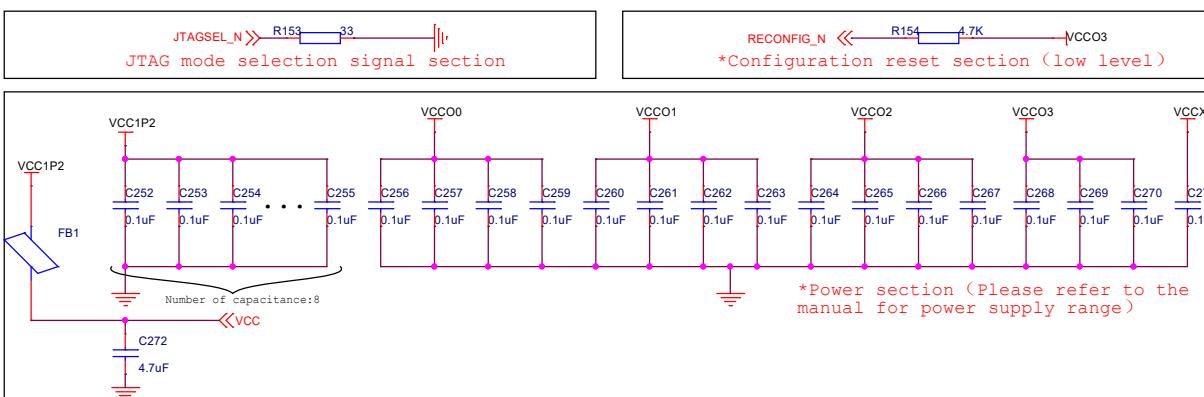
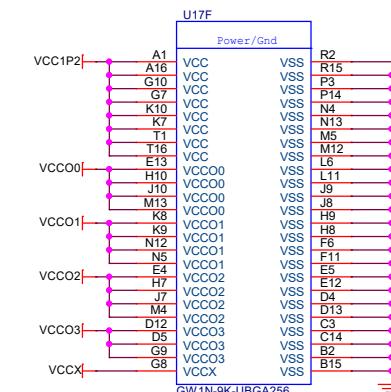
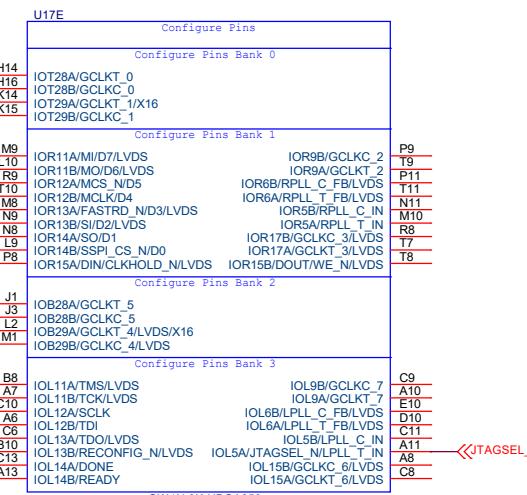
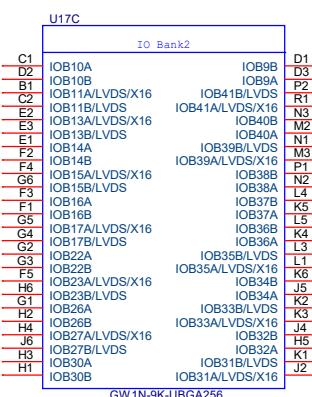
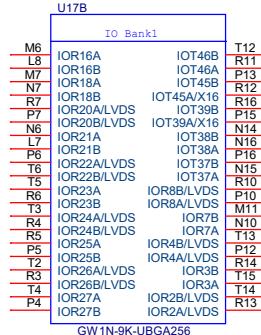
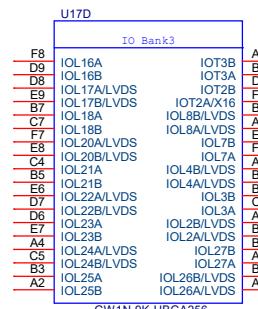
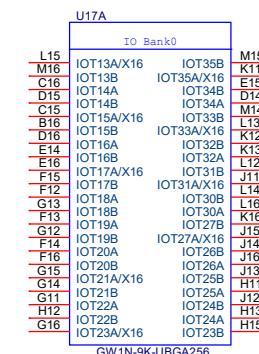
\*JTAG download section

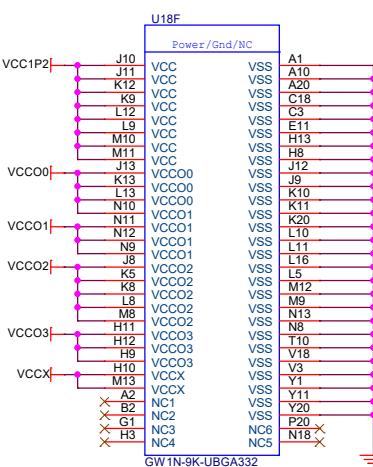
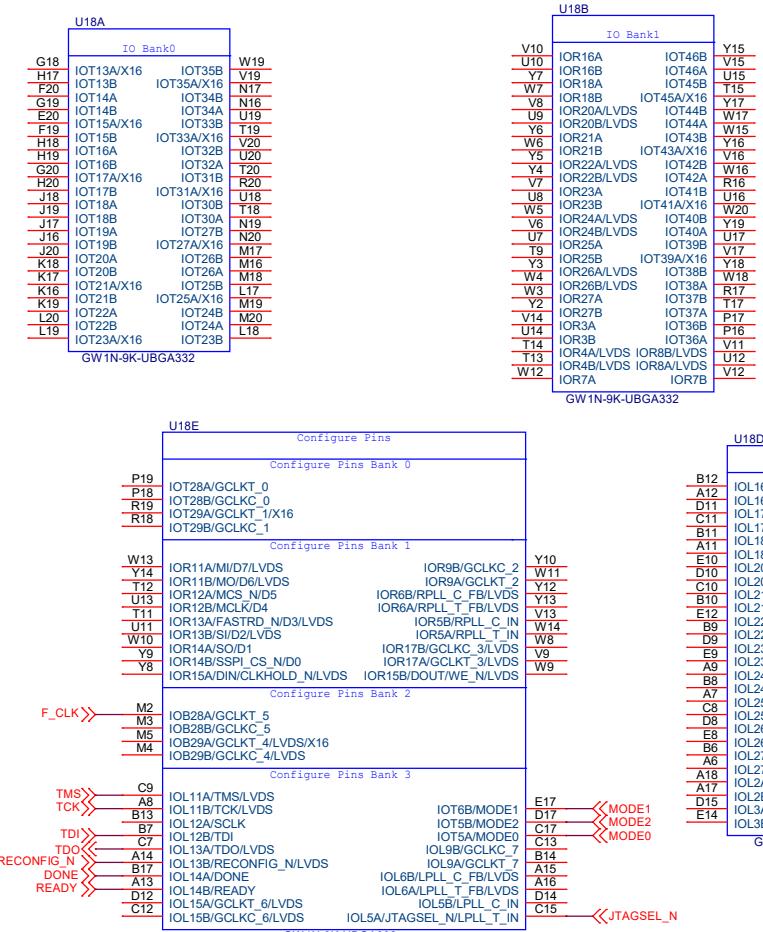
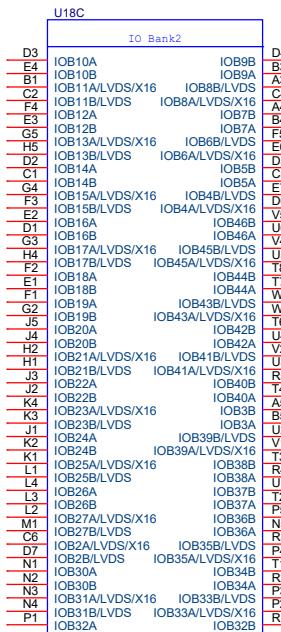


Title	
GOWIN Minimum System Diagram	
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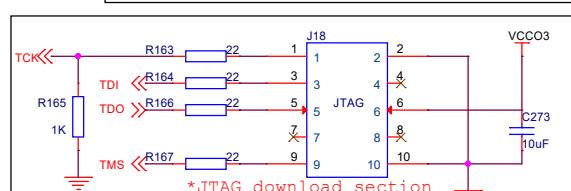




JTAGSEL\_N >> R158 -> 33      JTAG mode selection signal section

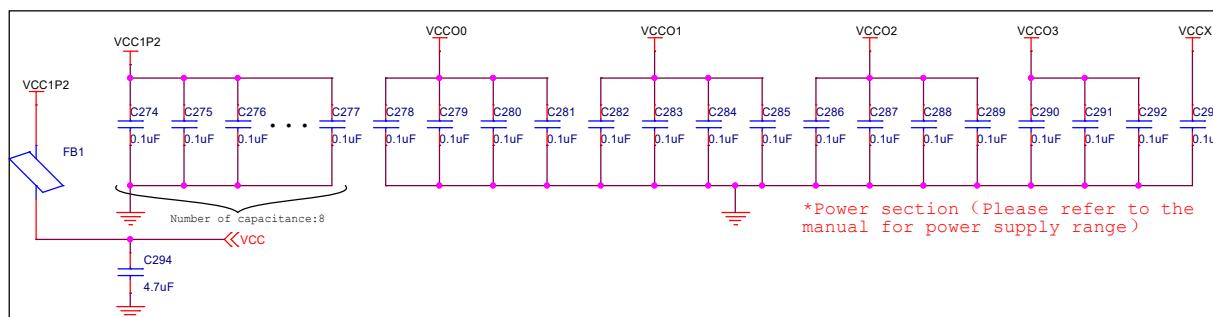
RECONFIG\_N << R159 -> 4.7K -> VCC03      \*Configuration reset section (low level)

R160 -> 33 -> MODE0  
R161 -> 33 -> MODE1  
R162 -> 33 -> MODE2      \*Configuration mode signal selection



READY << R168 -> 4.7K -> VCC03      \*Configurable detection section

D13 ED R169 -> 4.7K -> VCC03  
DONE >> D13  
\*Configuration completed detection section



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