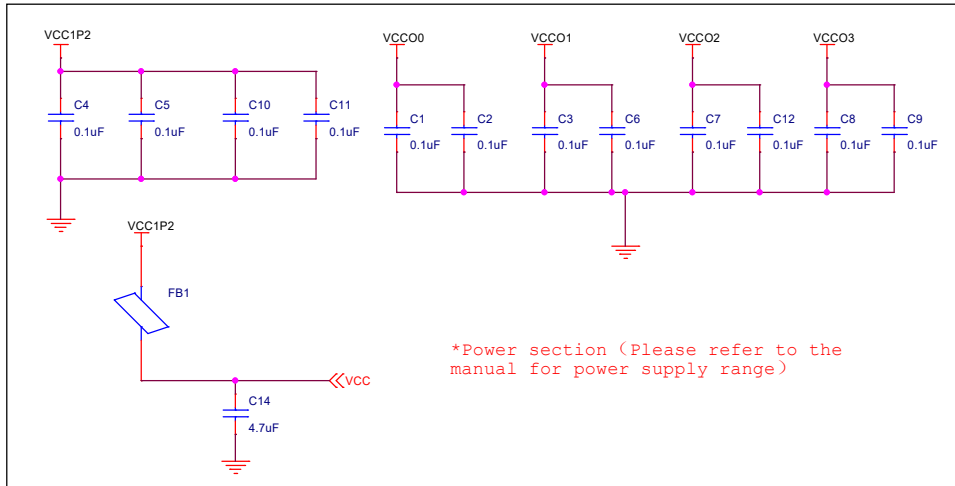


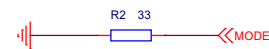
\*The unconnected port of the chip is used as a single-ended signal port or multiplexing function  
\*Configuration: JTAG.AUTO BOOT.SSPI



\*Power section (Please refer to the manual for power supply range)



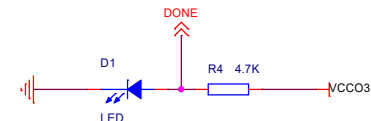
JTAG mode selection signal section



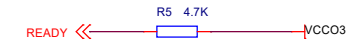
\*Configuration mode signal selection section



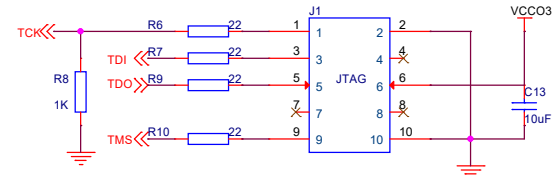
\*Configuration reset section (low level)



\*Configuration completed detection section

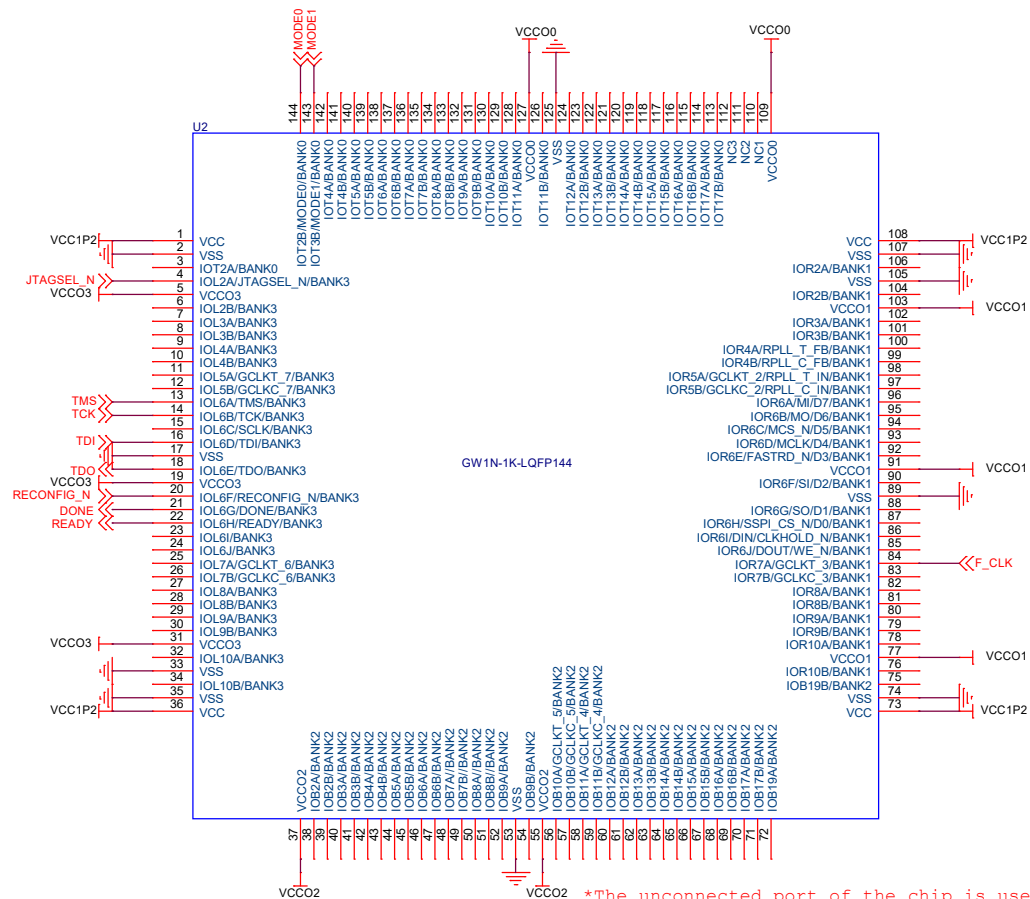


\*Configurable detection section

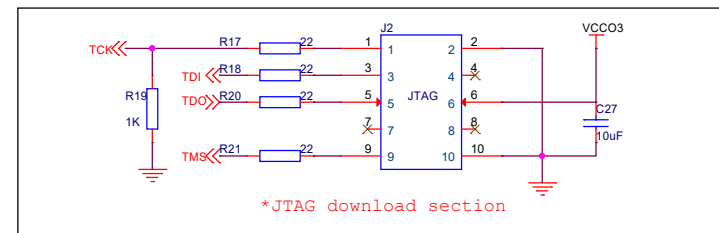
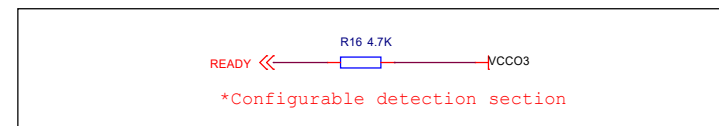
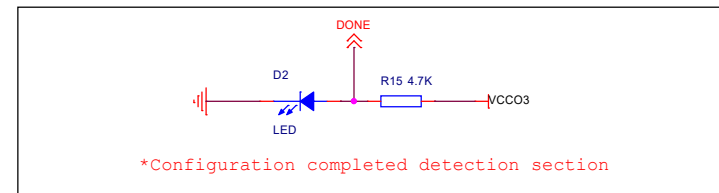
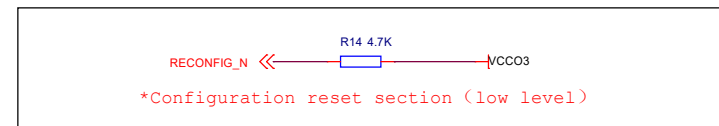
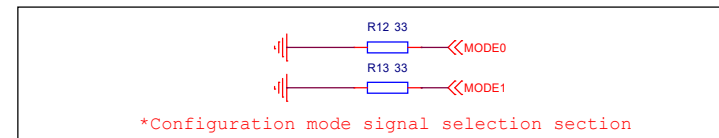
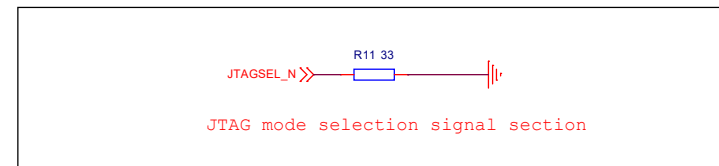
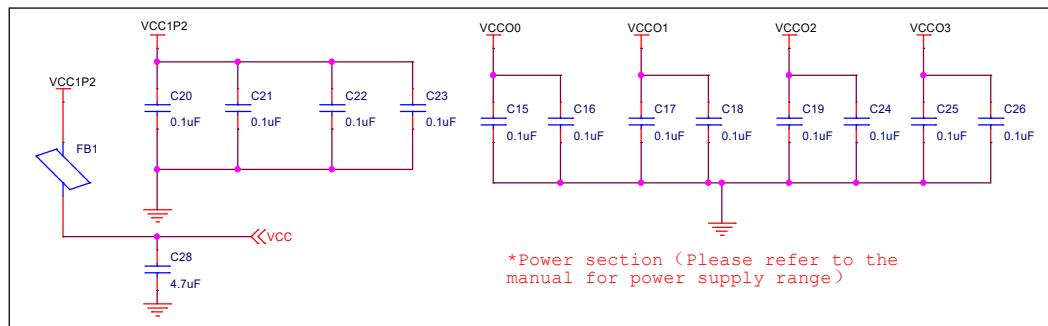


\*JTAG download section

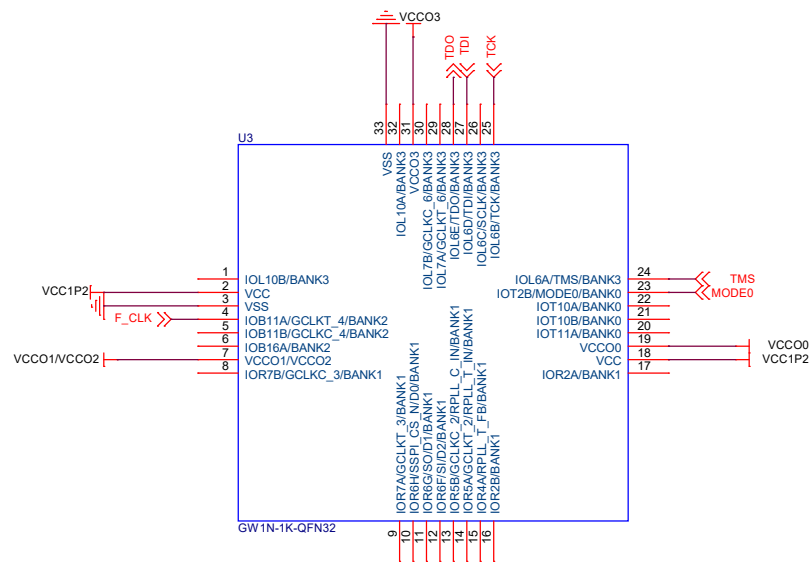
5  
***GW1N-LV1-LQFP144***



\*The unconnected port of the chip is used as a single-ended signal port or multiplexing function  
\*Configuration: JTAG.AUTO BOOT.SSPI.MSPI

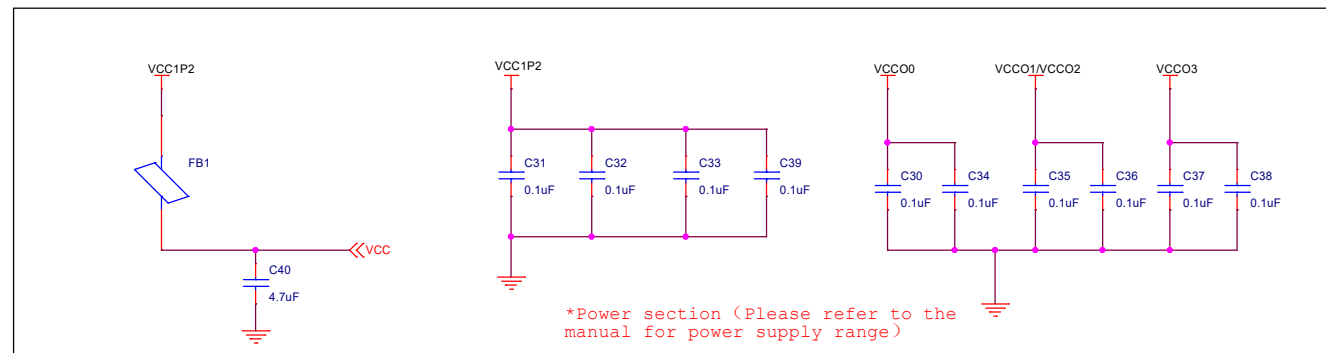
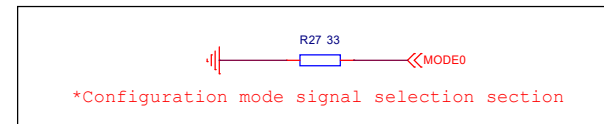
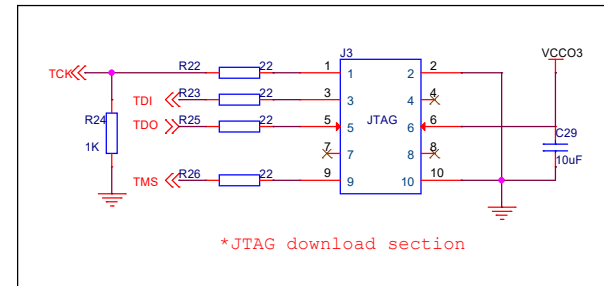


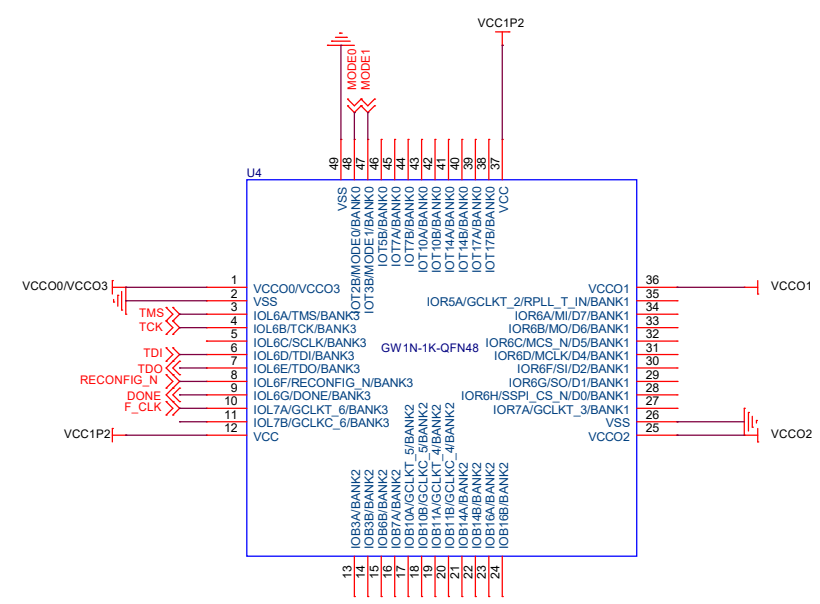
Title				
GOWIN Minimum System Diagram				
Size A3	Document Number GW1N-L-V1-L-QFP144			Rev 1.1
Date:	Tuesday, June 14, 2022	Sheet	2 of 5	



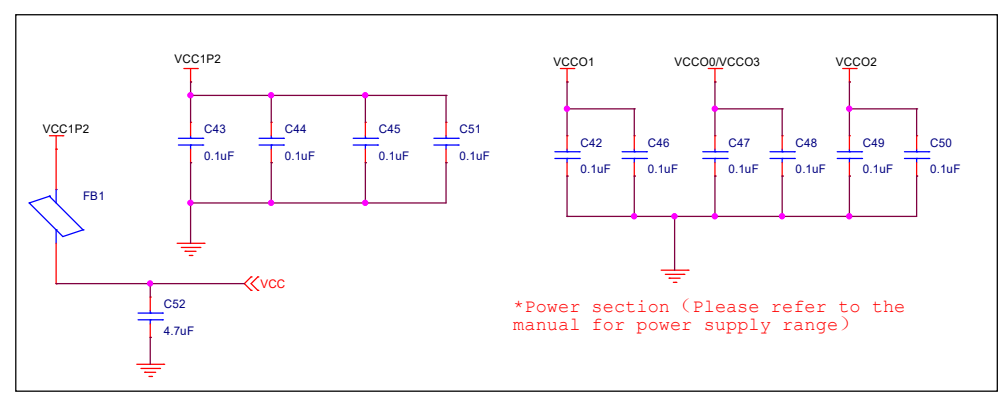
\*The unconnected port of the chip is used as a single-ended signal port or multiplexing function

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*Configuration: JTAG.AUTO BOOT.SSPI
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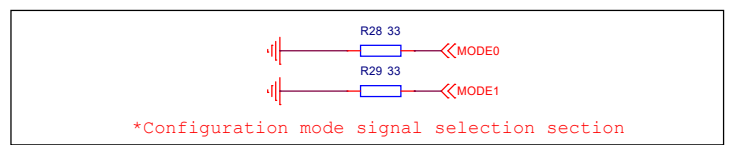




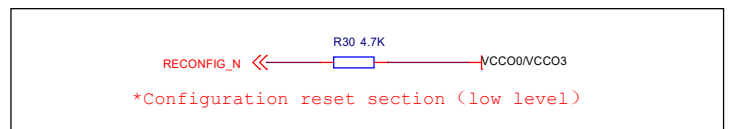
\*The unconnected port of the chip is used as a single-ended signal port or multiplexing function  
\*Configuration: JTAG.AUTO BOOT.SSPI.MSPI



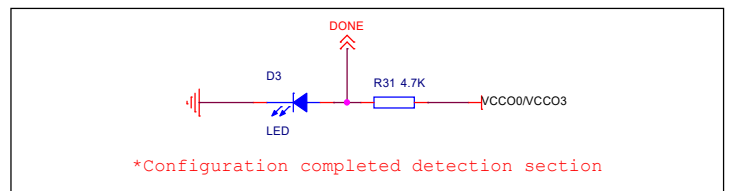
\*Power section (Please refer to the manual for power supply range)



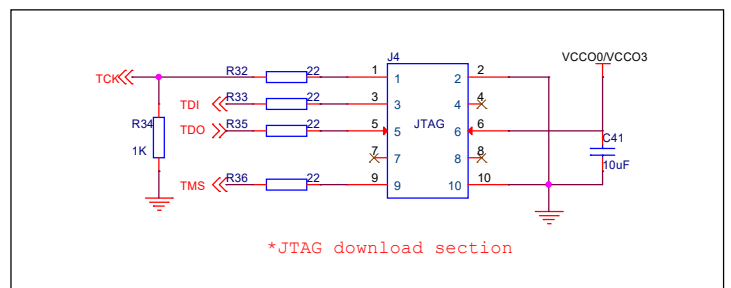
\*Configuration mode signal selection section



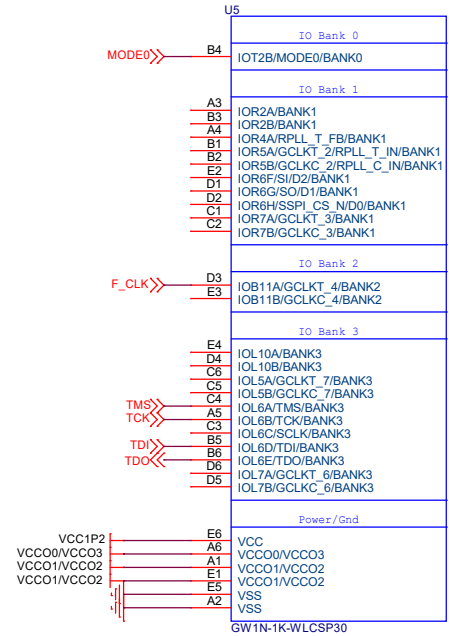
\*Configuration reset section (low level)



\*Configuration completed detection section



\*JTAG download section



\*The unconnected port of the chip is used as a single-ended signal port or multiplexing function  
\*Configuration: JTAG.AUTO BOOT.SSPI

