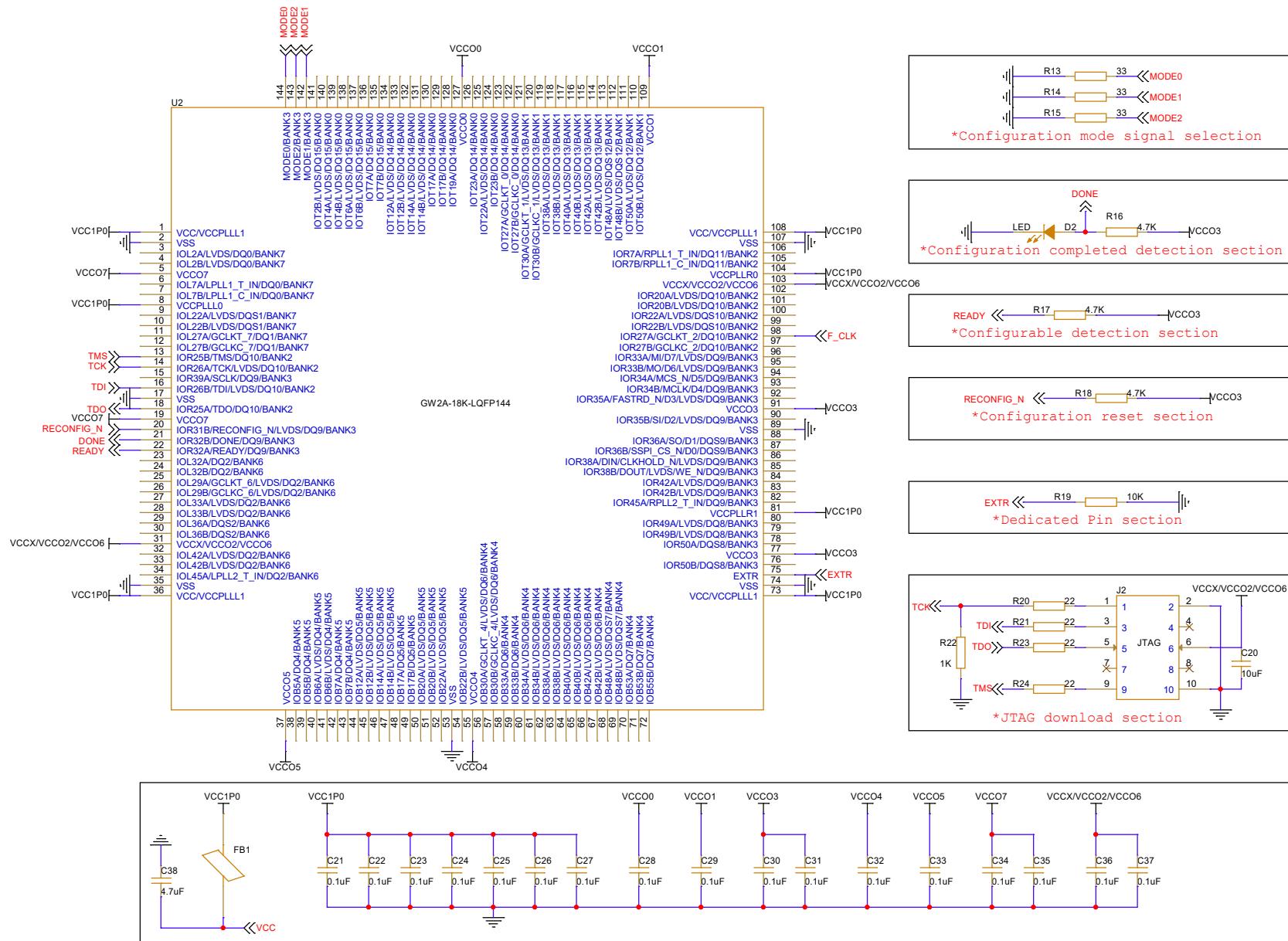
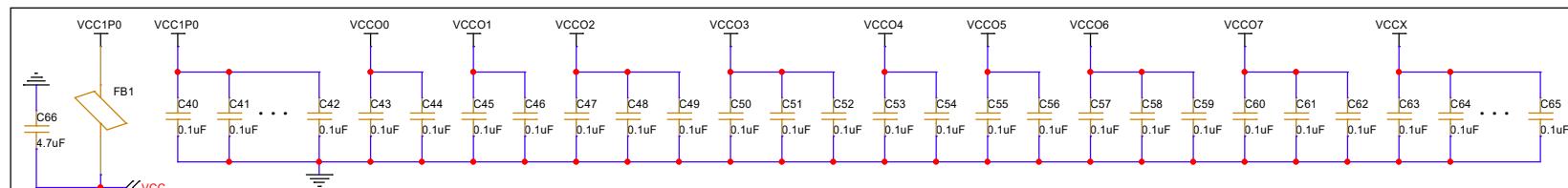
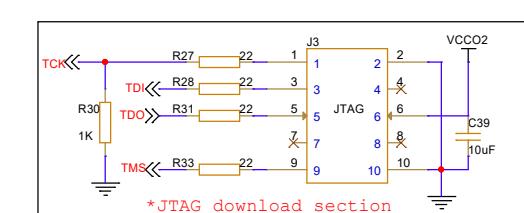
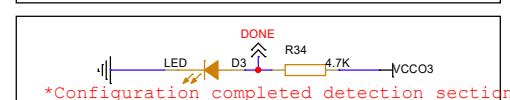
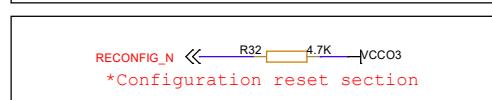
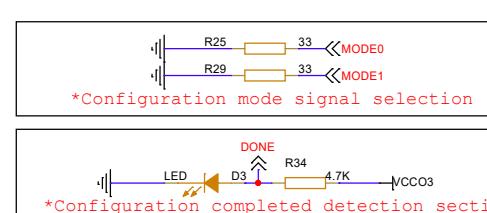
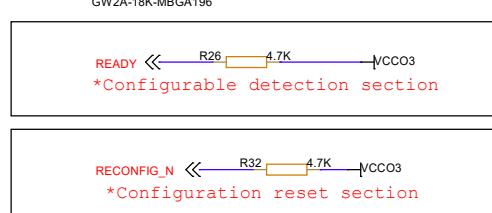
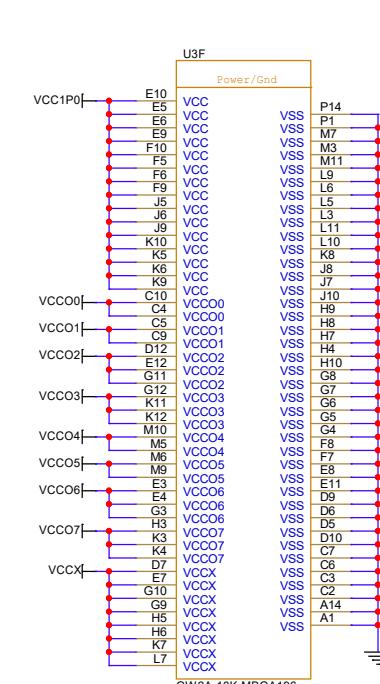
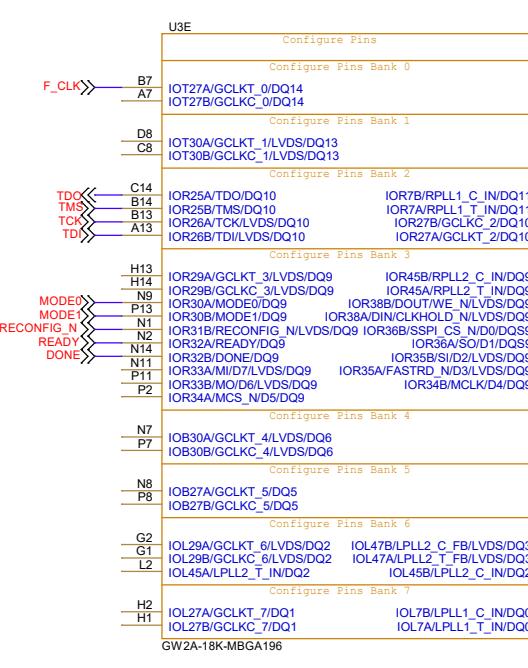
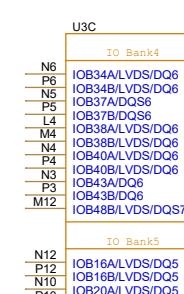
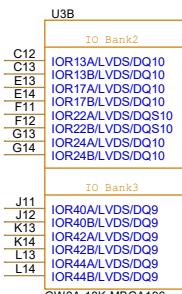
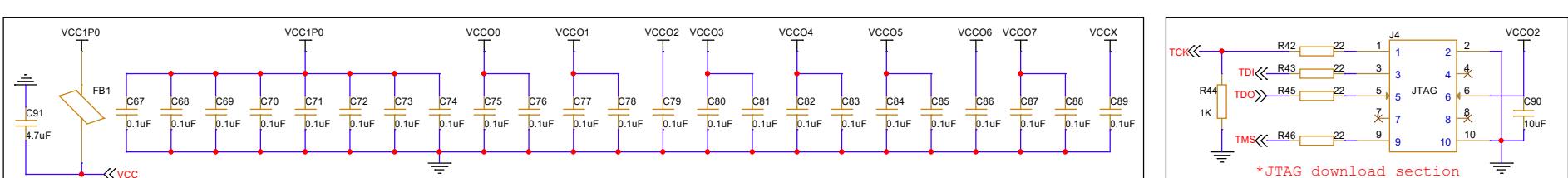
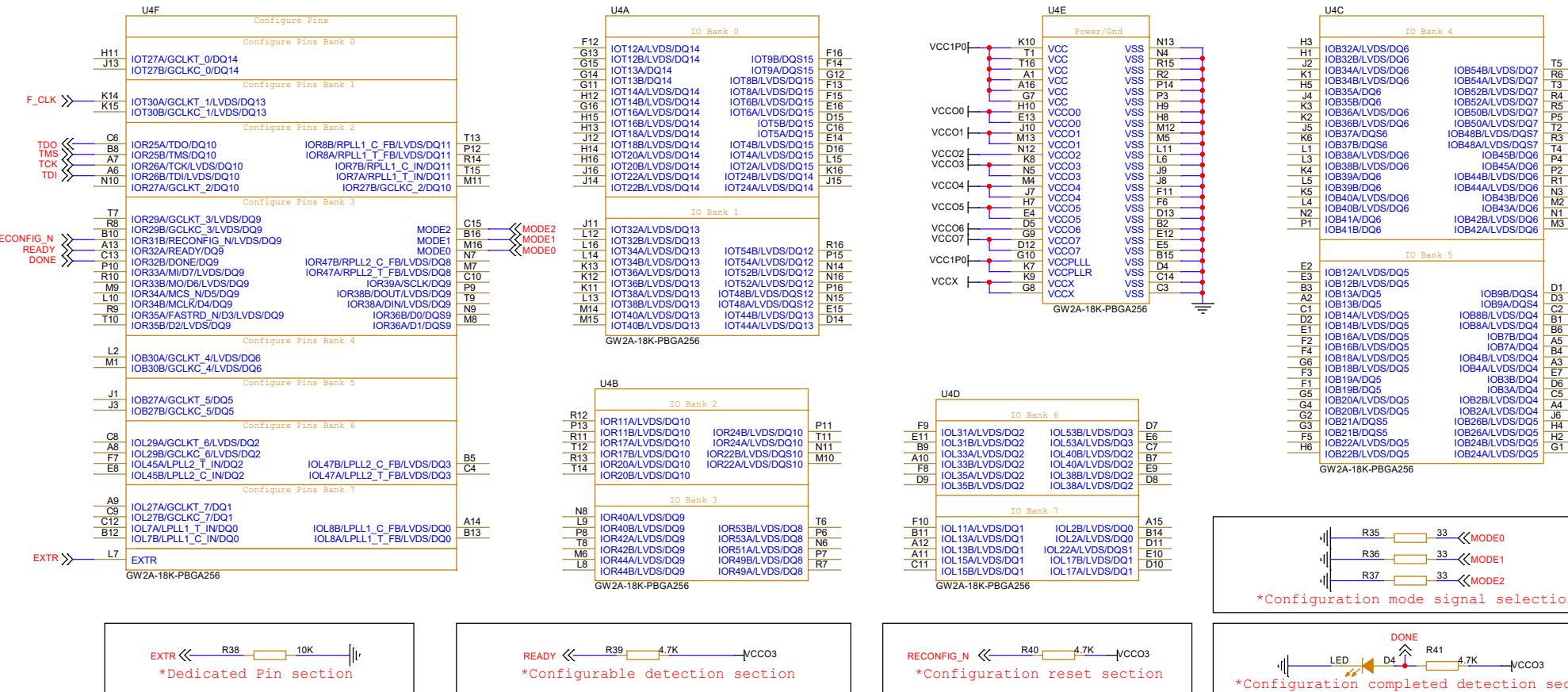
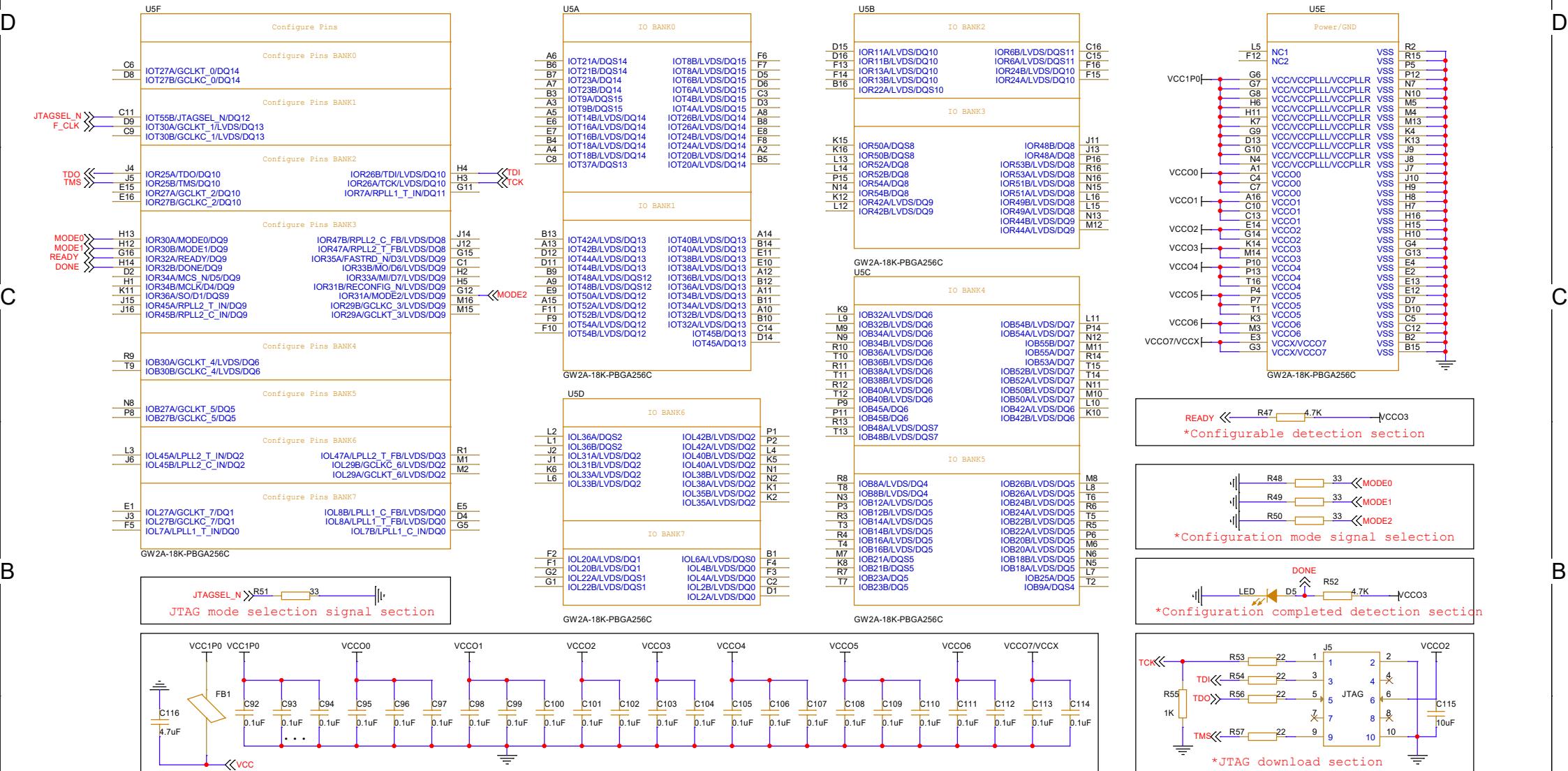


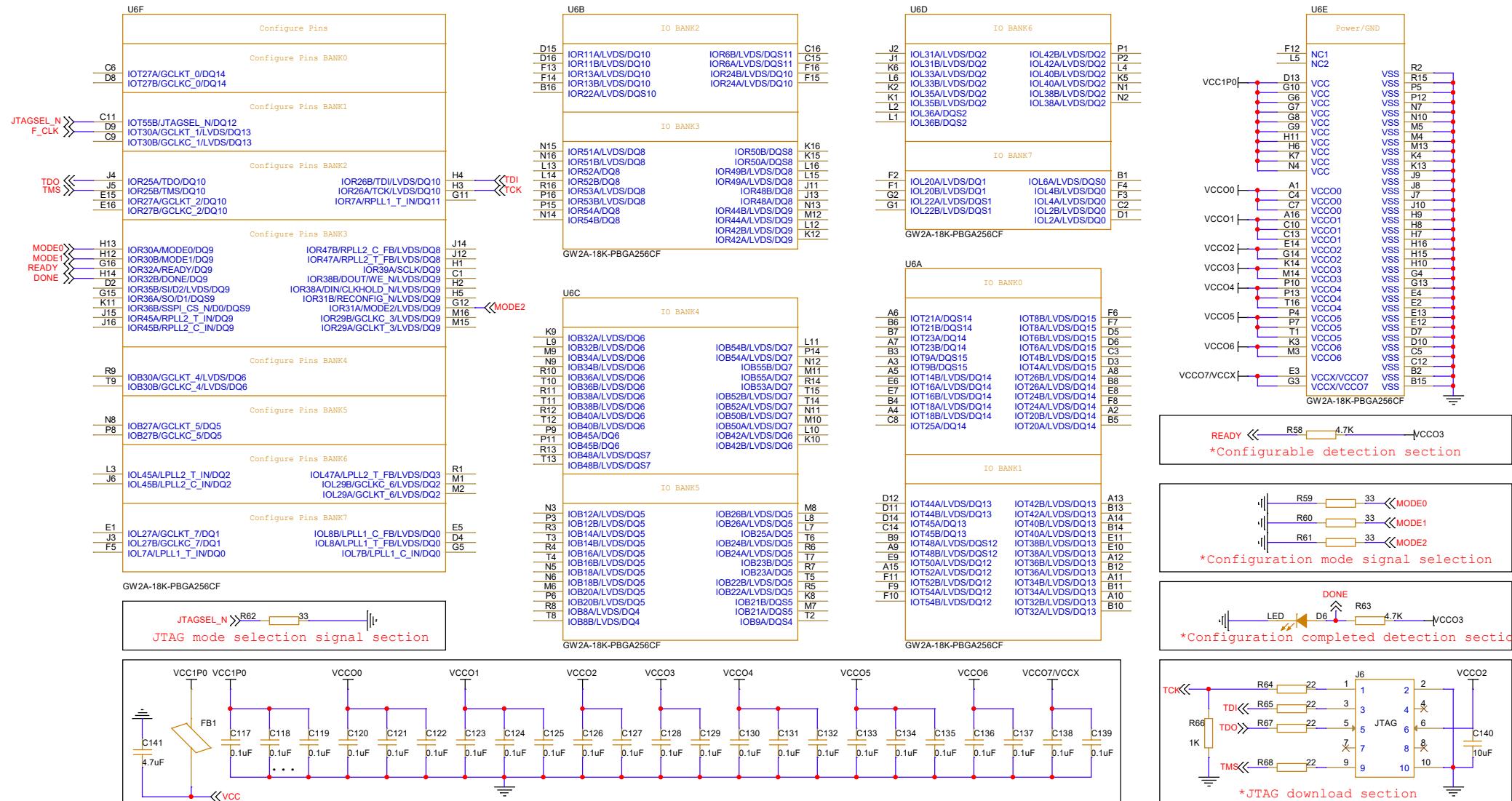
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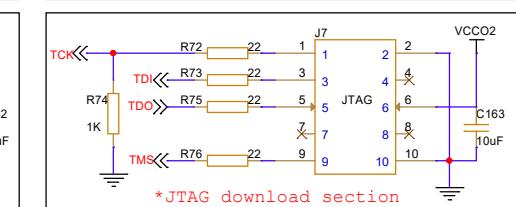
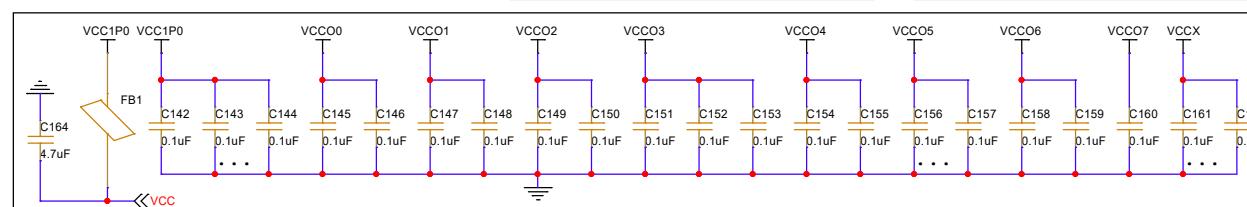
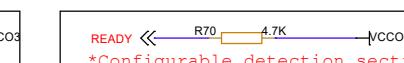
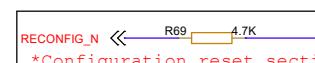
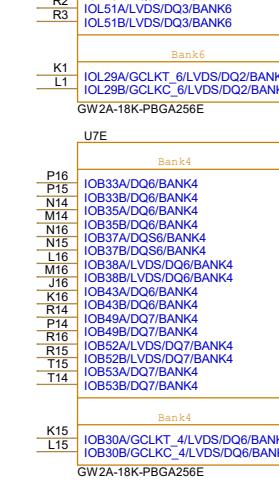
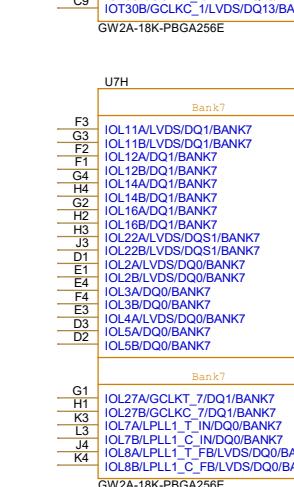
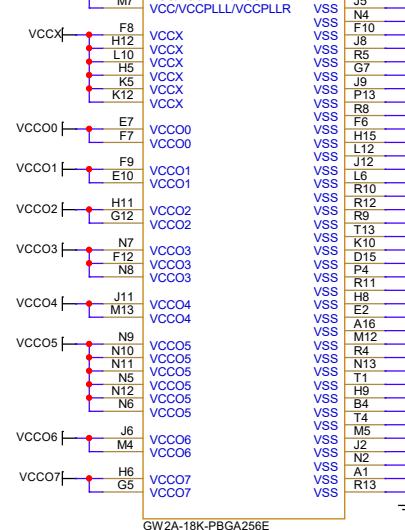
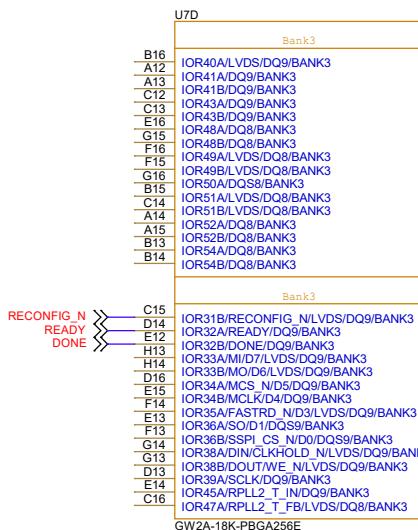
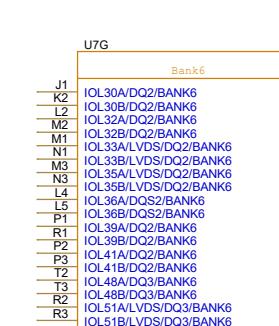
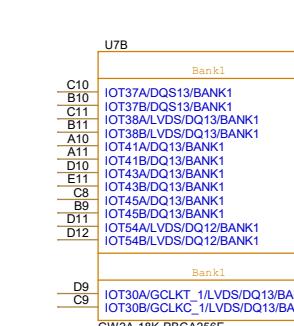
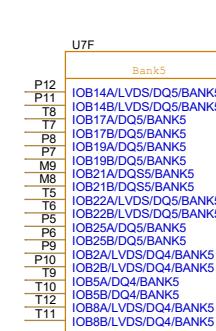
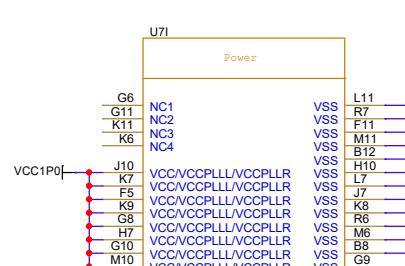
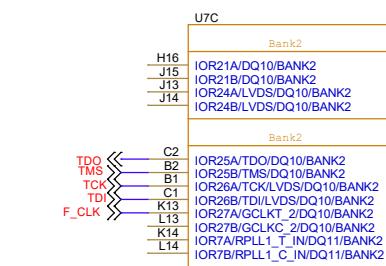




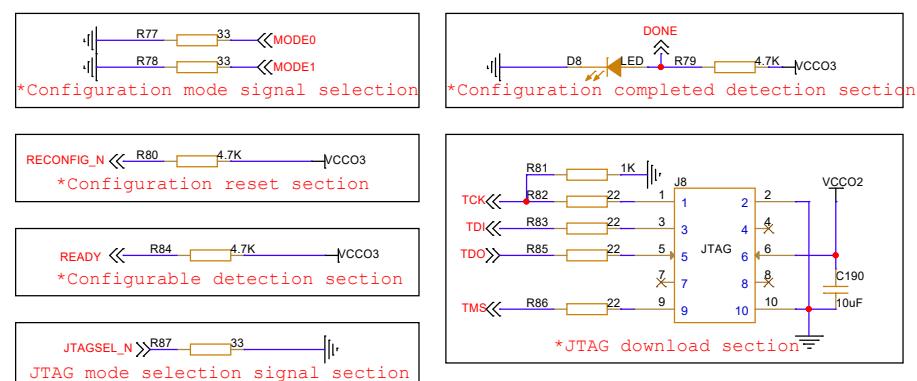
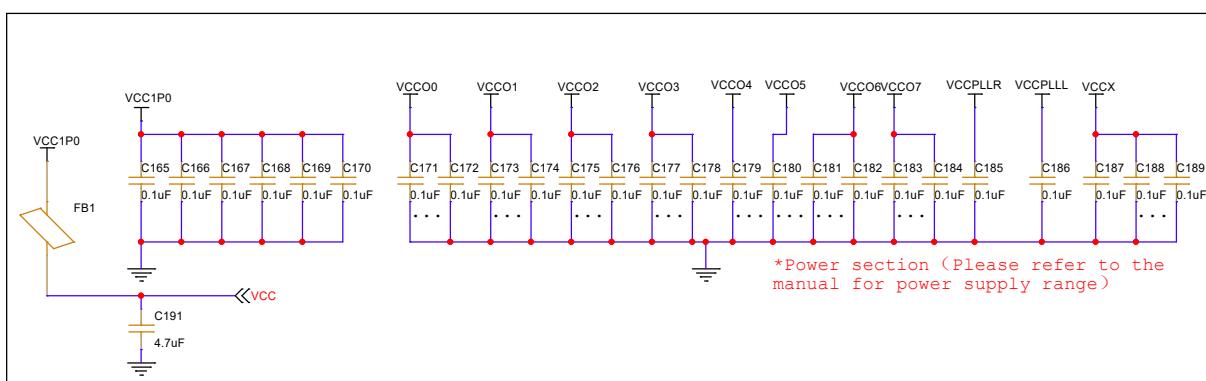
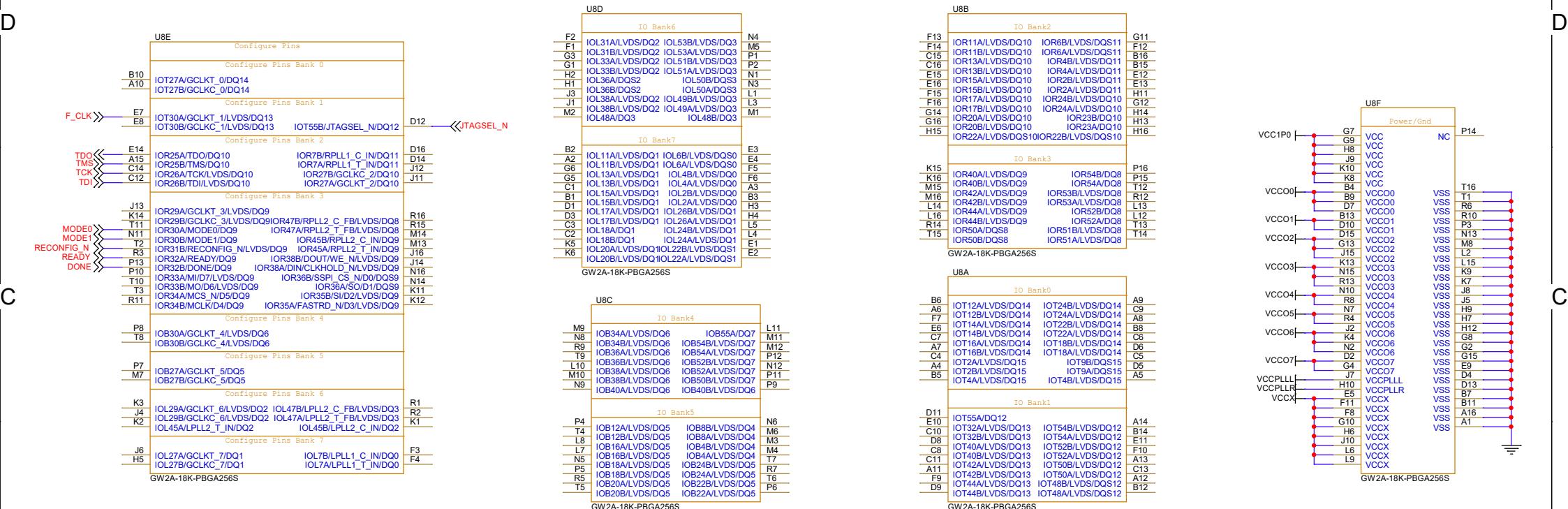


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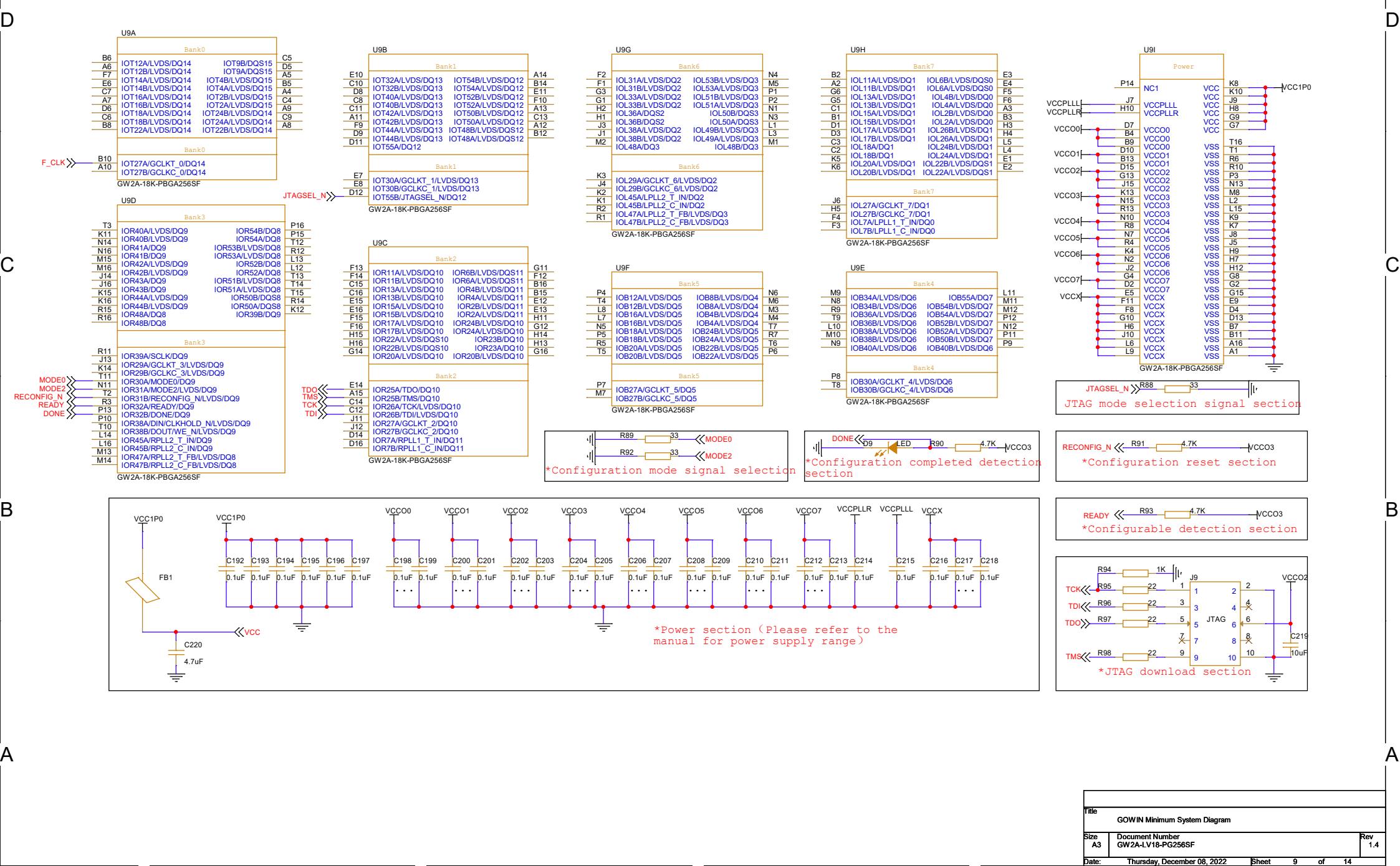
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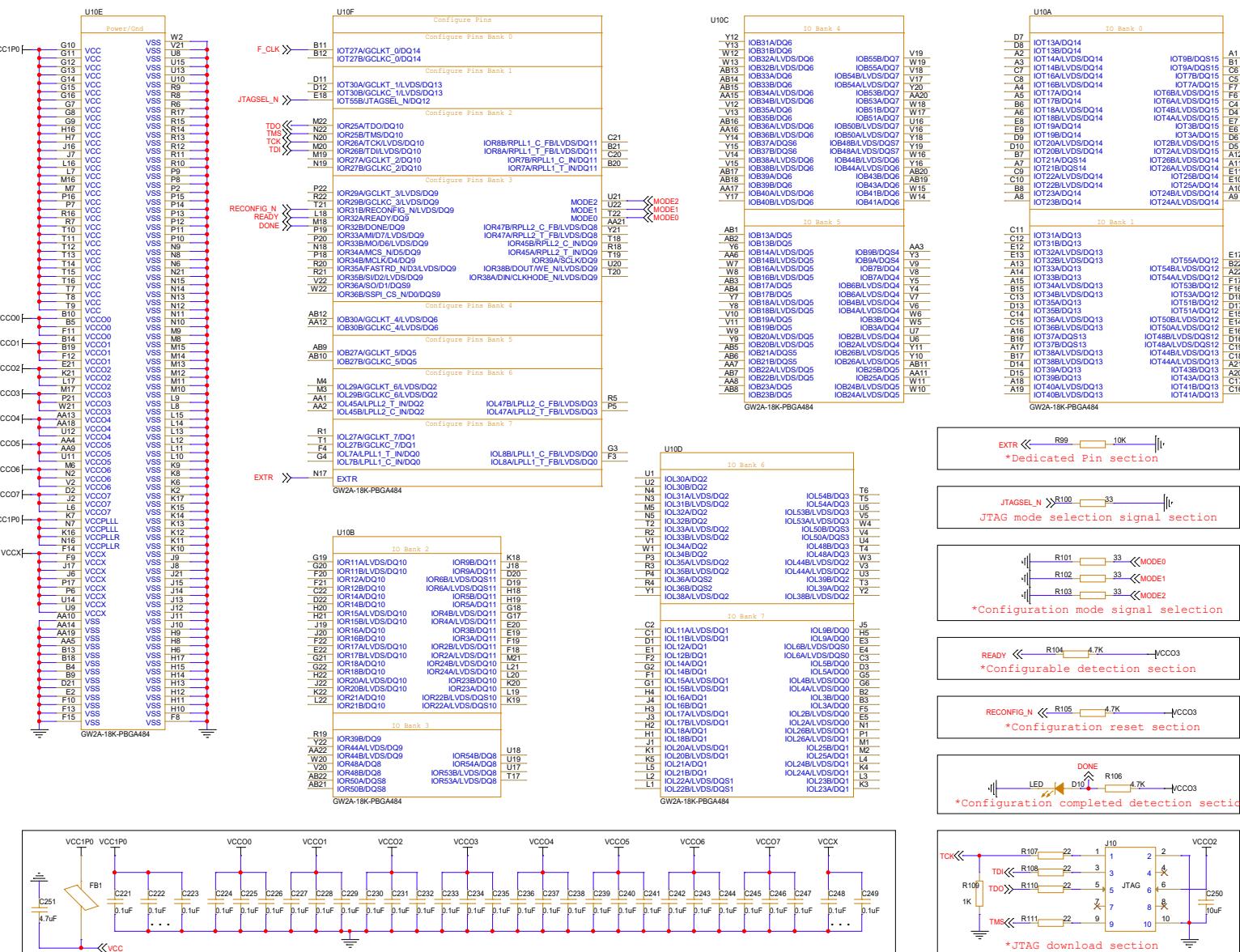


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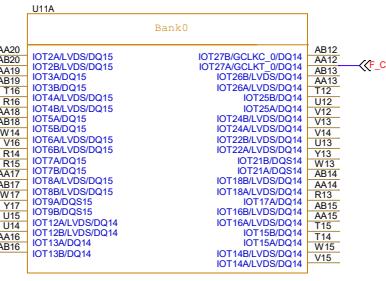
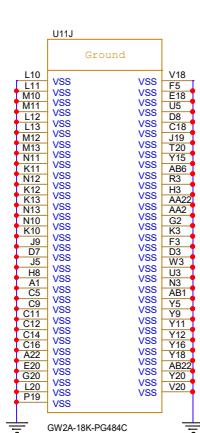
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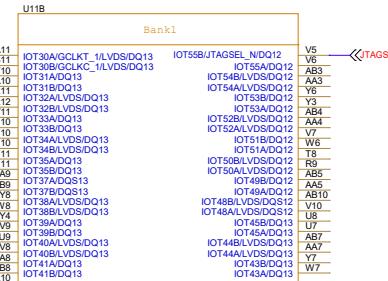


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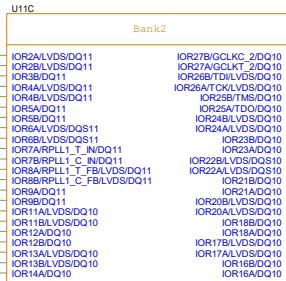
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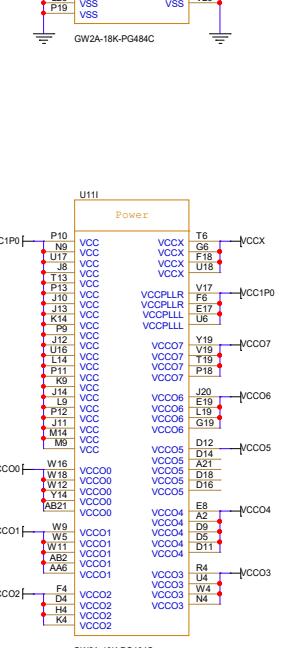
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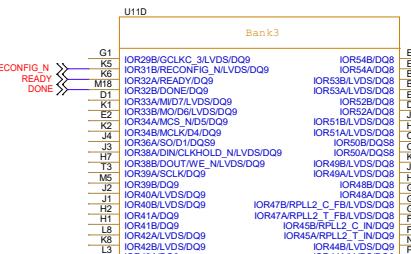
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IOT42B/LVDS/DQ13



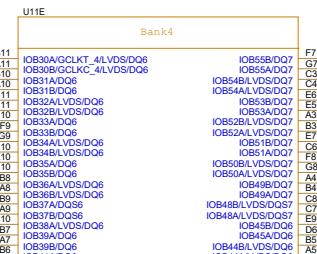
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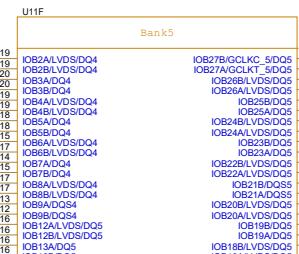
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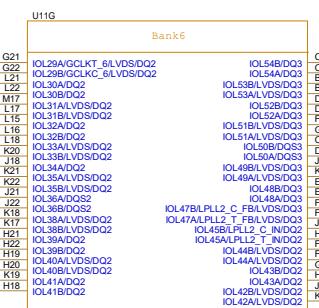
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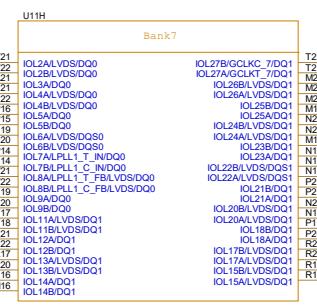
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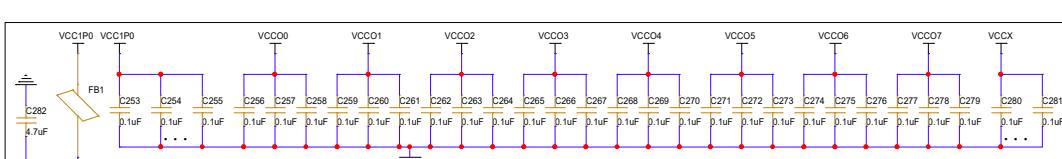
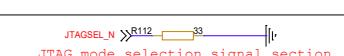
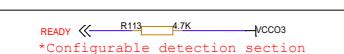


Figure 1.2.10. A logic symbol for a three-terminal voltage-controlled voltage source (VCCS). The output terminal is labeled V_{out}, the input terminal is labeled V_{in}, and the control terminal is labeled V_{CC}.



Time mode selection signal section



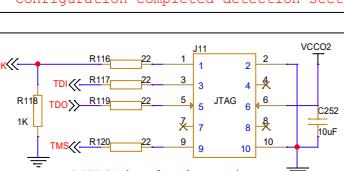
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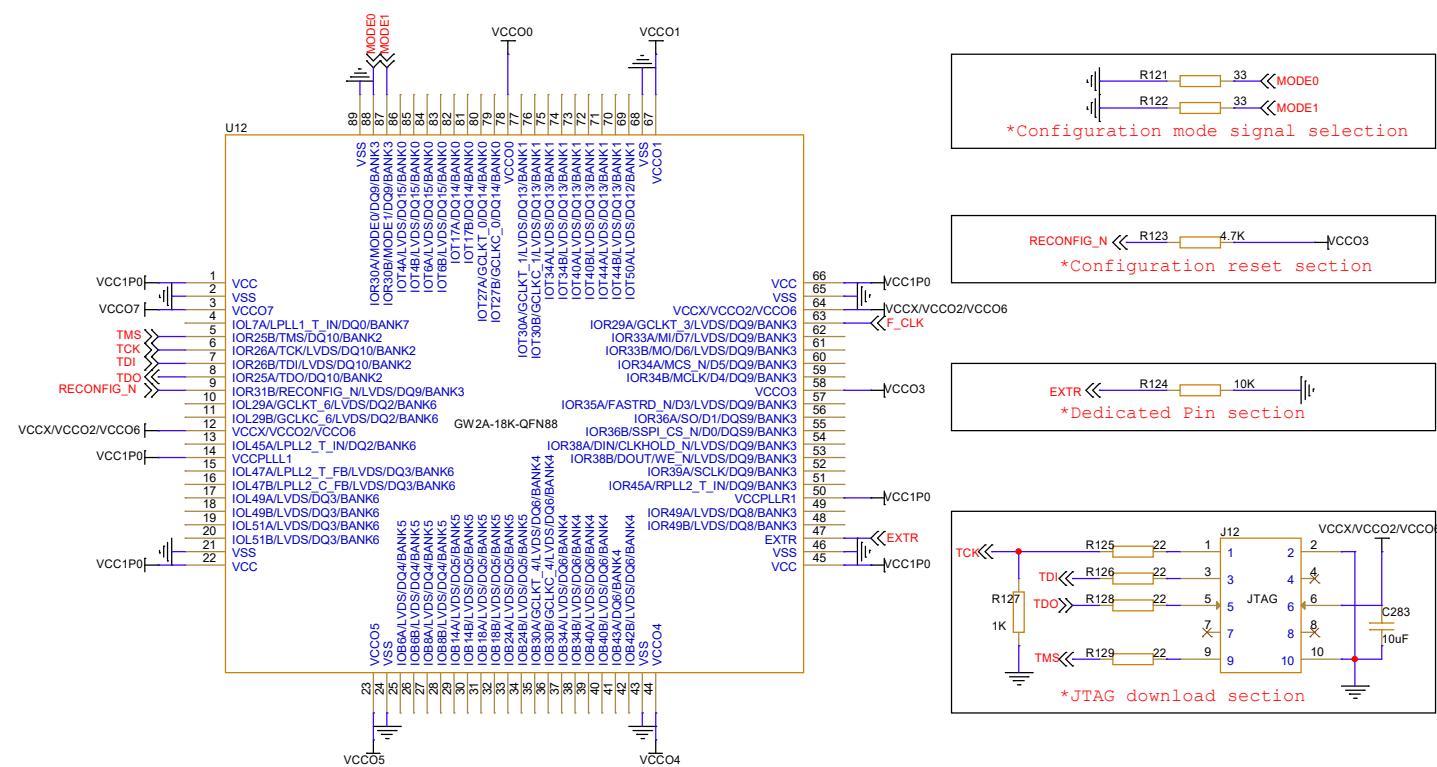


LED D11 4.7K VCC03

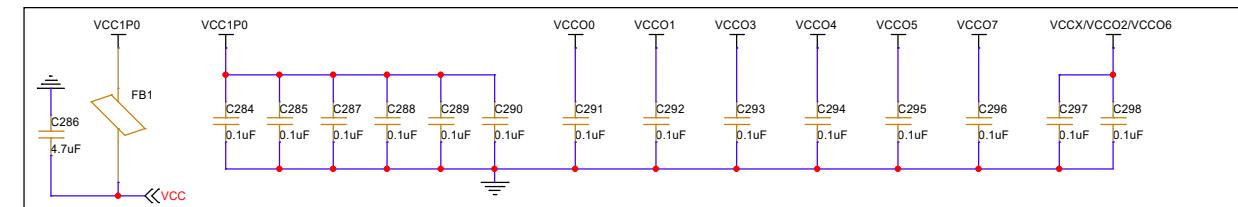


*JTAG download section

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