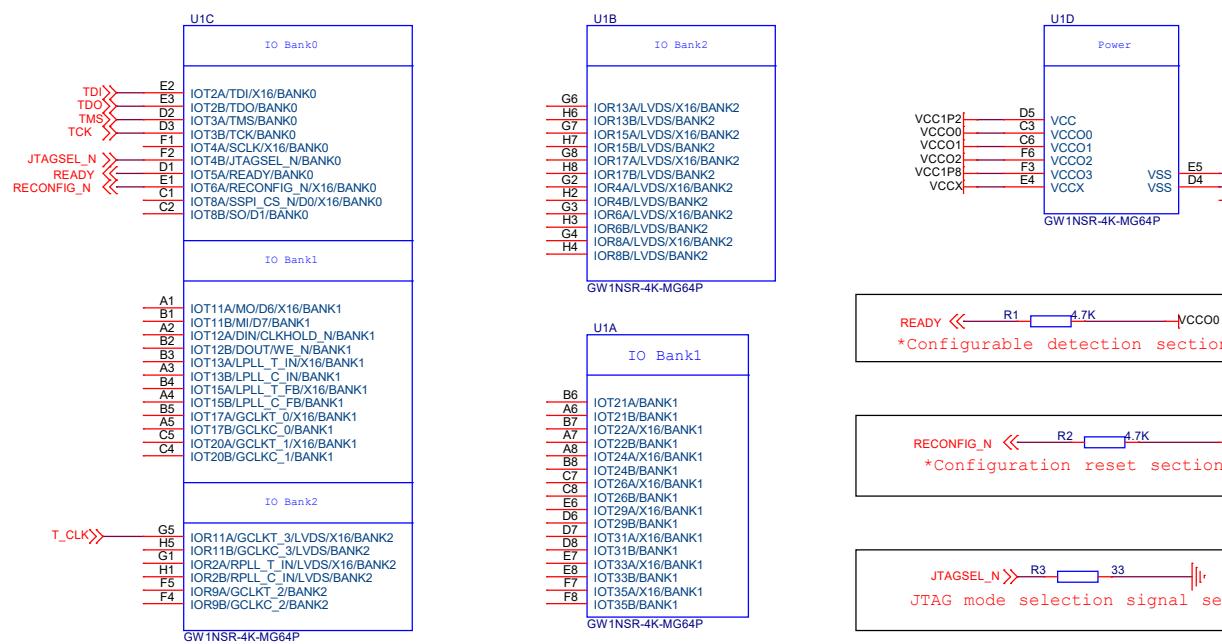


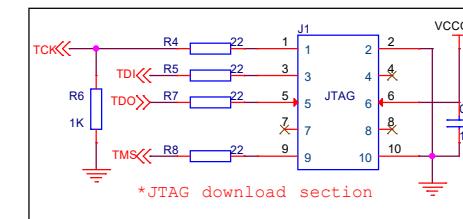
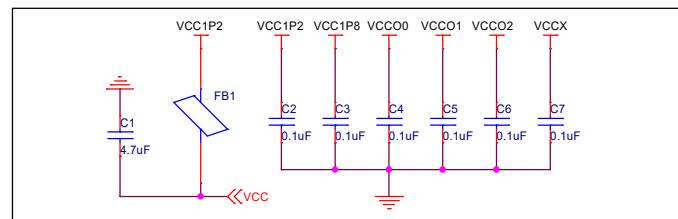
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READY ⇐ R1 4.7K VCC00
*Configurable detection section

RECONFIG_N ⇐ R2 4.7K VCC00
*Configuration reset section

JTAGSEL_N ⇐ R3 33 JTAG mode selection signal section



Title		
GW1NSR Minimum System Diagram		
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