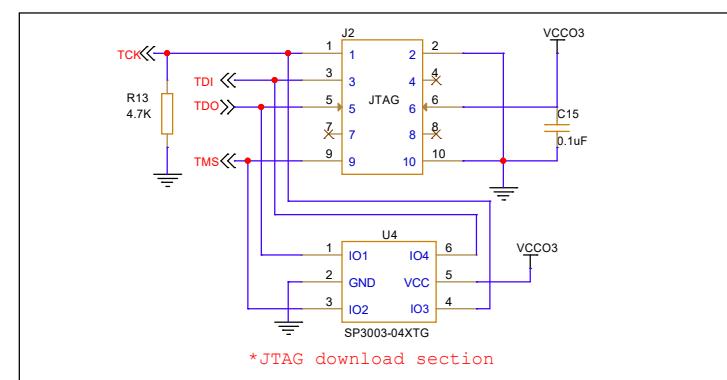
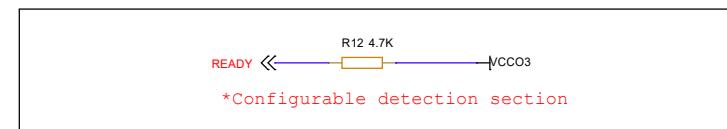
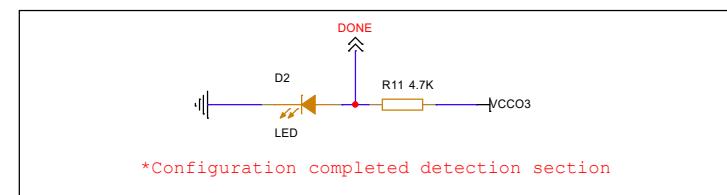
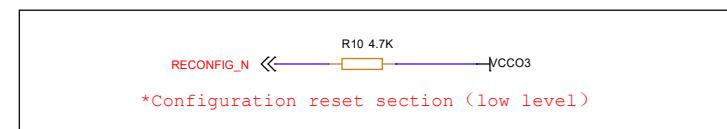
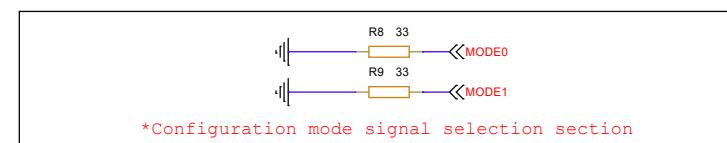
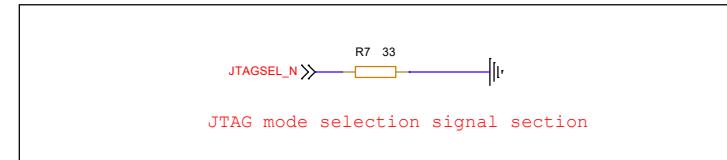
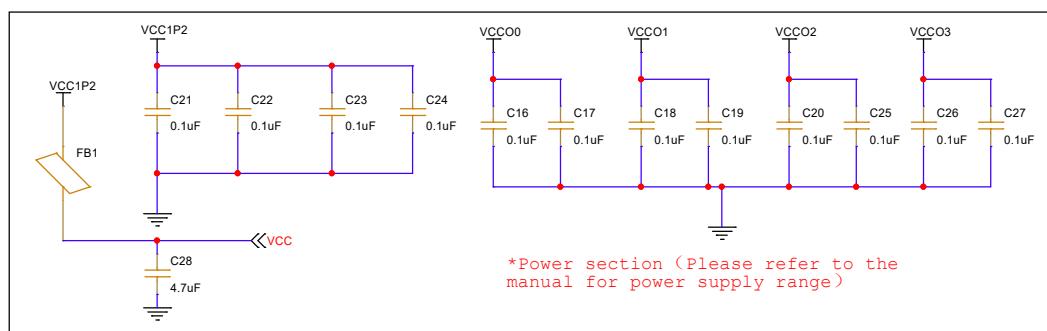
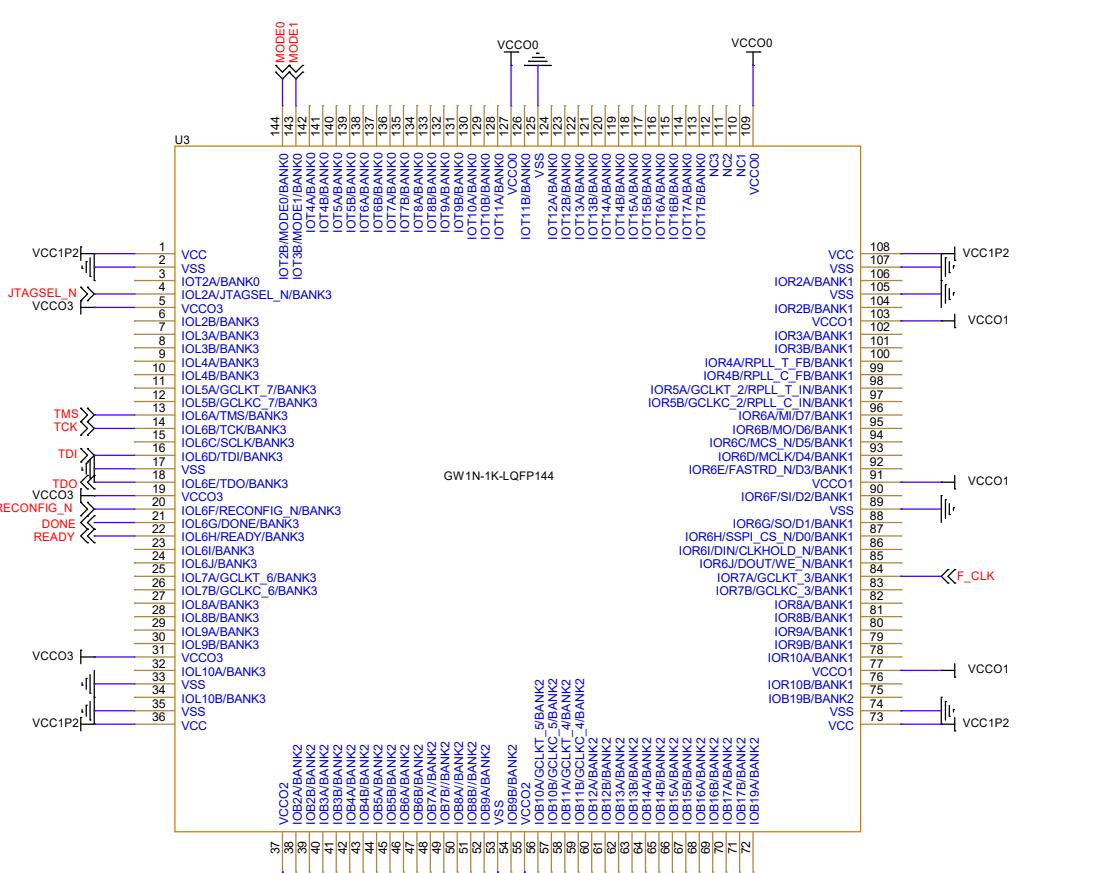
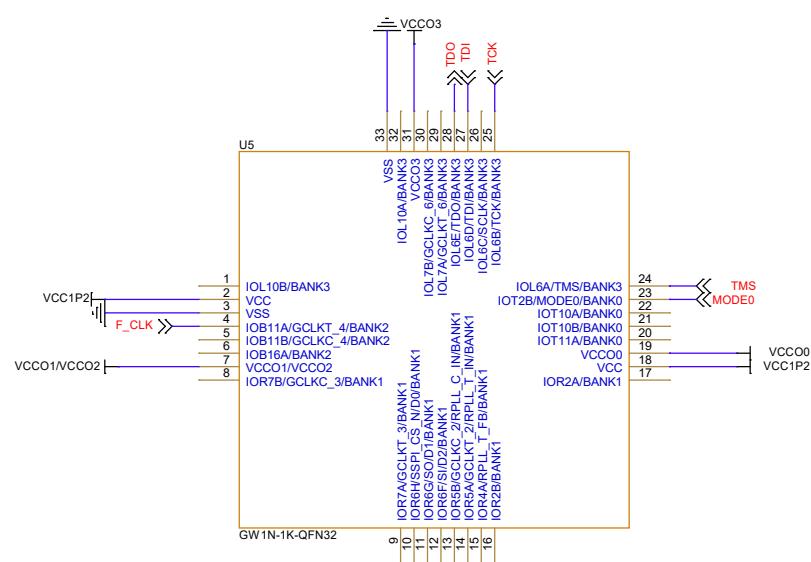


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GOWIN Minimum System Diagram	
Size A3	Document Number GW1N-LV1-LQFP100
Date: Thursday, February 02, 2023	Sheet 1 of 5

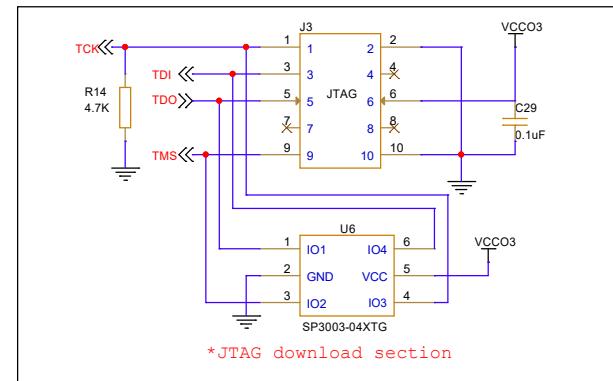


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Date:	Thursday, February 02, 2023	Rev	1.2

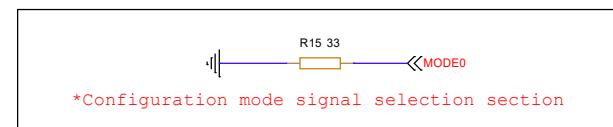


*The unconnected port of the chip is used as a single-ended signal port or multiplexing function

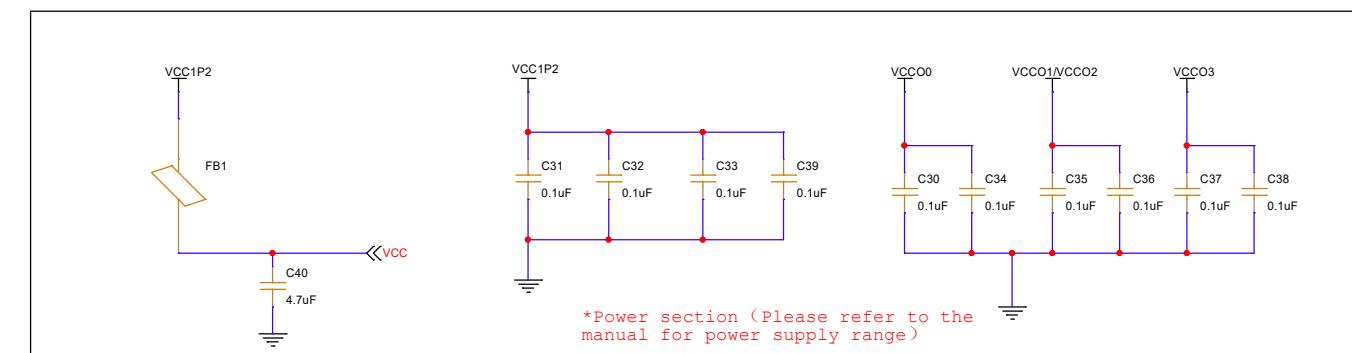
*Configuration: JTAG.AUTO BOOT.SSPI



*JTAG download section



*Configuration mode signal selection section



*Power section (Please refer to the manual for power supply range)

Title		
GOWIN Minimum System Diagram		
Size	Document Number	Rev
A3	GW1N-LV1-QFN32	1.2
Date:	Thursday, February 02, 2023	Sheet 1 of 5

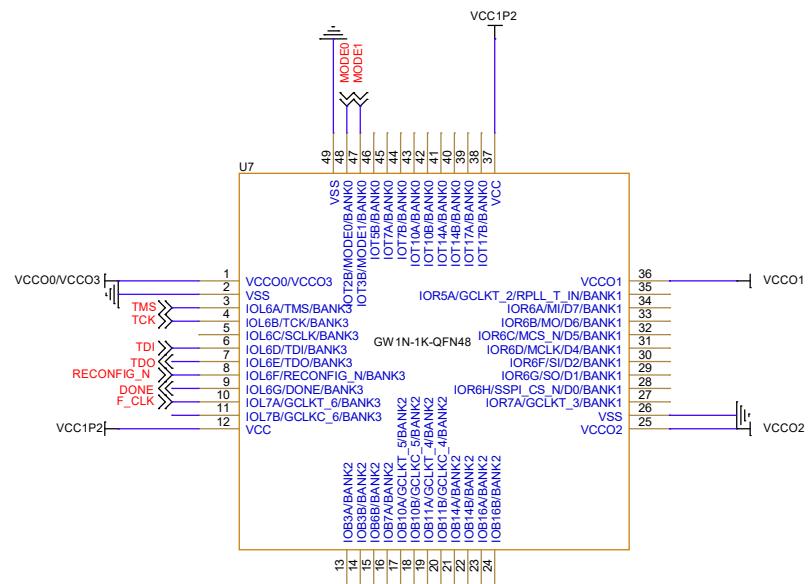
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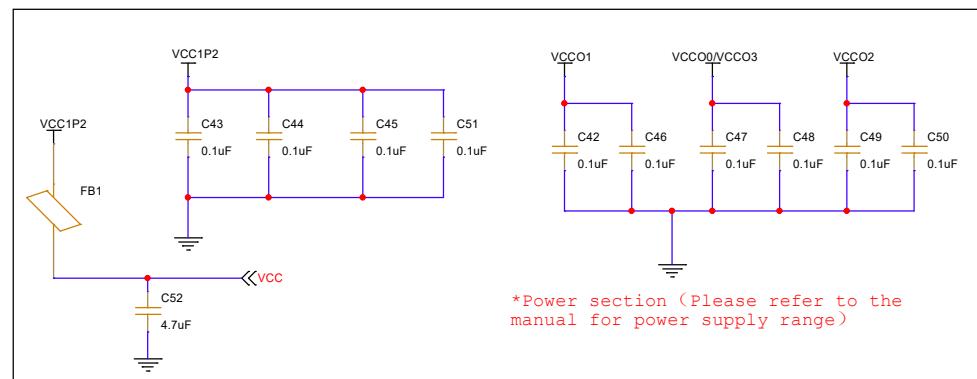
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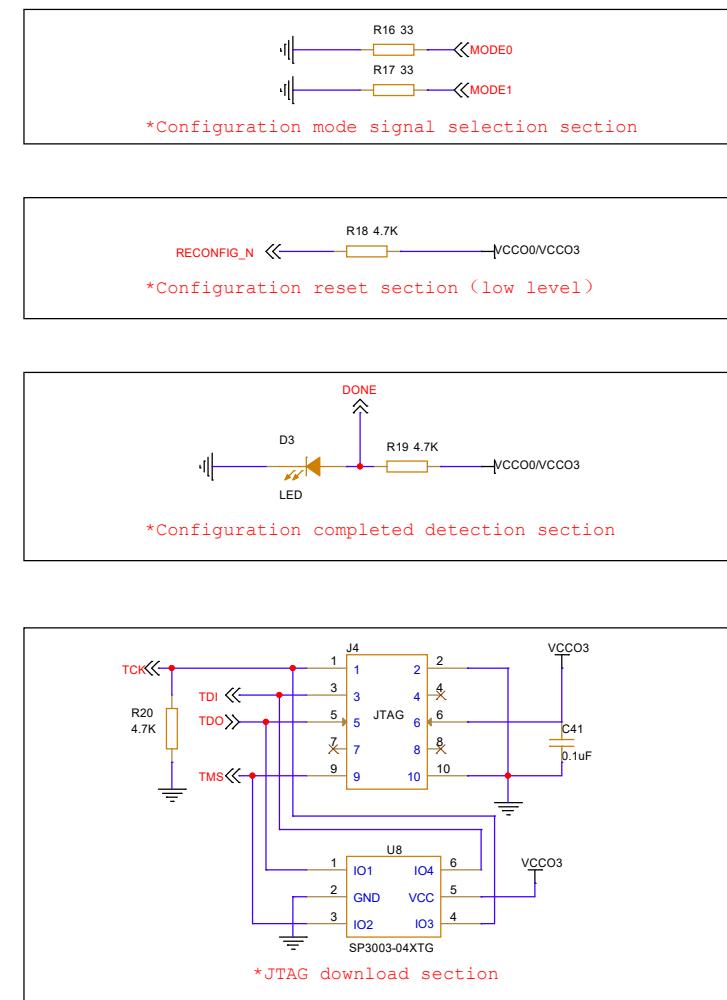


*The unconnected port of the chip is used as a single-ended signal port or multiplexing function
*Configuration: JTAG AUTO BOOT SSP! MSPI

*Configuration: JTAG.AUTO BOOT.SSPI.MSP

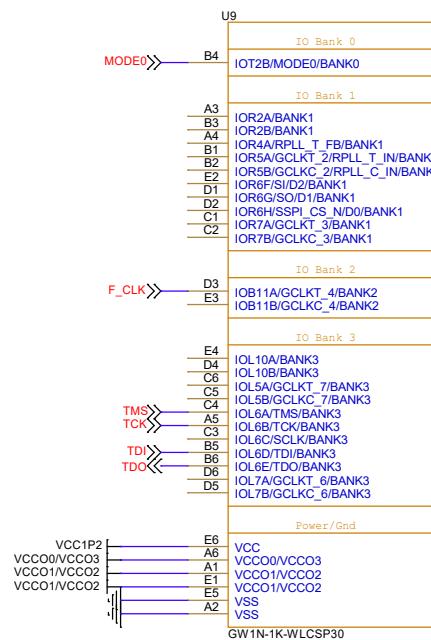


*Power section (Please refer to the manual for power supply range)



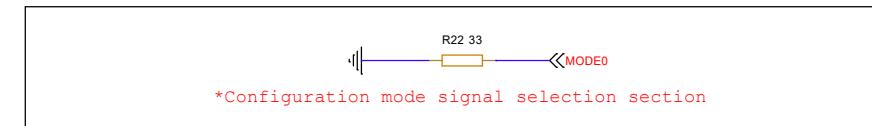
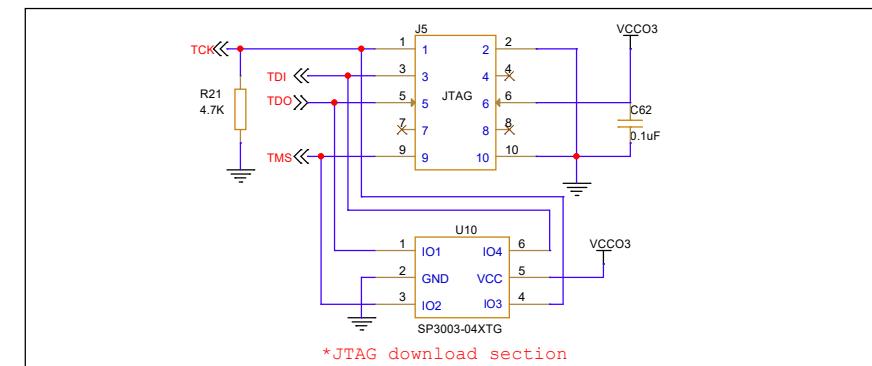
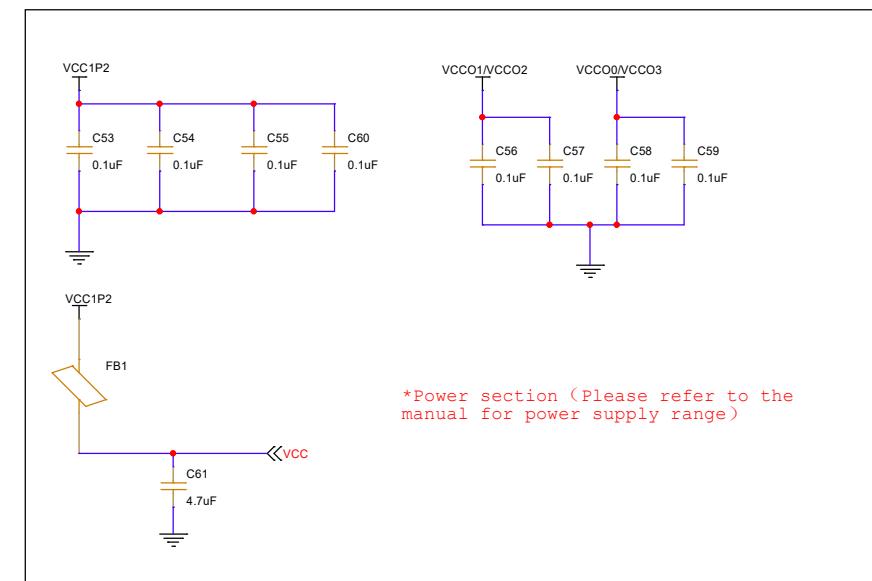
*JTAG download section

Title		GOWIN Minimum System Diagram		
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*The unconnected port of the chip is used as a single-ended signal port or multiplexing function

*Configuration: JTAG.AUTO BOOT.SSPI



Title		
GW1N Minimum System Diagram		
Size	Document Number	Rev
A3	GW1N-LV1-WLCSP30	1.2
Date:	Thursday, February 02, 2023	Sheet 5 of 5