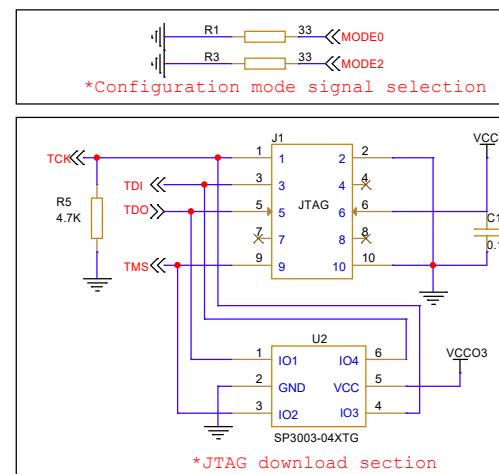
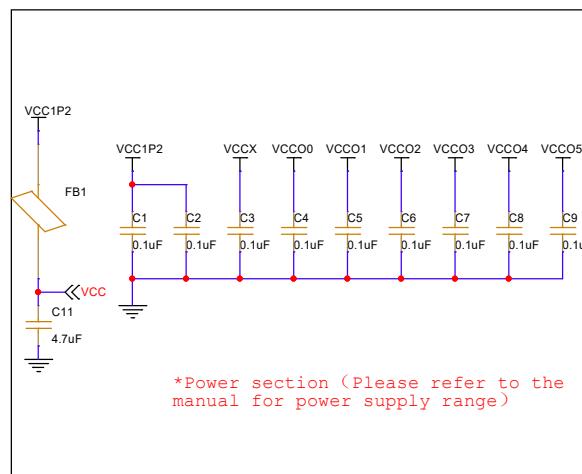
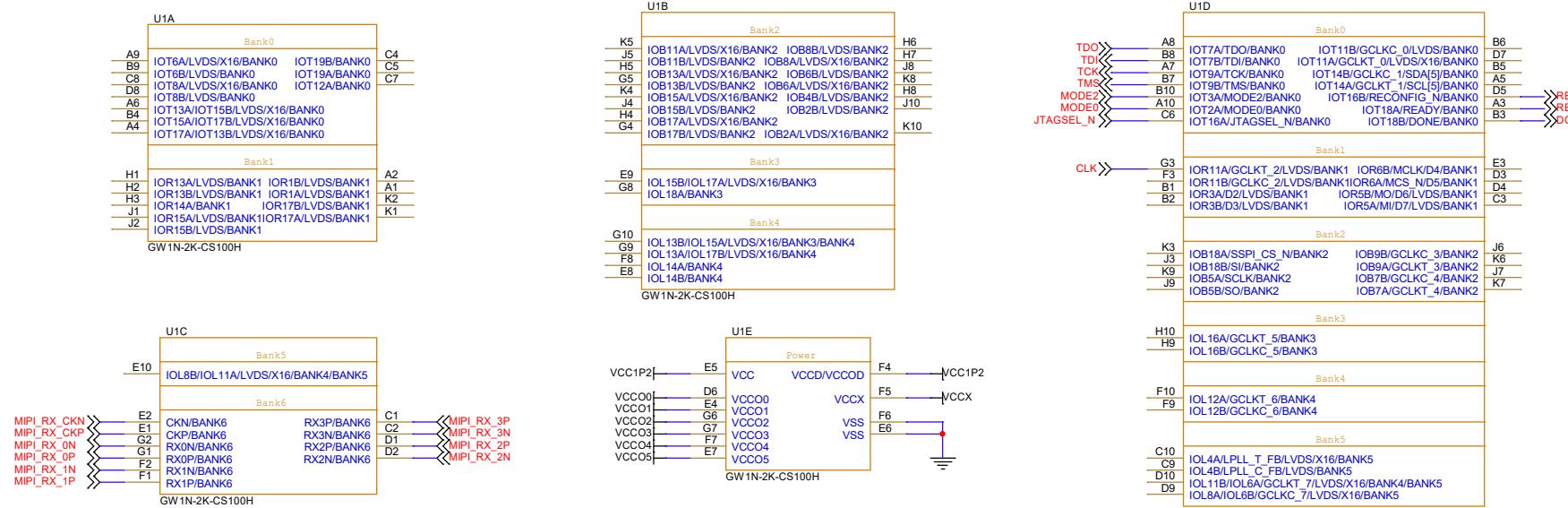
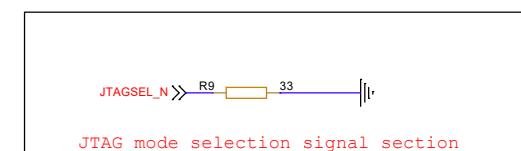
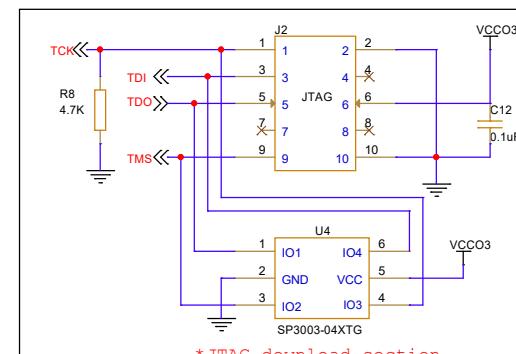
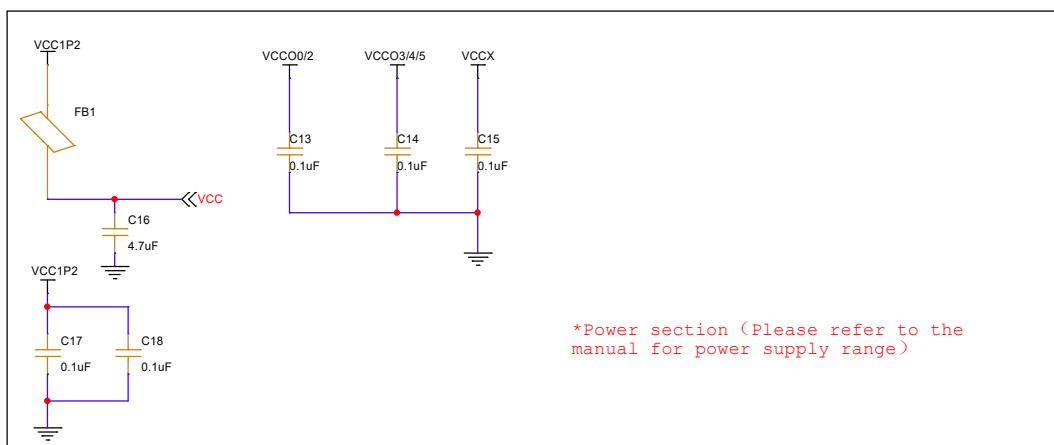
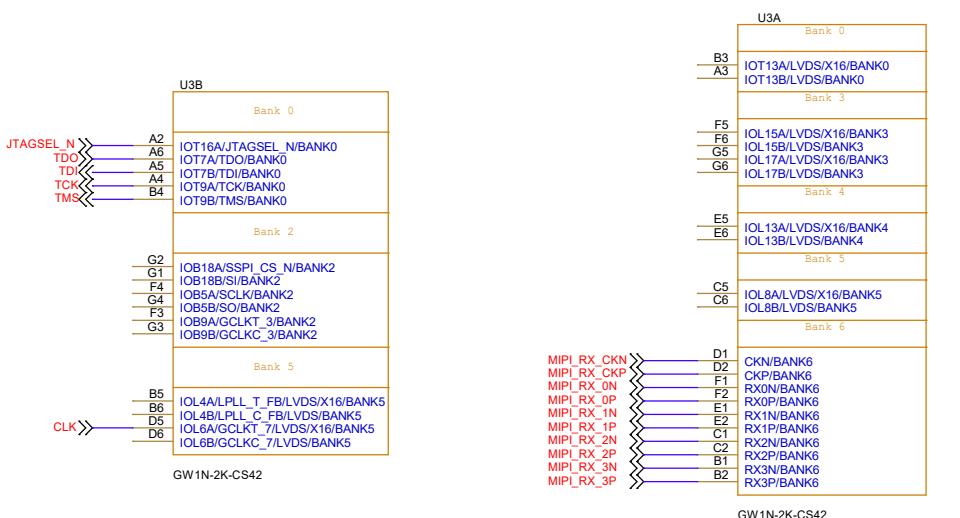
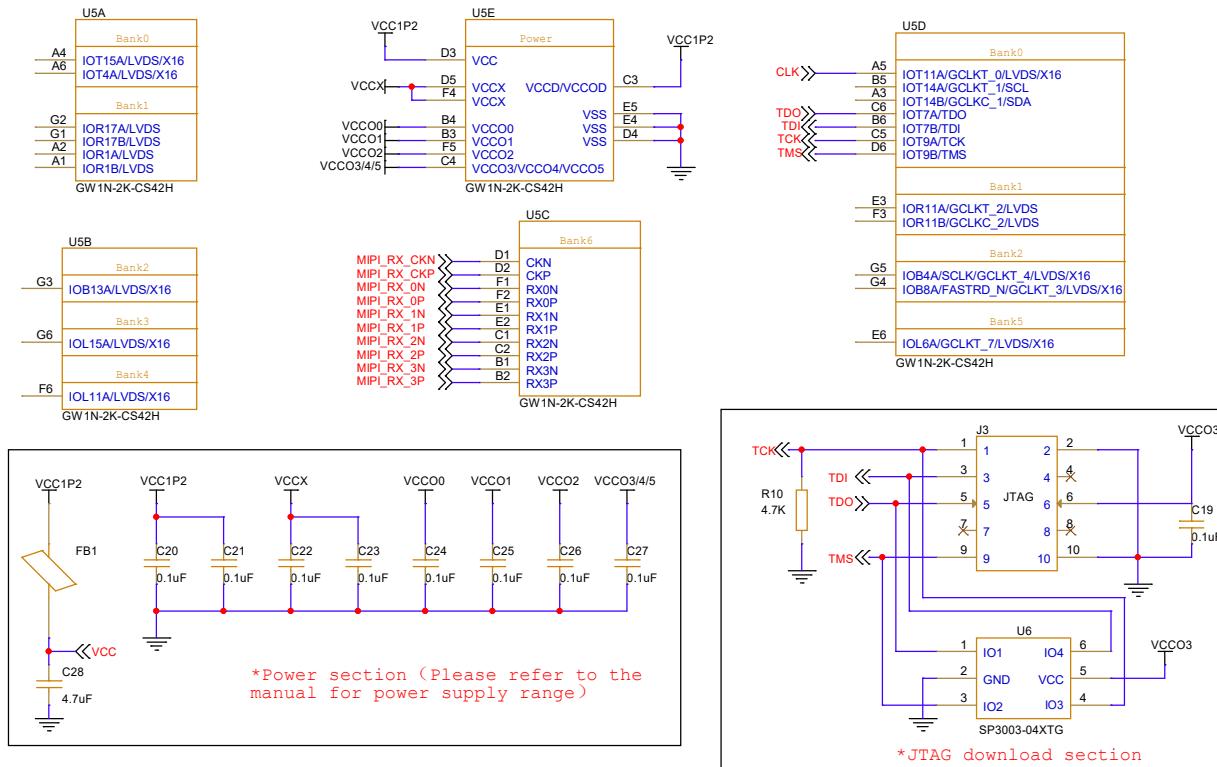


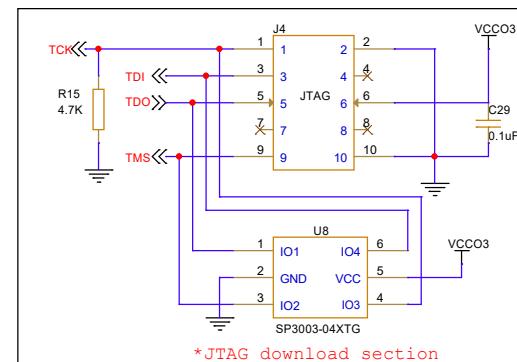
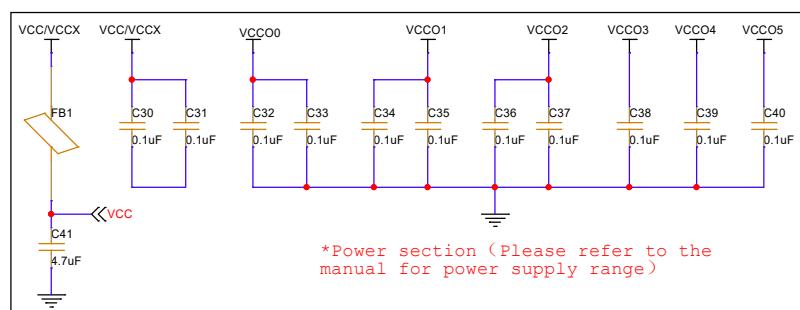
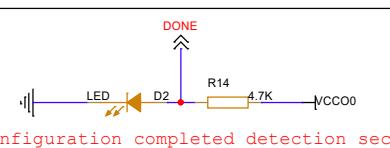
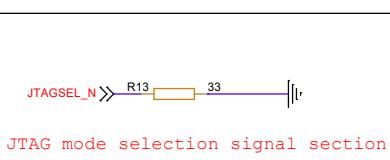
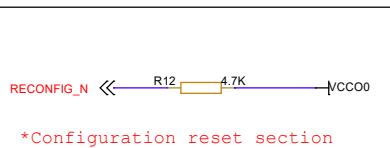
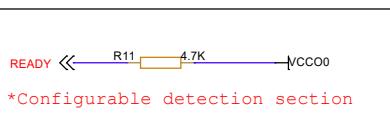
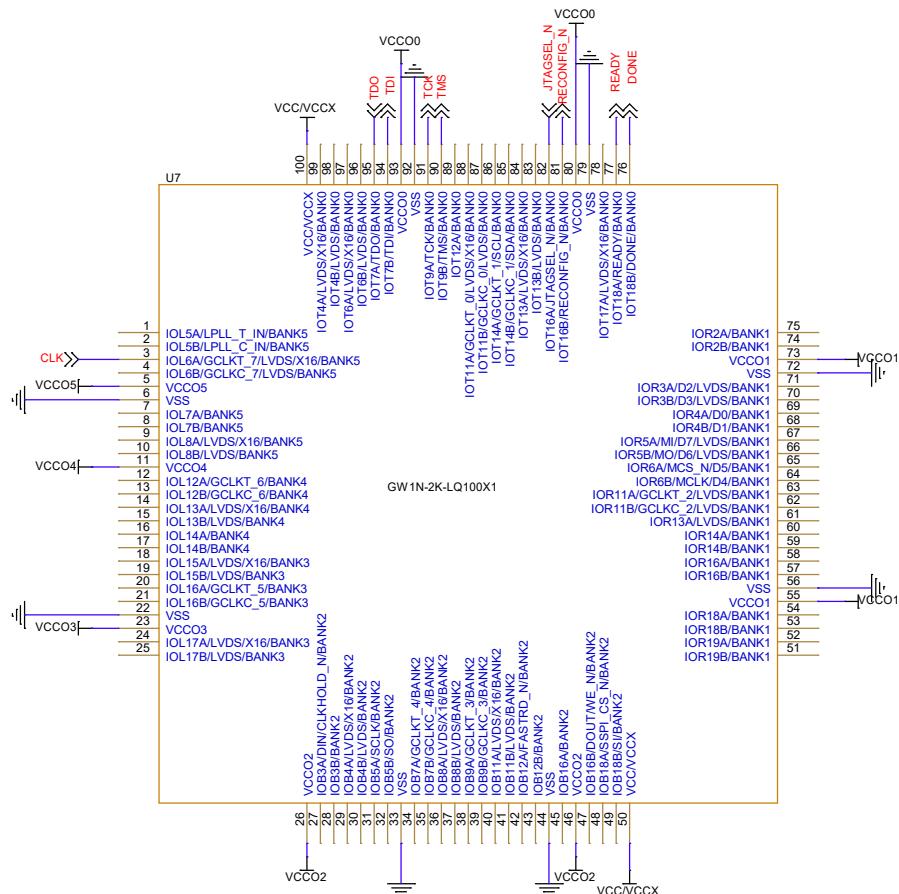
**GW1N-LV2-CS100H**

Title		
GOWIN Minimum System Diagram		
Size	Document Number	Rev
A3	GW1N-LV2-CS100H	V1.3

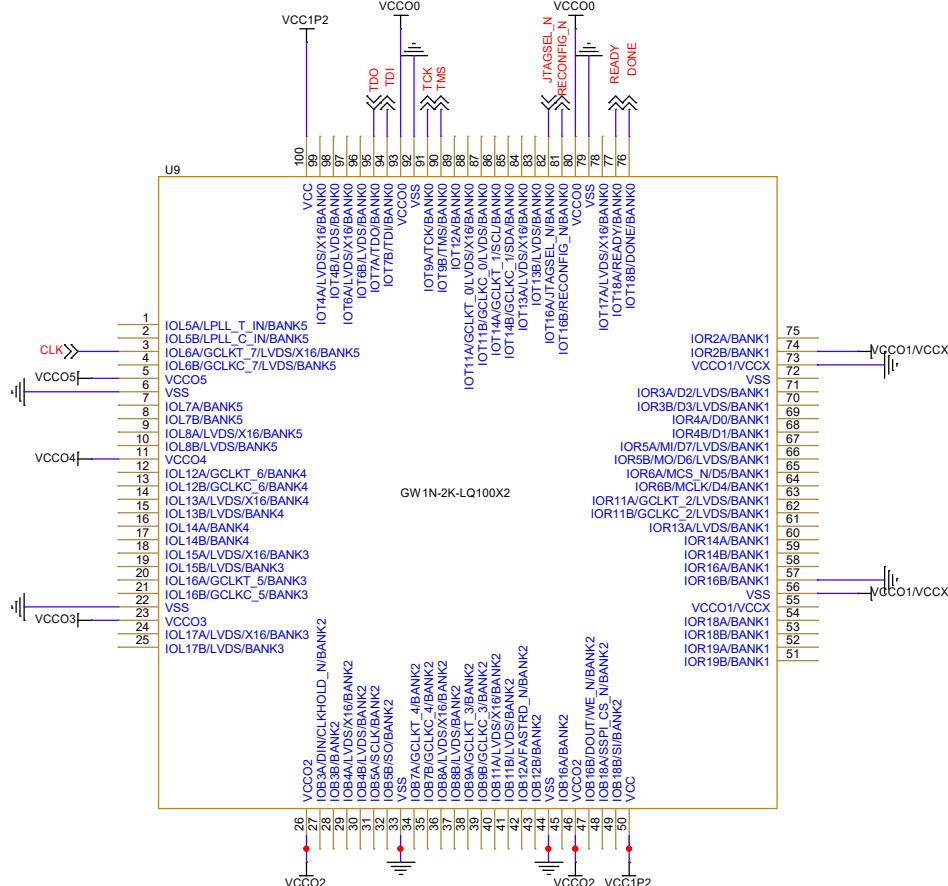




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GW1N Minimum System Diagram		
Size	Document Number	Rev
A3	GW1N-LV2-CS42H	V1.3
Date:	Friday, February 03, 2023	Sheet 3 of 31



Title		GW1N Minimum System Diagram	
Size	Document Number	Rev V1.3	
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Date:	Friday, February 03, 2023	Sheet	4 of 31

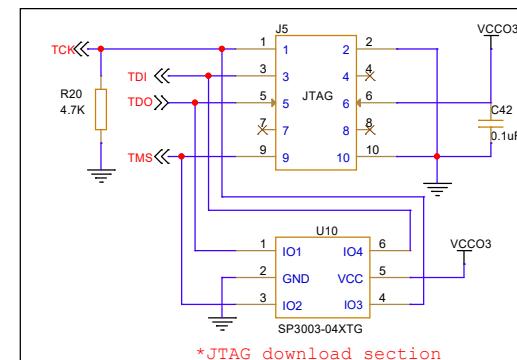
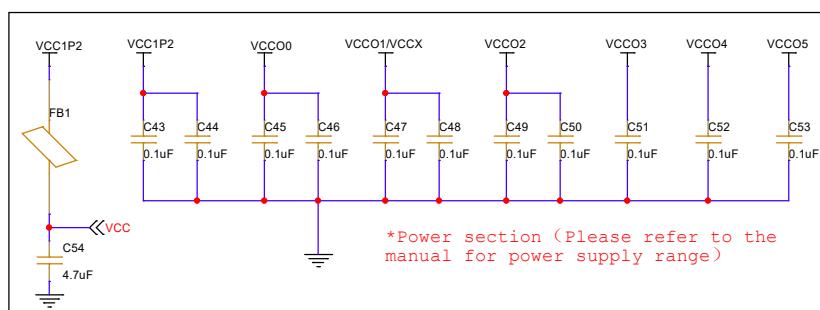


READY  $\ll$  R16 4.7K  $\rightarrow$  VCCO0  
\*Configurable detection section

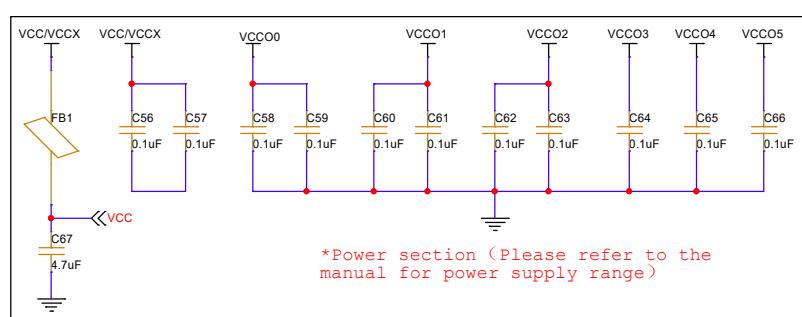
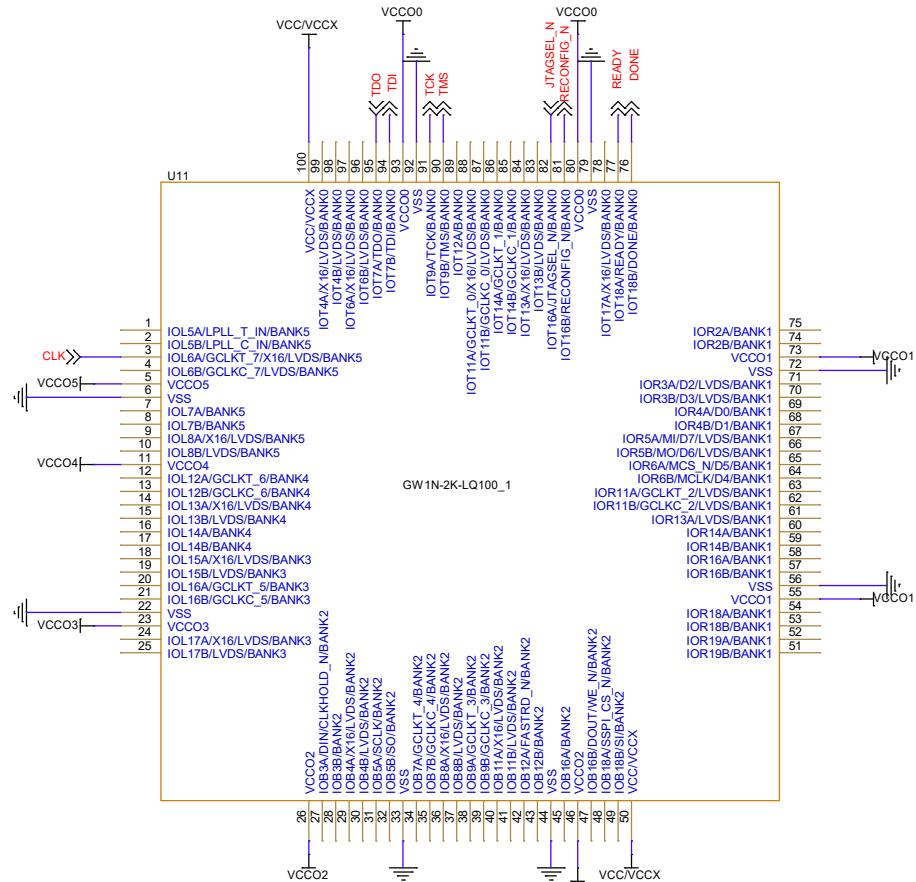
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\*Configuration reset section

JTAGSEL\_N  $\gg$  R18 33  $\rightarrow$   
\*JTAG mode selection signal section

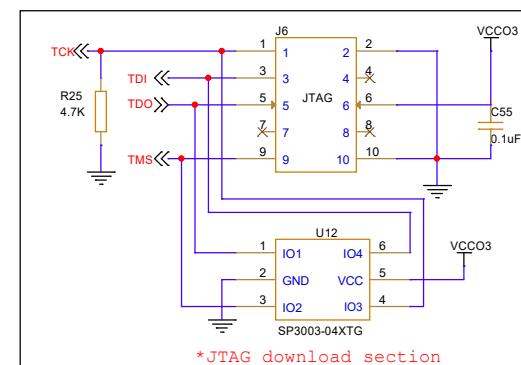
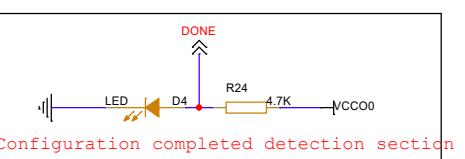
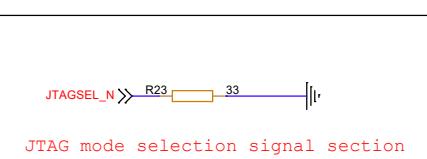
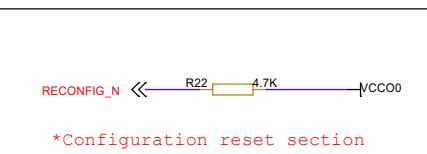
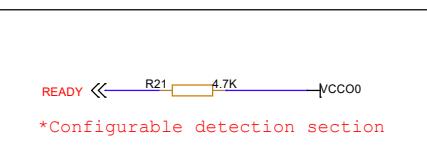
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LED  $\rightarrow$  D3  
R19 4.7K  $\rightarrow$  VCCO0  
\*Configuration completed detection section



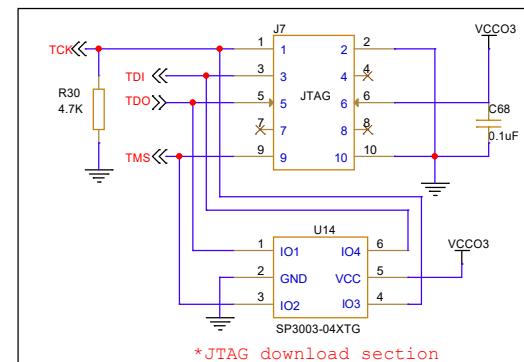
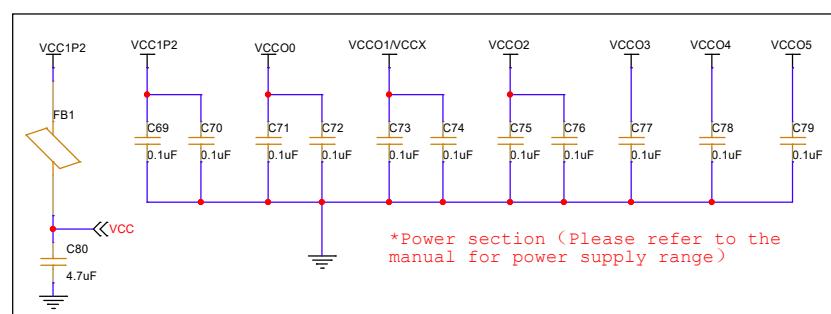
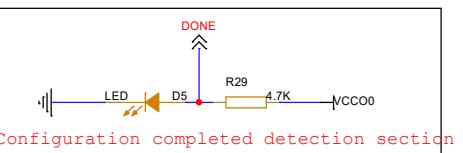
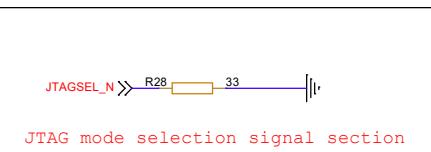
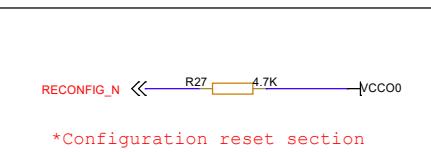
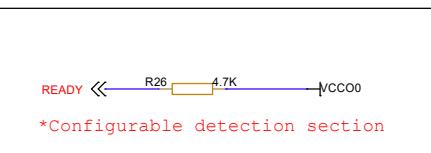
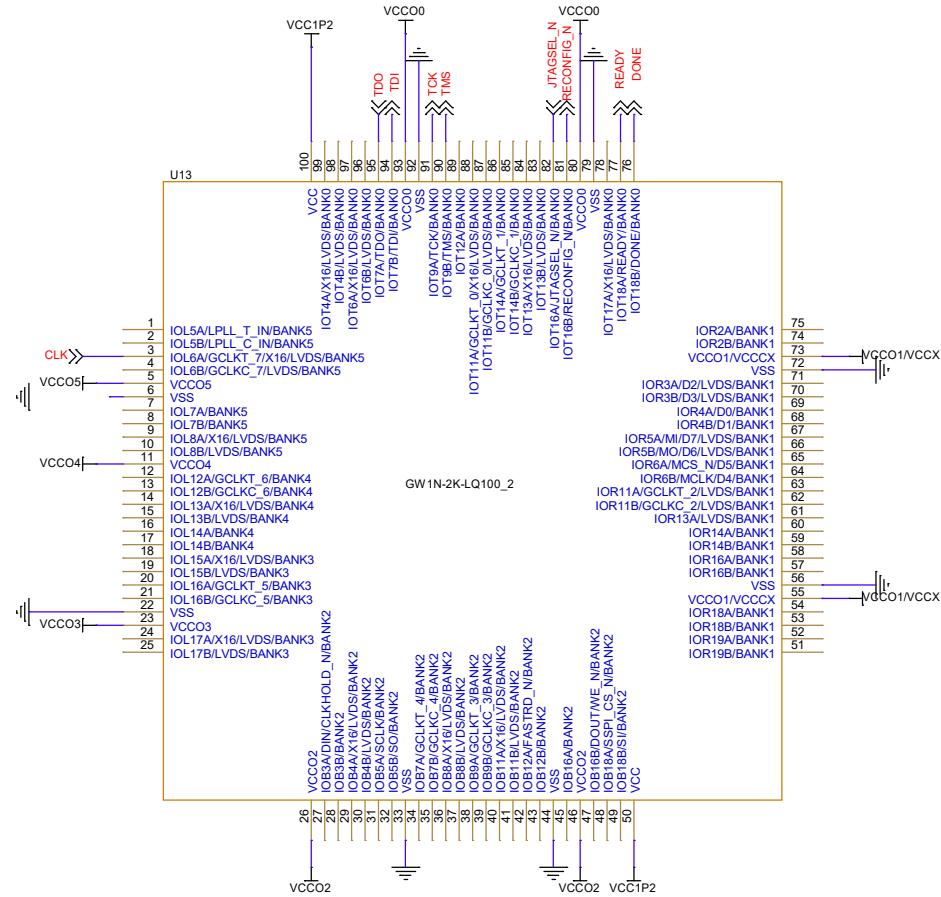
Title		GOWIN Minimum System Diagram	
Size	Document Number		
A3	GW1N-LV2-LQ100X2		
Date:	Friday, February 03, 2023	Sheet	5 of 31
		Rev	V1.3



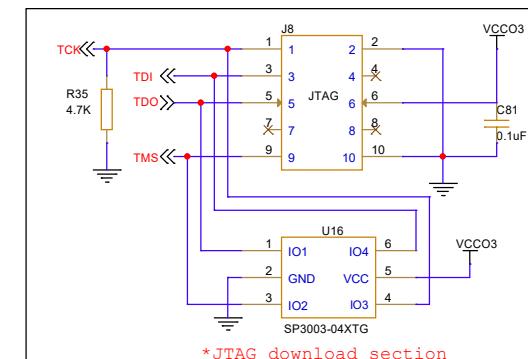
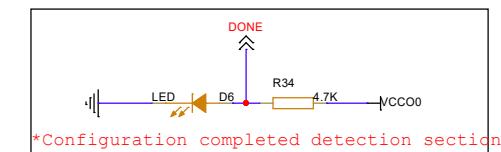
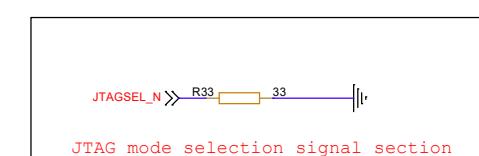
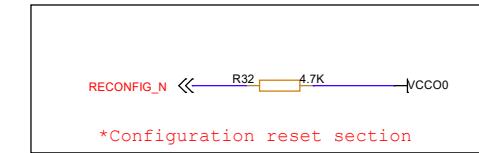
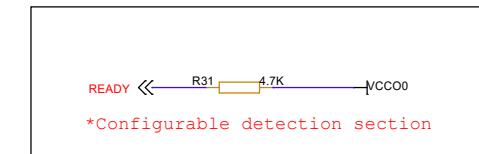
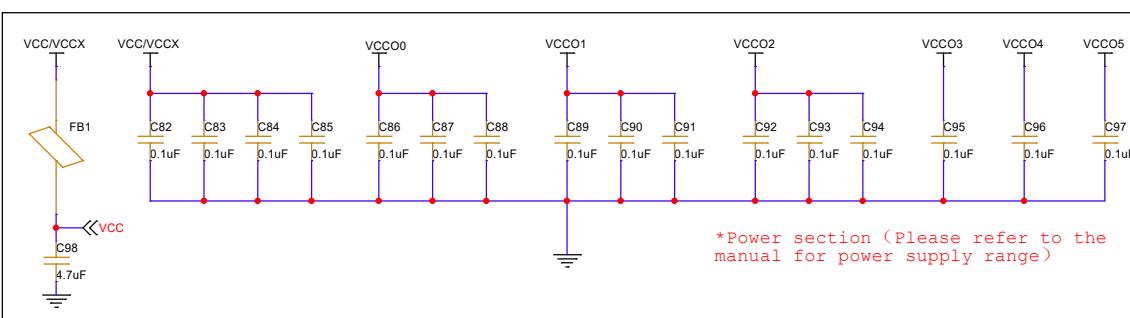
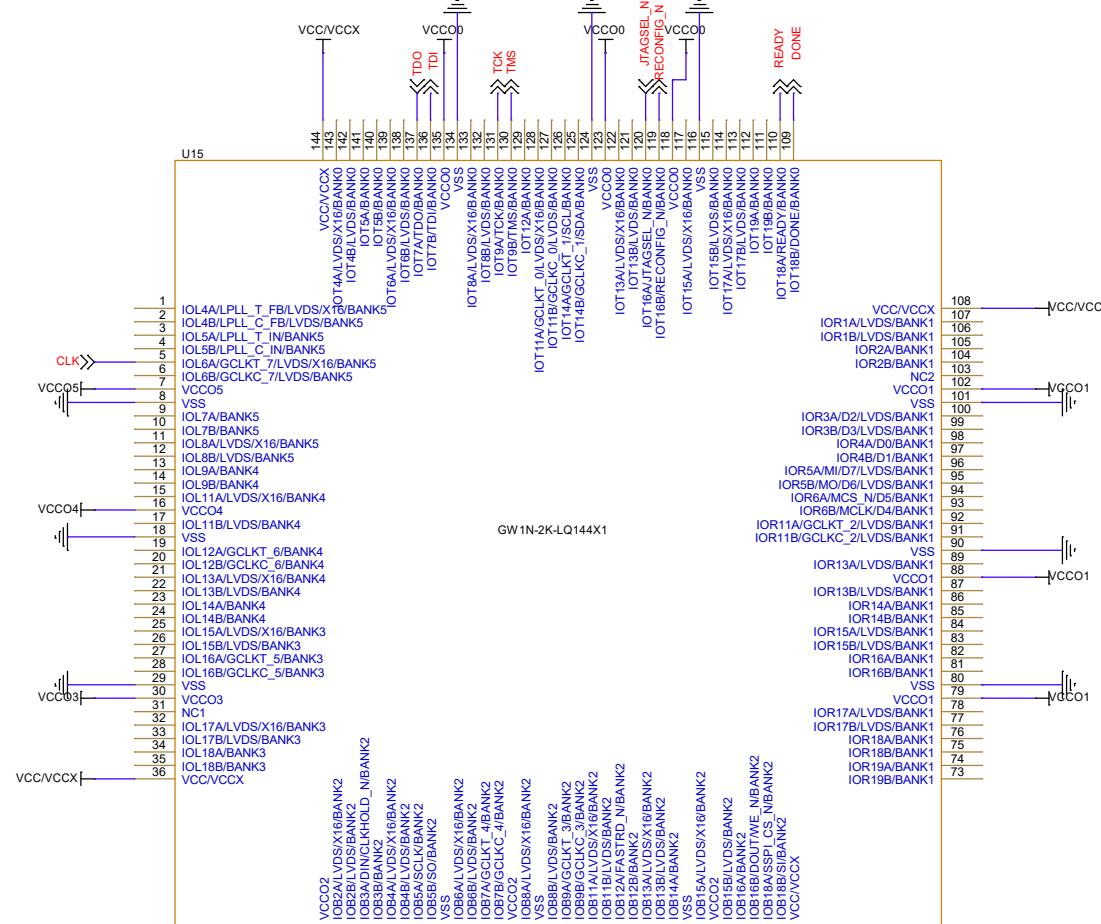
\*Power section (Please refer to the manual for power supply range)



Title	
GW1N Minimum System Diagram	
Size	Document Number
A3	GW1N-UV2-LQ100_1
Date:	Friday, February 03, 2023
Rev	V1.3



Title		GOWIN Minimum System Diagram
Size	Document Number	Rev V1.3
A3	GW1N-LV2-LQ100_2	
Date:	Friday, February 03, 2023	Sheet 7 of 31



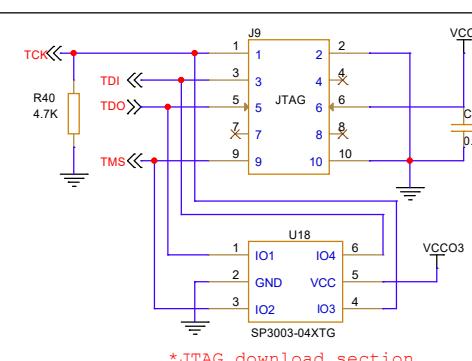
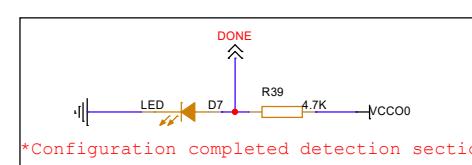
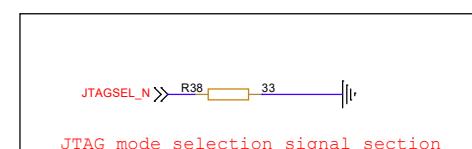
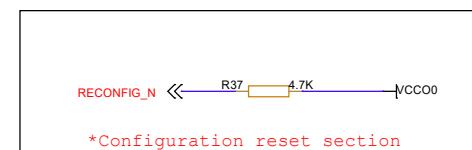
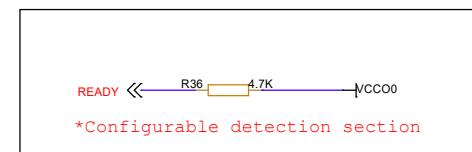
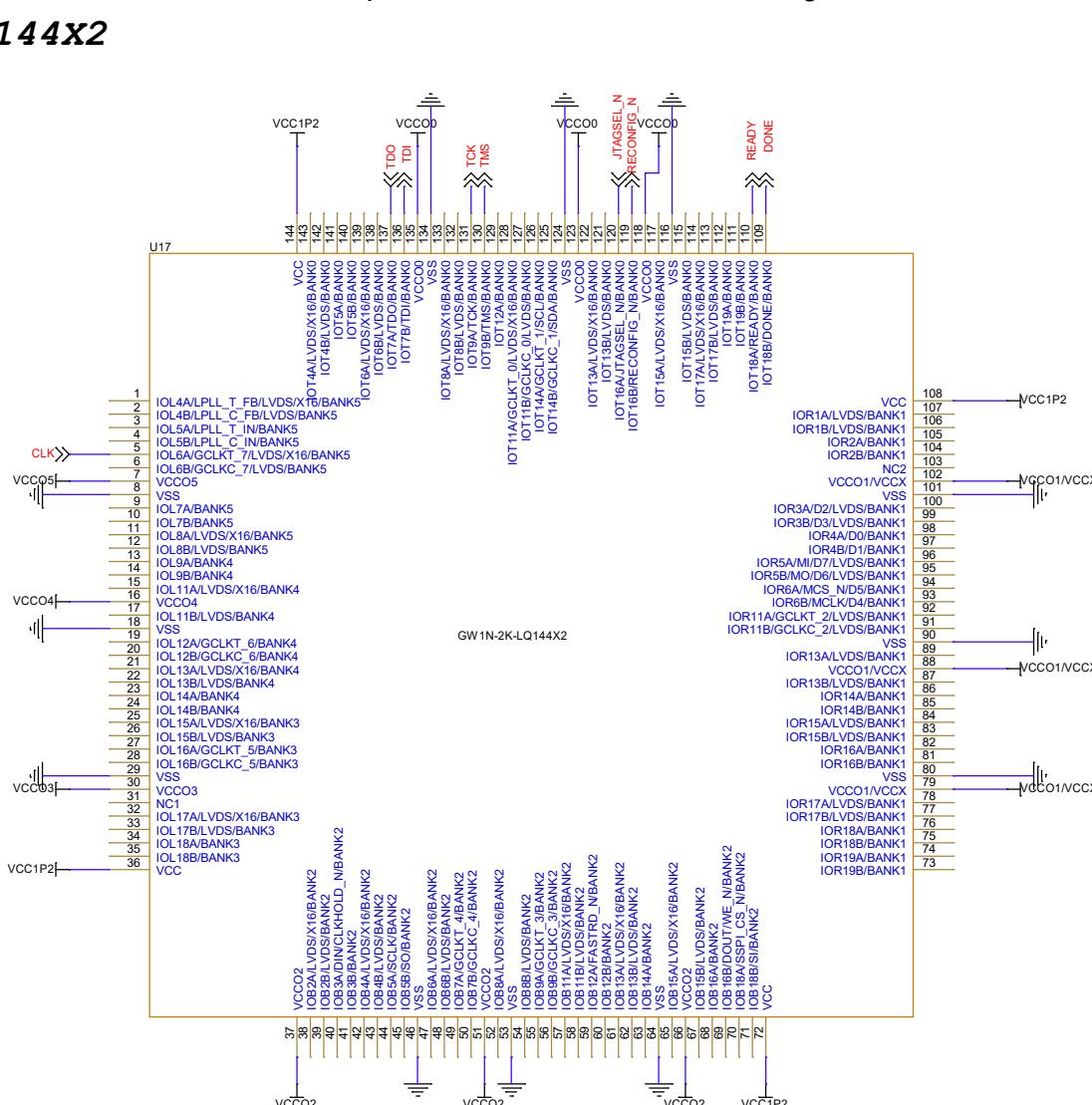
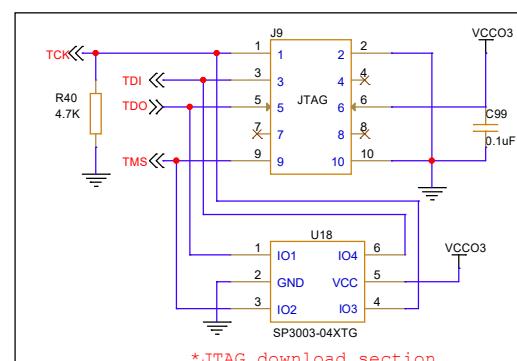
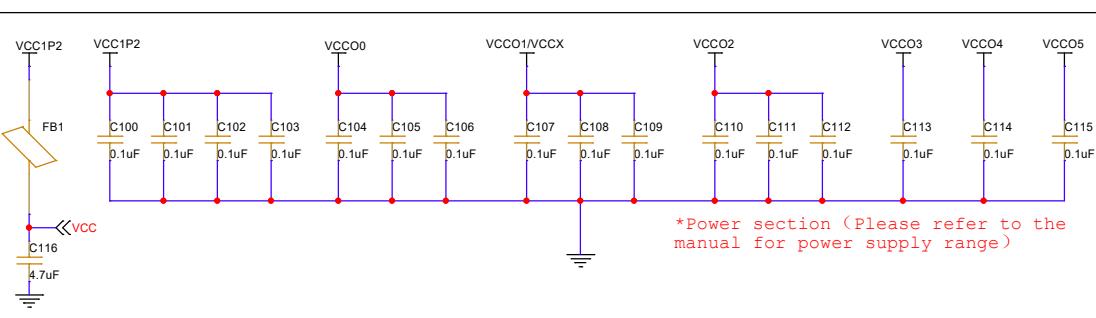
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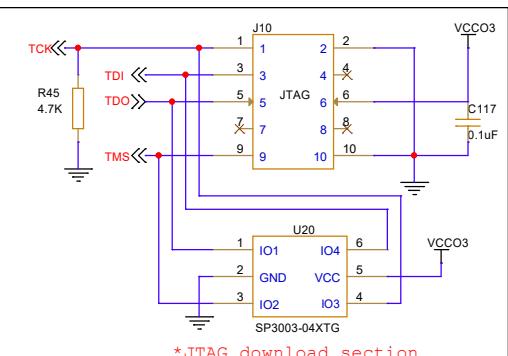
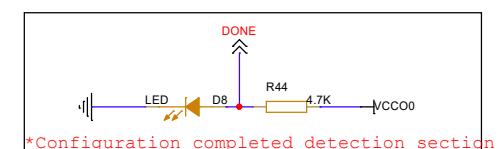
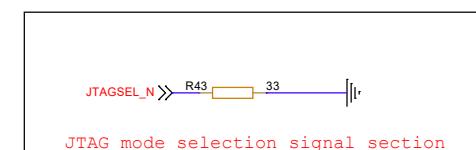
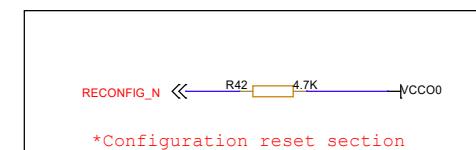
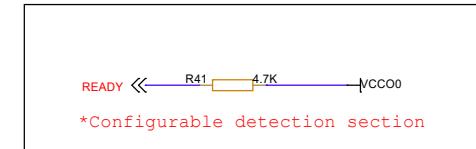
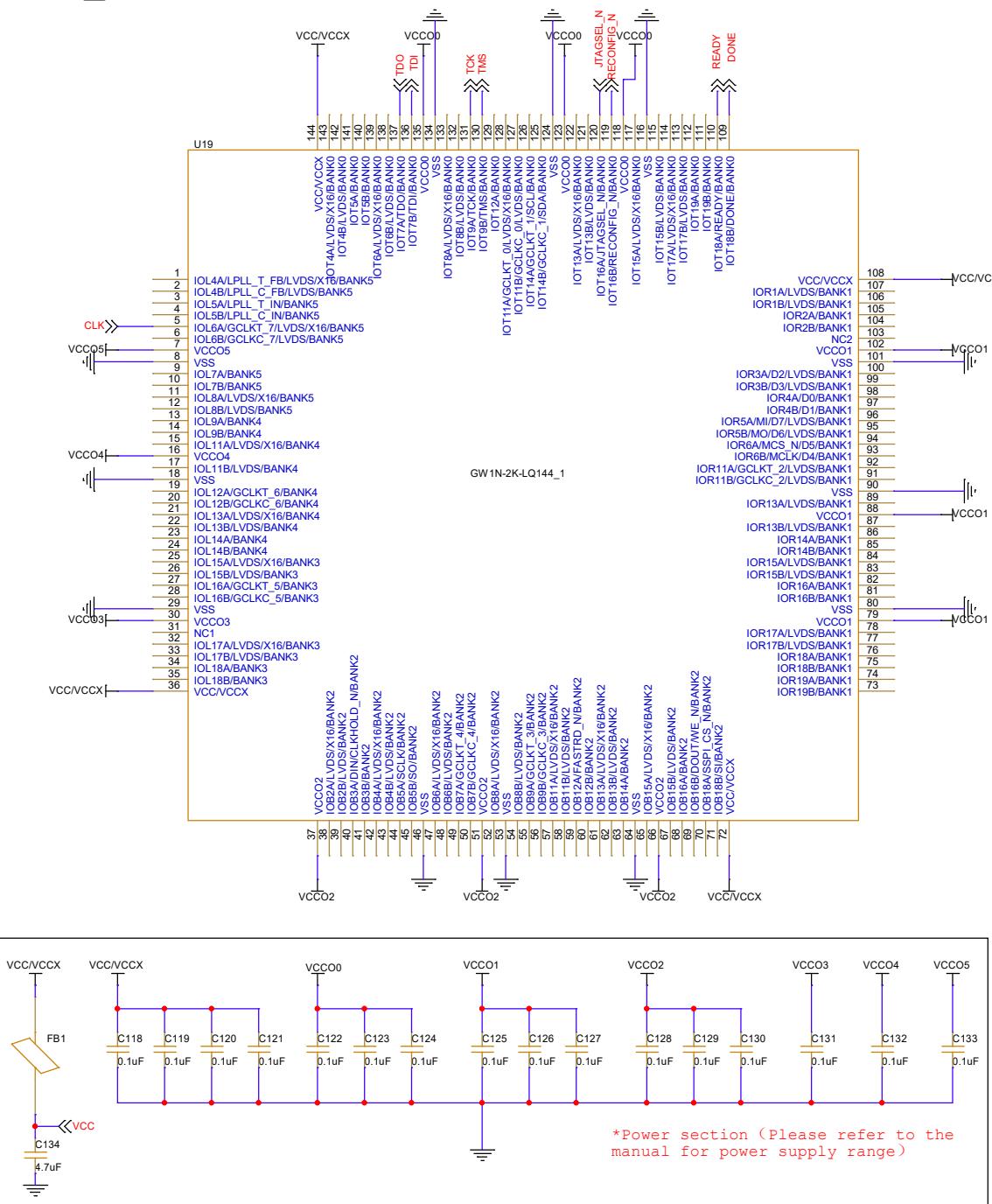
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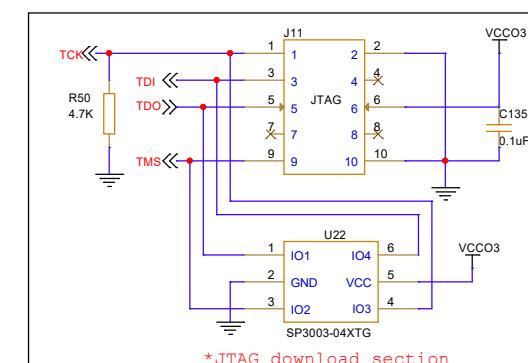
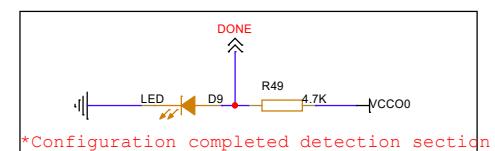
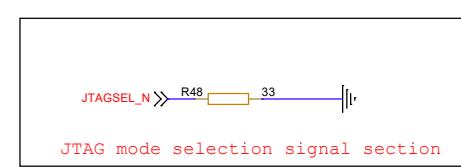
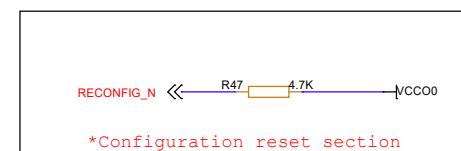
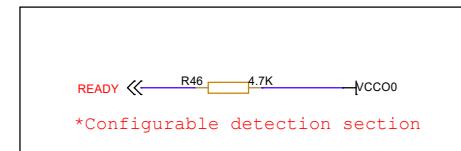
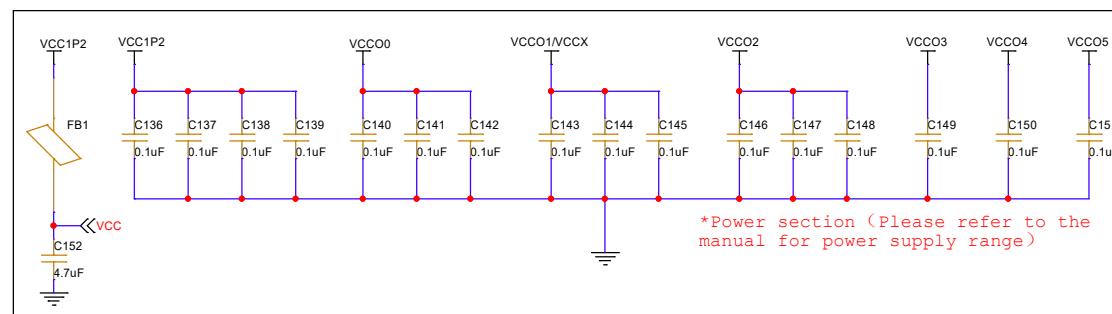
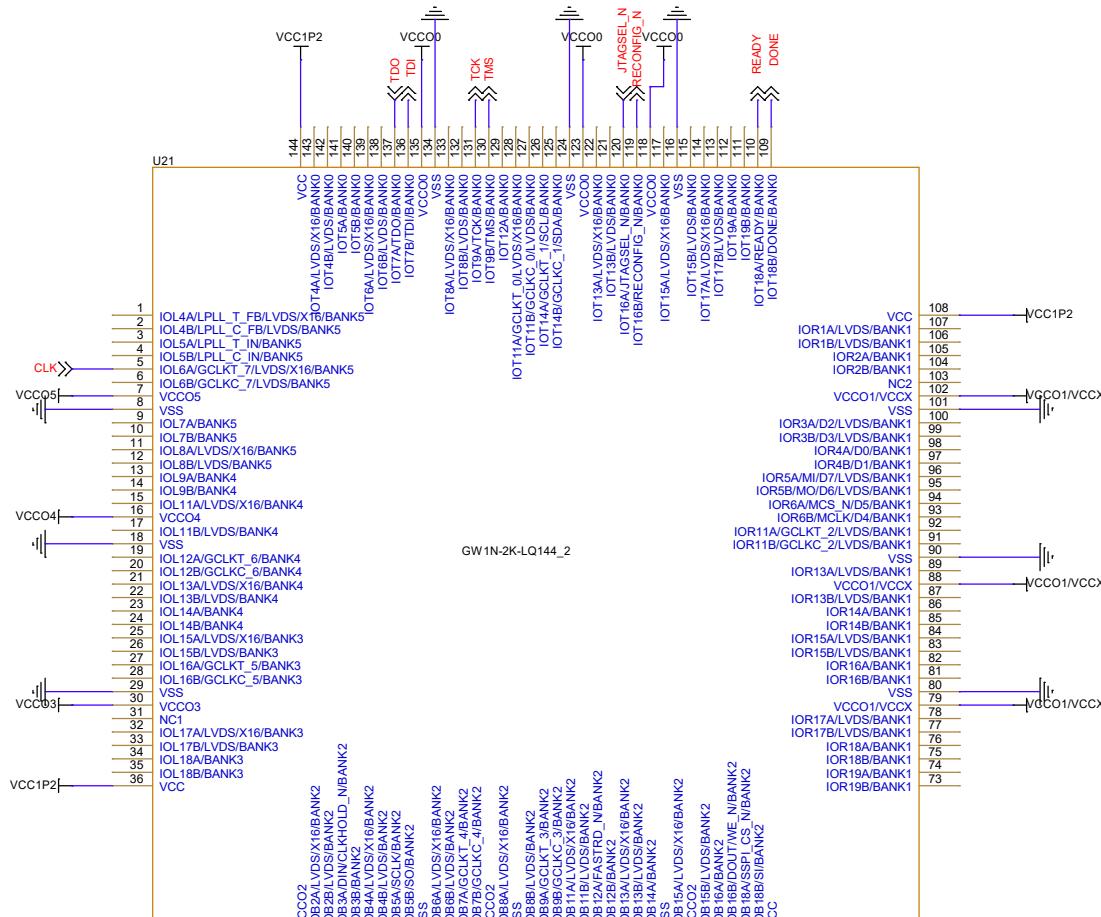
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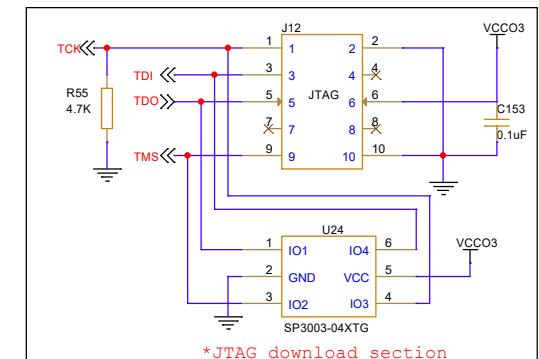
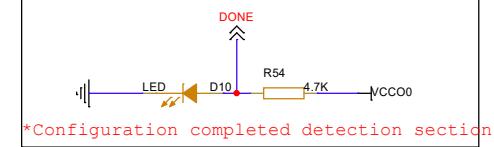
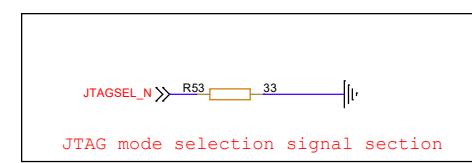
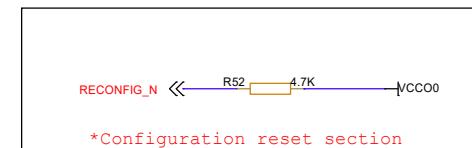
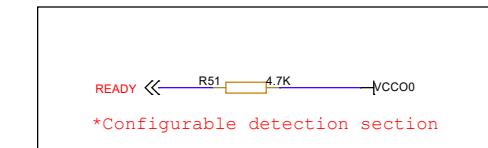
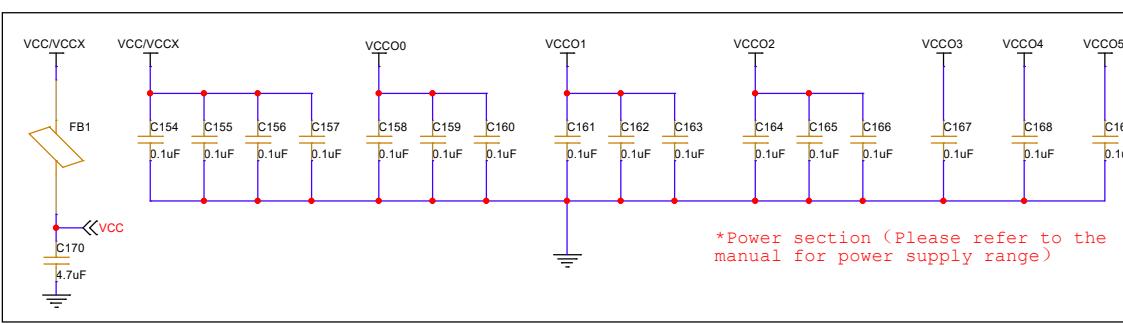
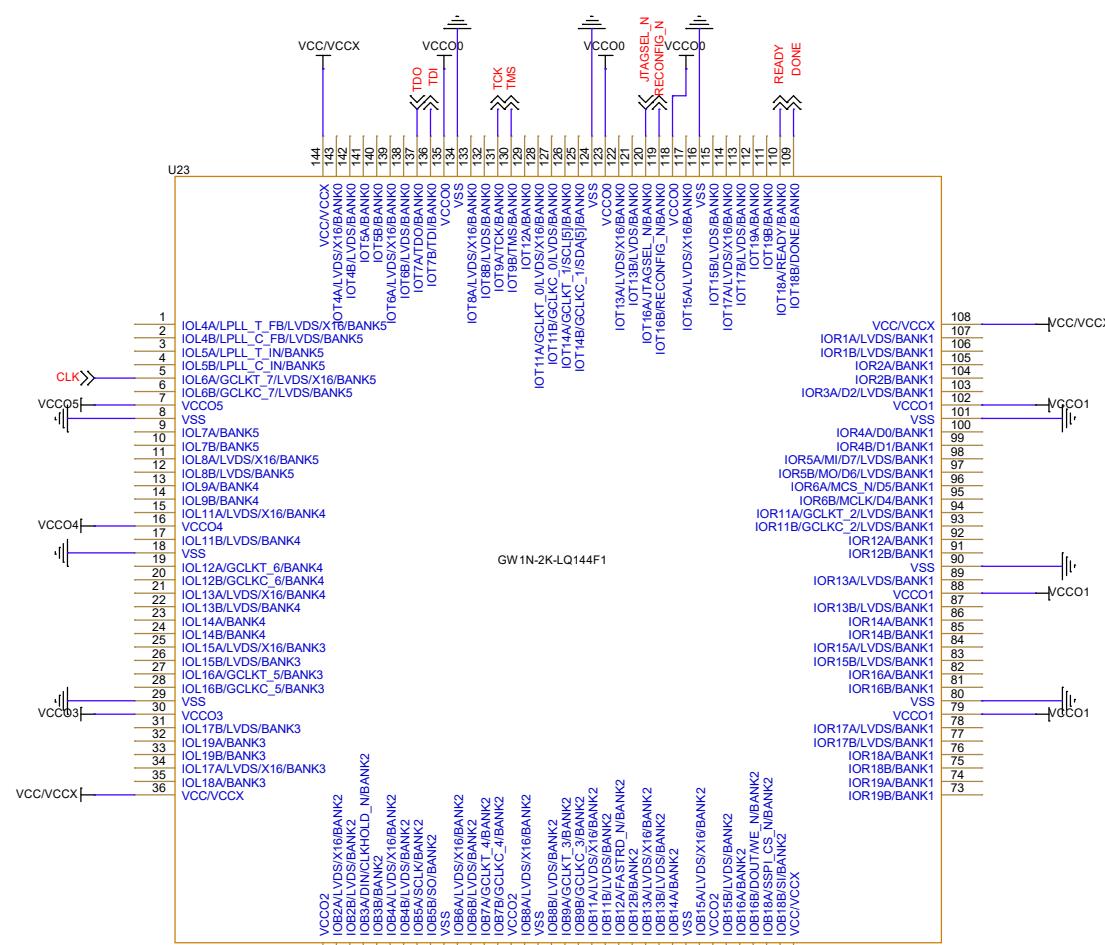
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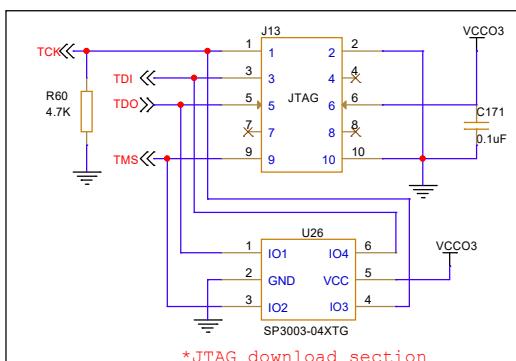
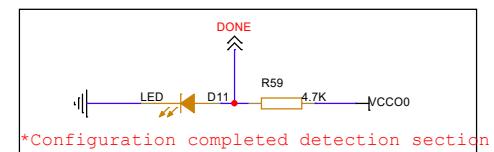
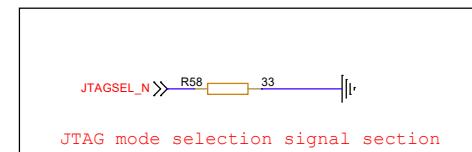
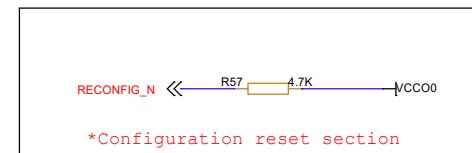
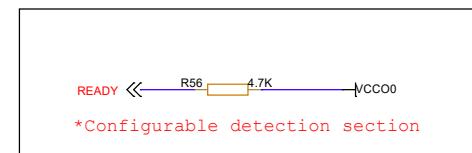
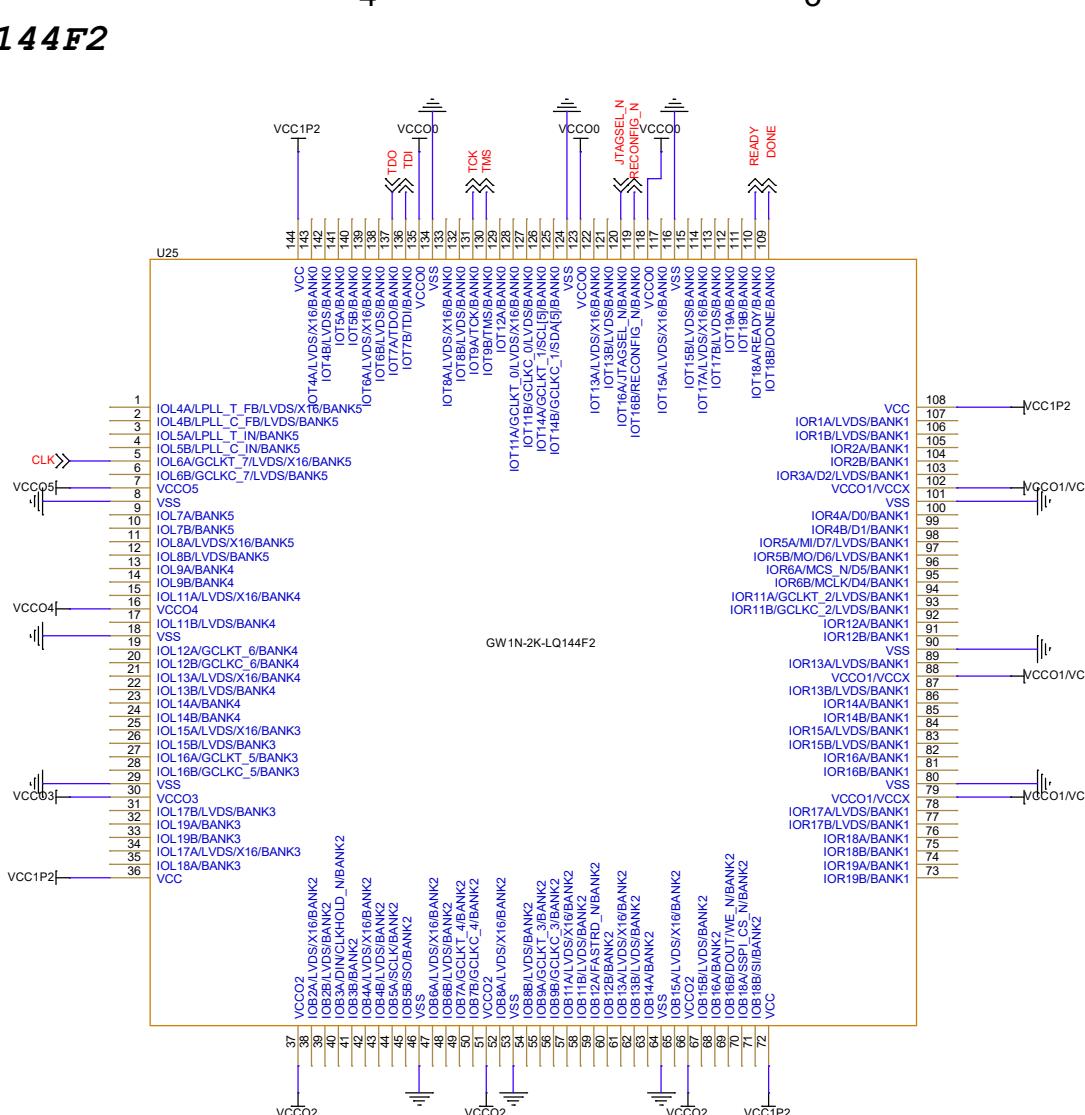
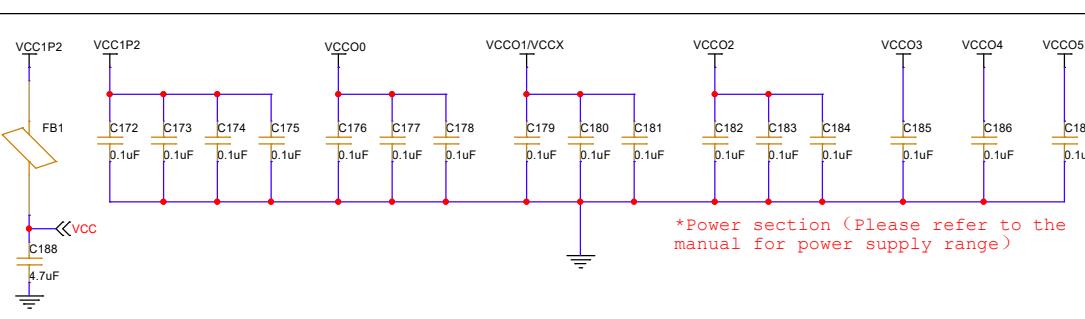
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D

C

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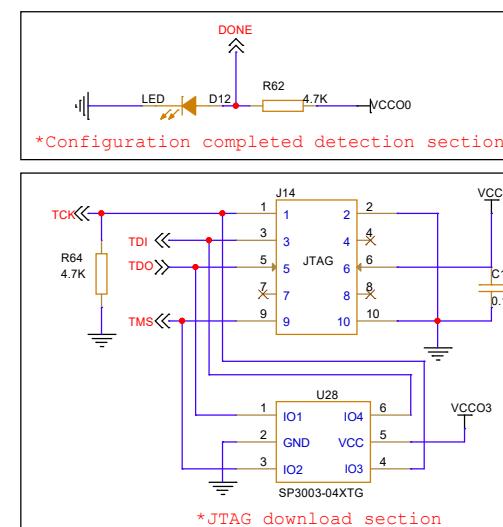
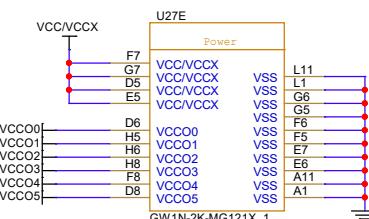
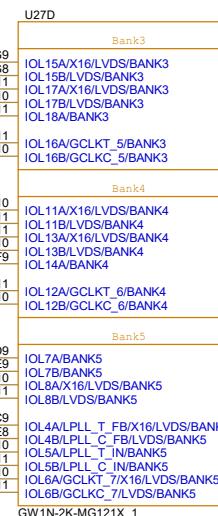
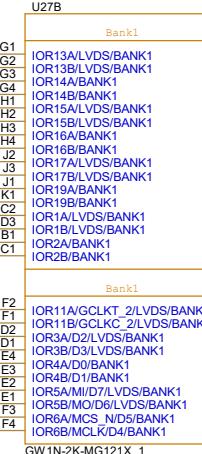
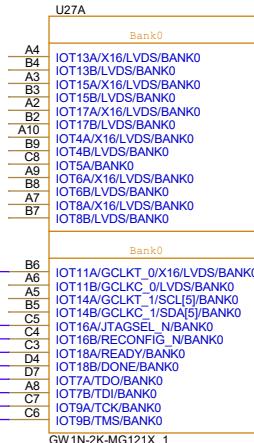
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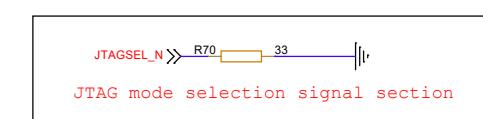
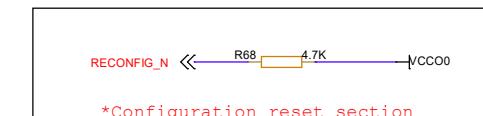
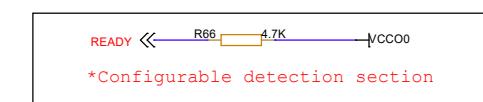
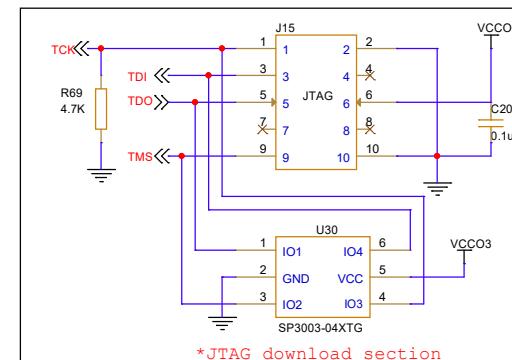
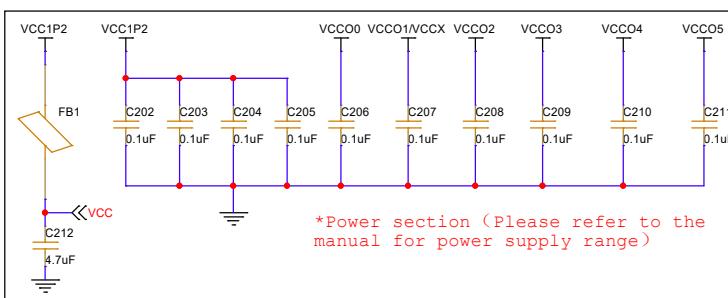
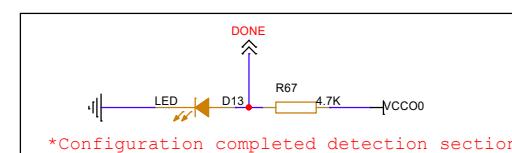
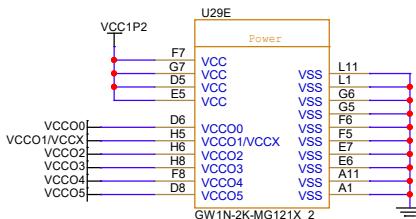
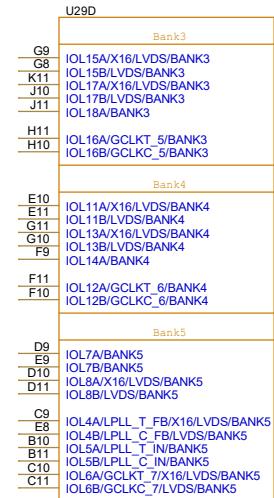
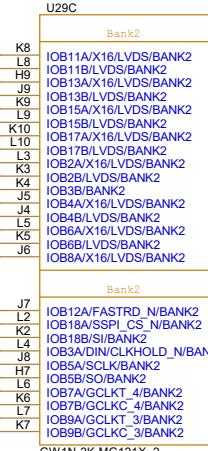
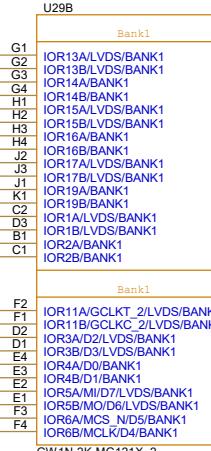
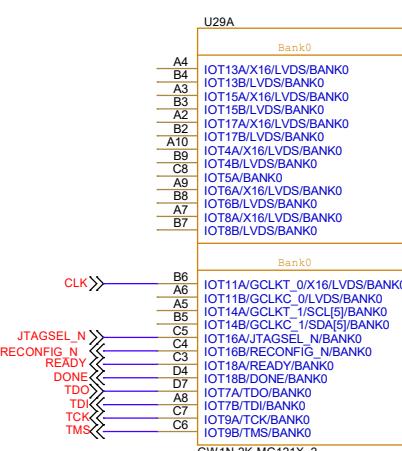
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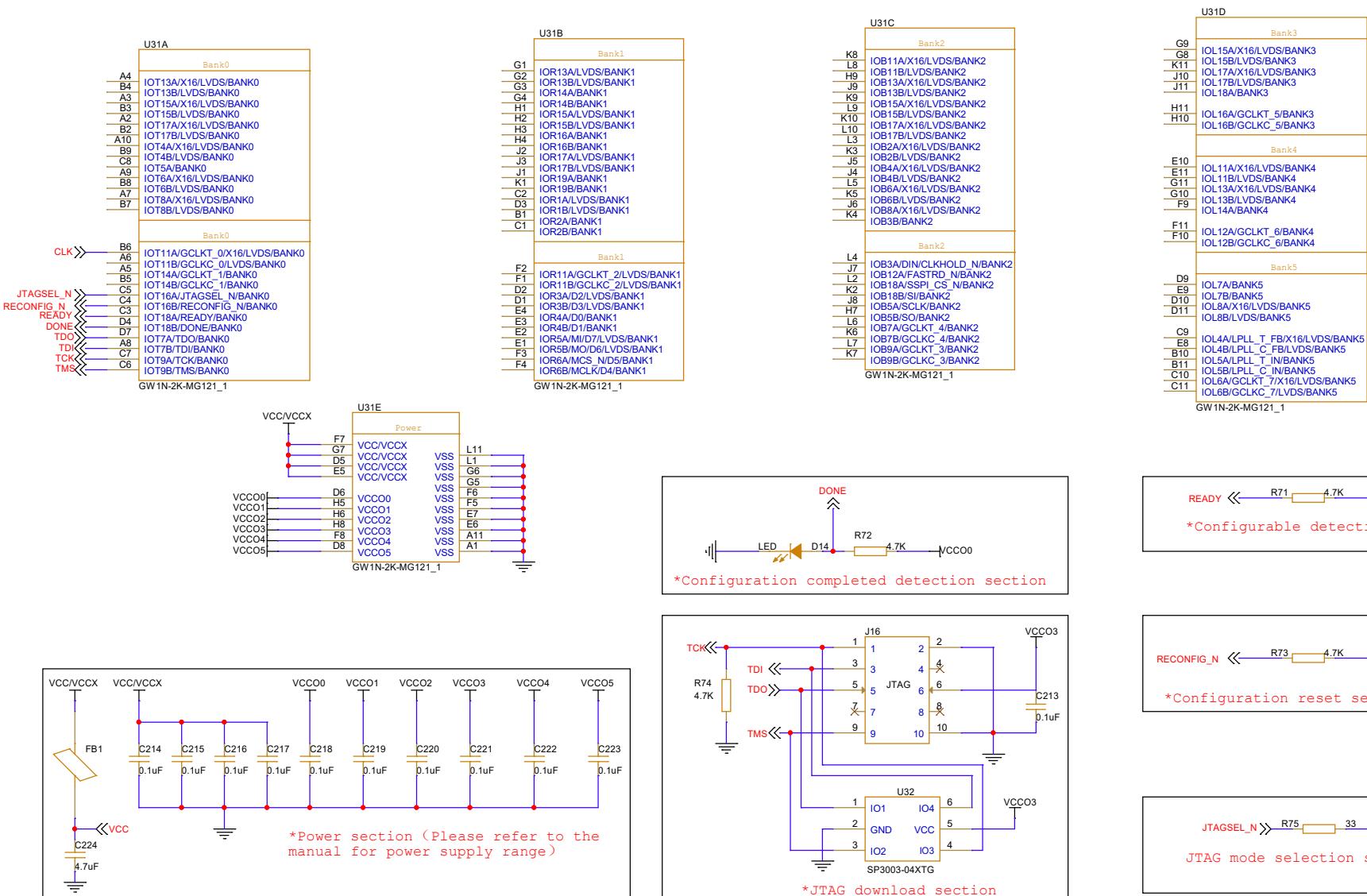
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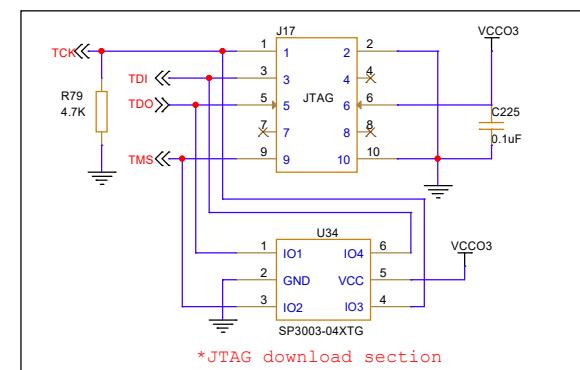
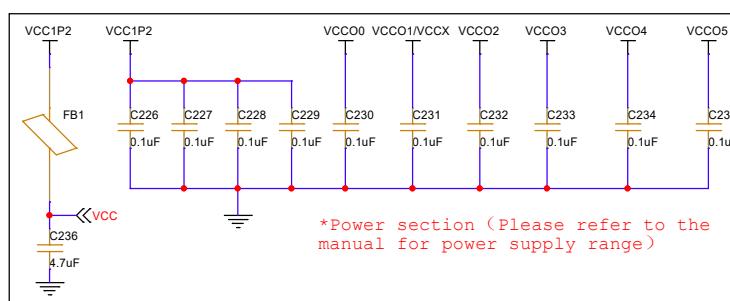
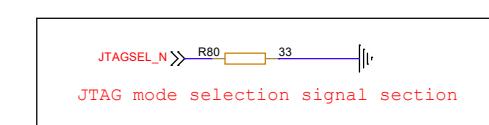
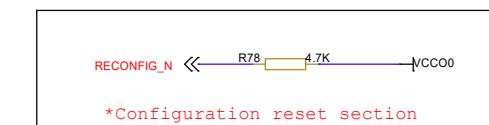
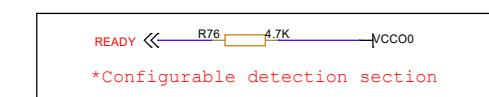
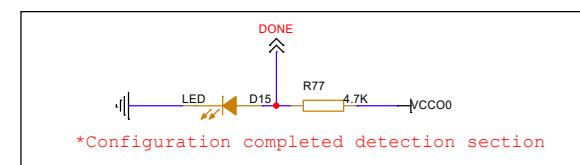
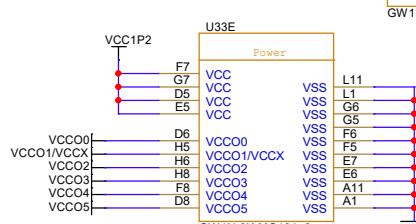
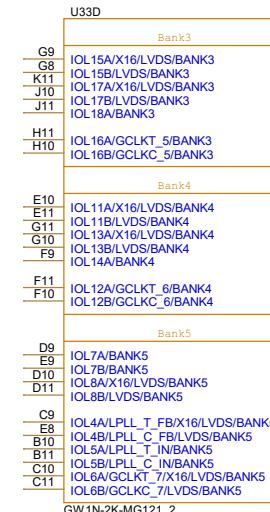
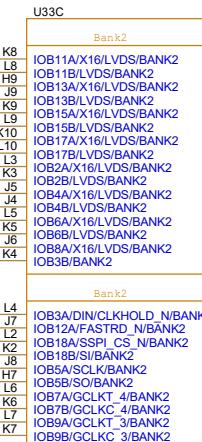
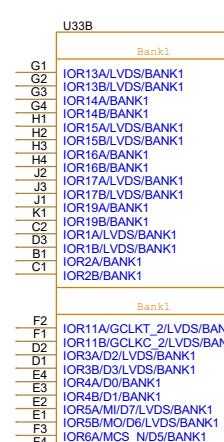
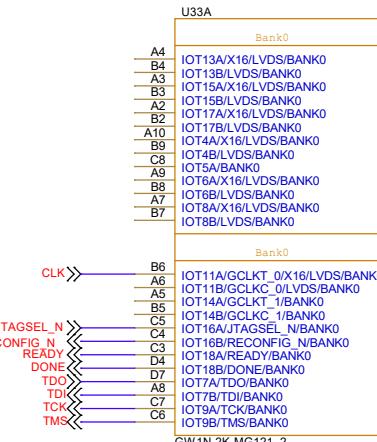
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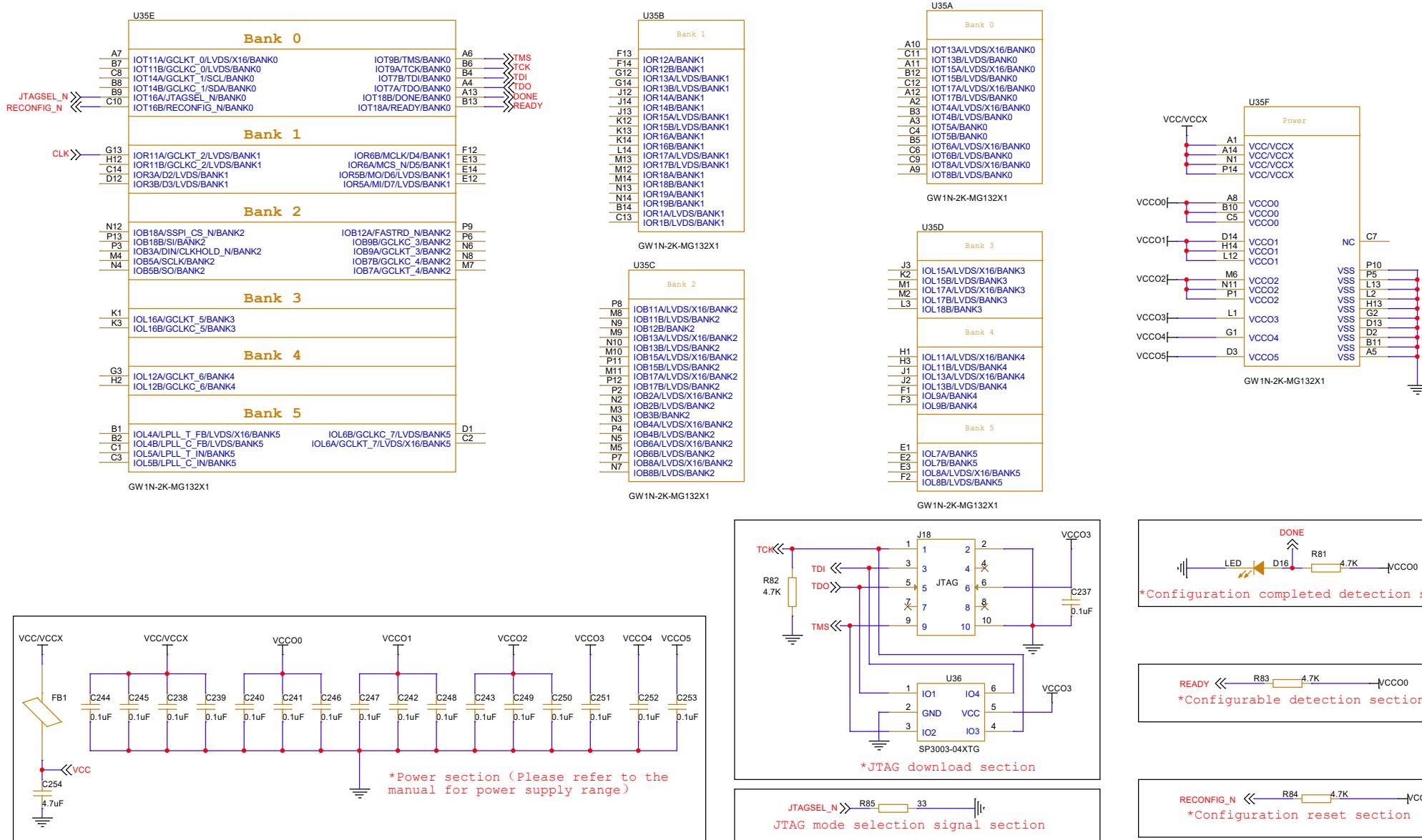


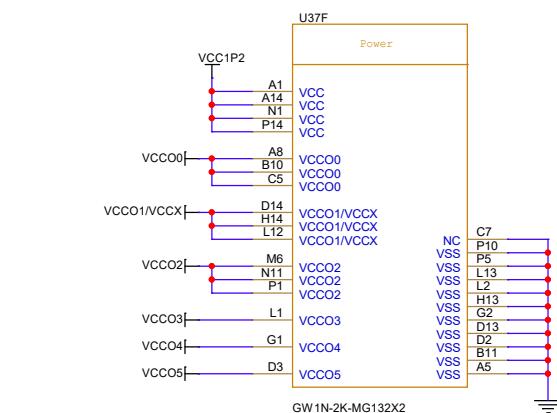
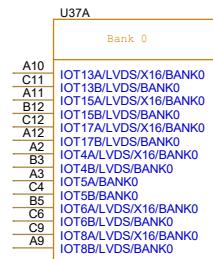
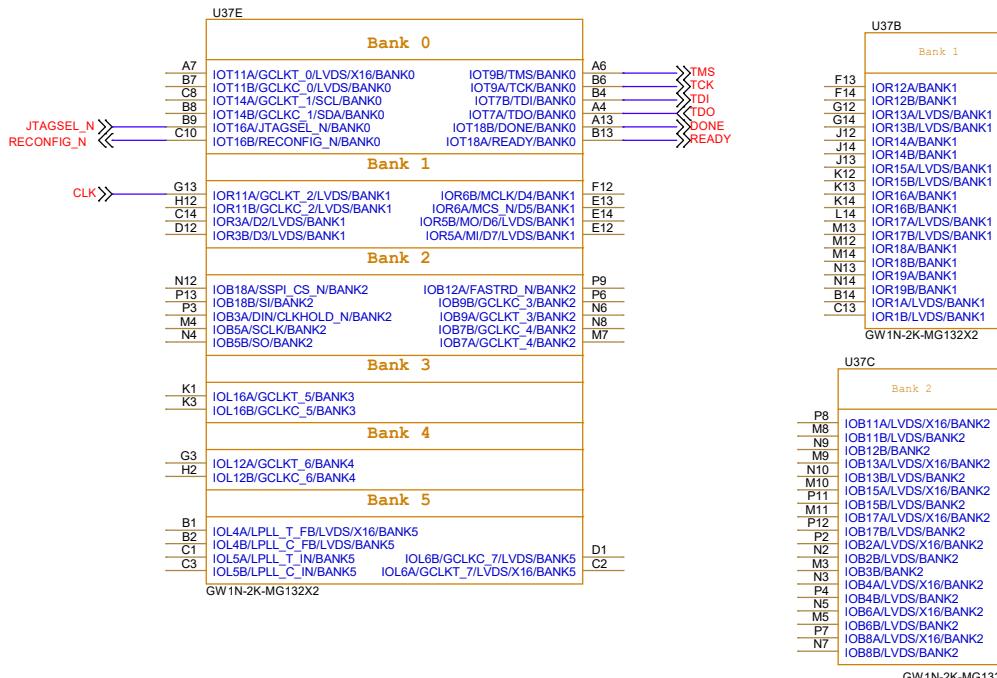
**GW1N-LV2-MG121\_2**

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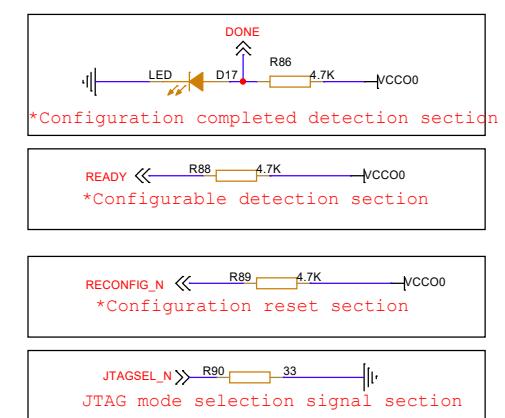
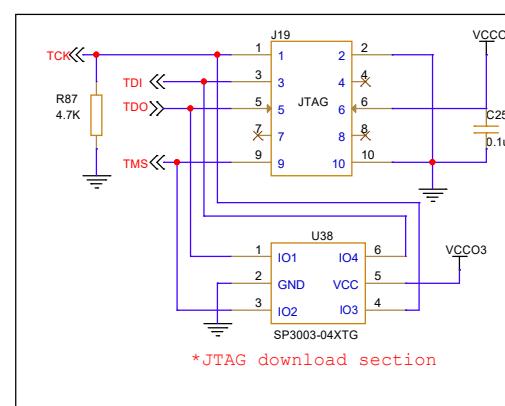
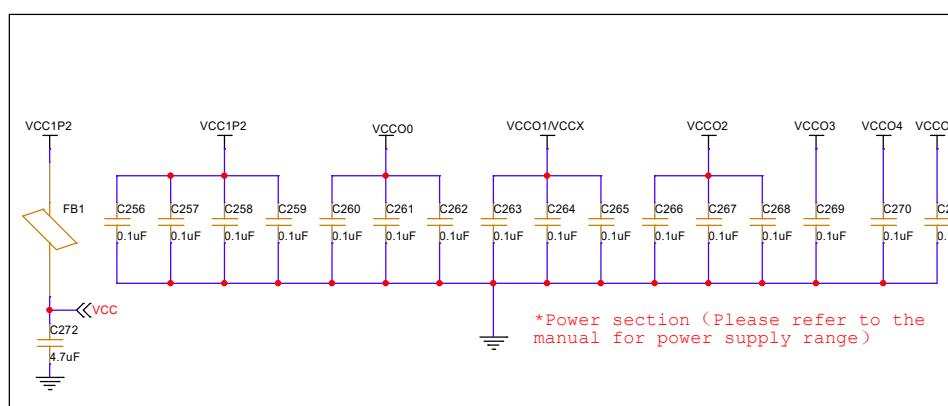


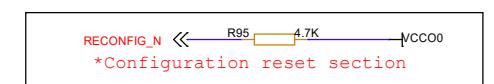
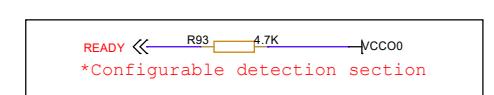
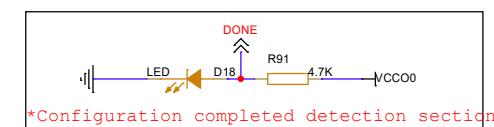
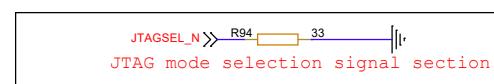
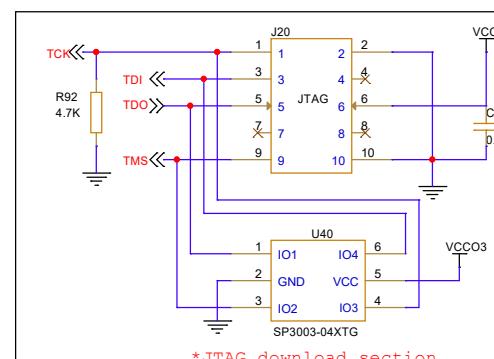
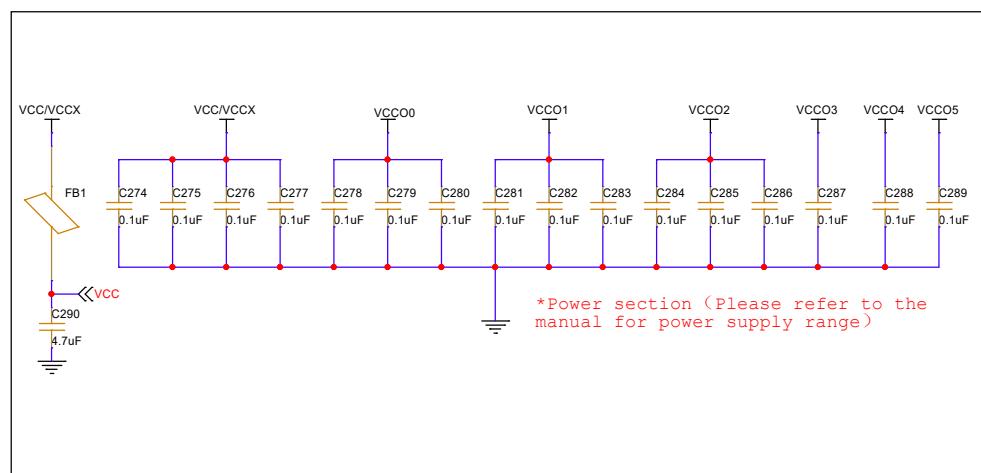
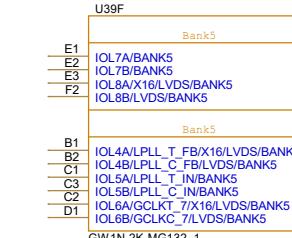
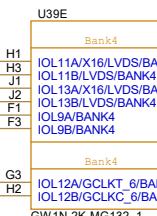
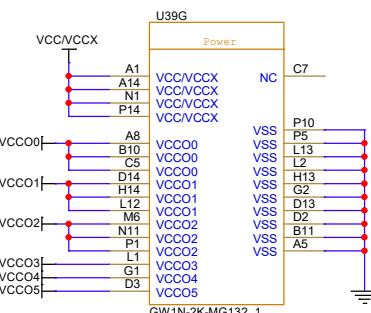
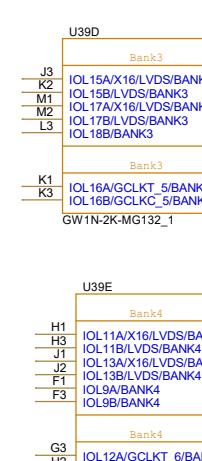
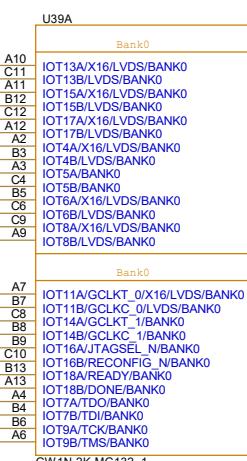
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Size	Document Number	Rev V1.3
A3	GW1N-LV2-MG121_2	
Date:	Friday, February 03, 2023	Sheet 17 of 31

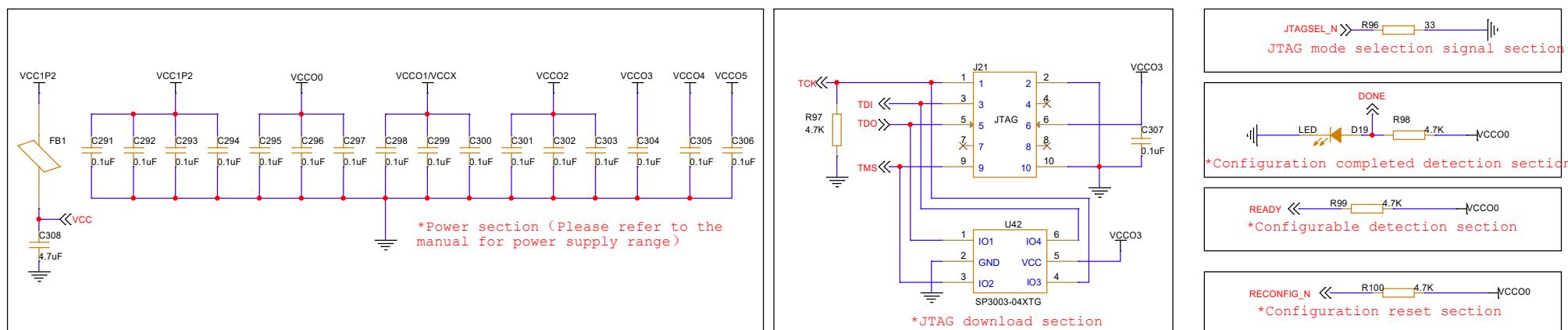
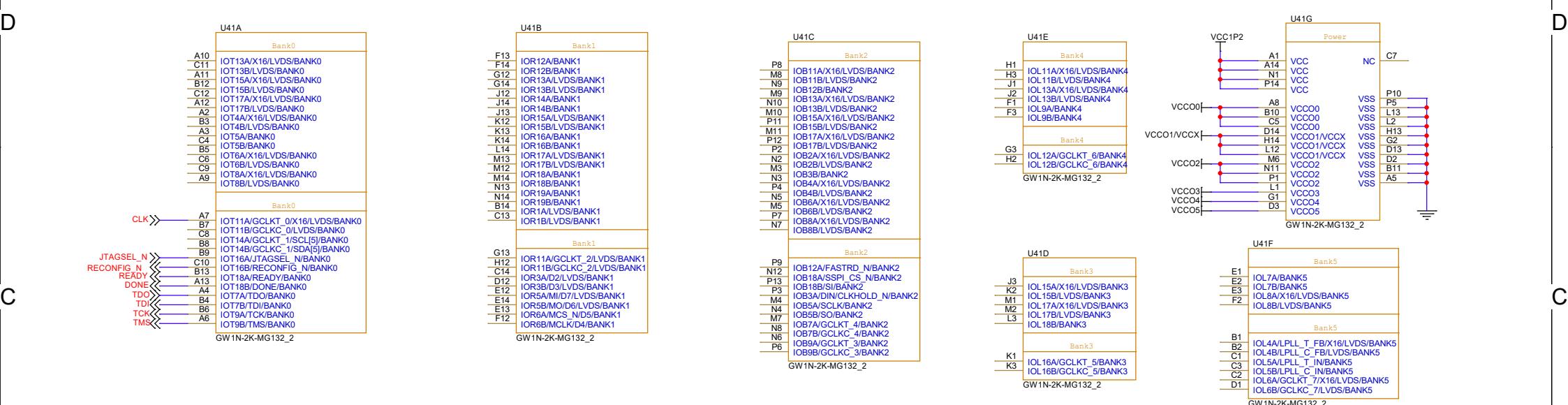


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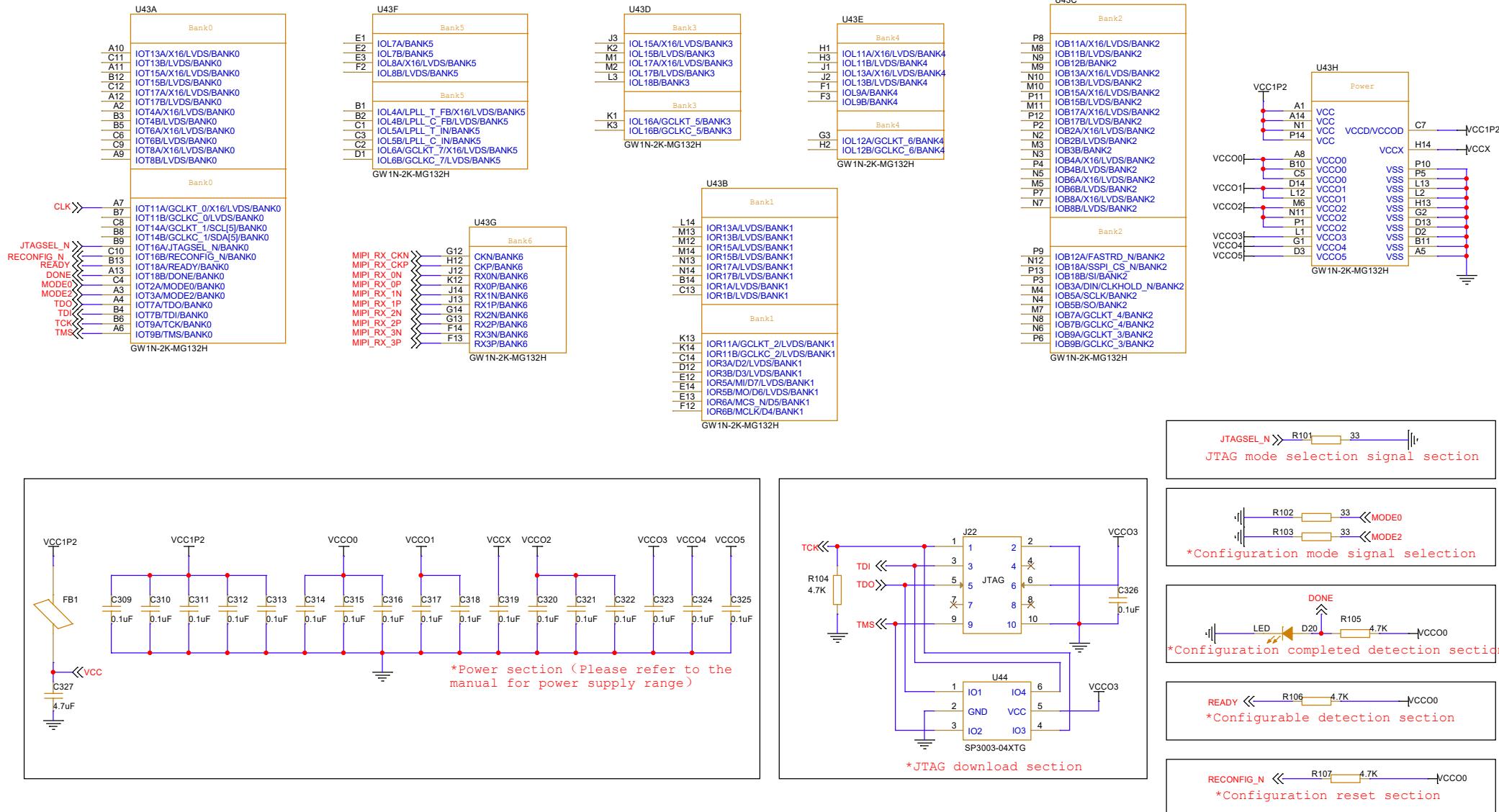
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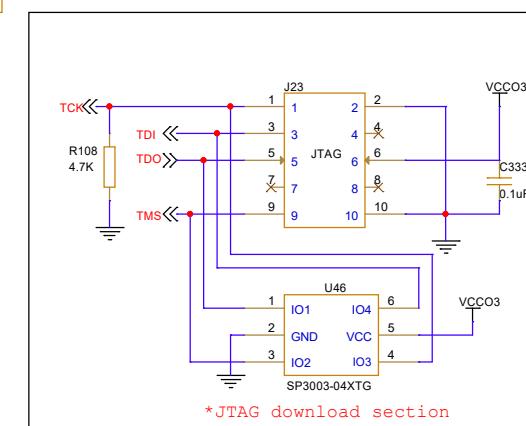
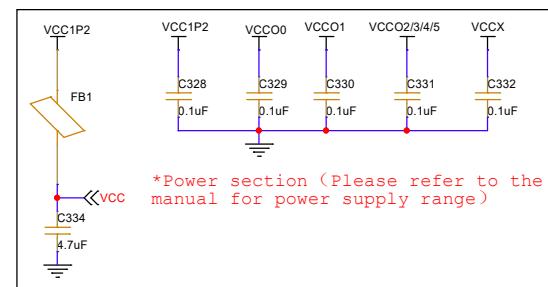
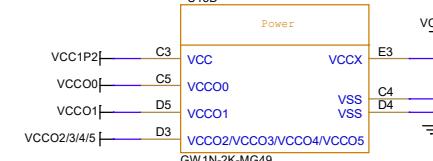
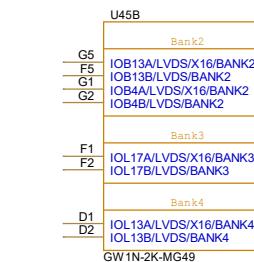
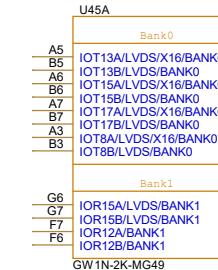
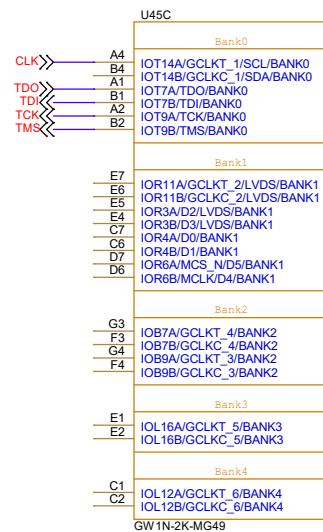




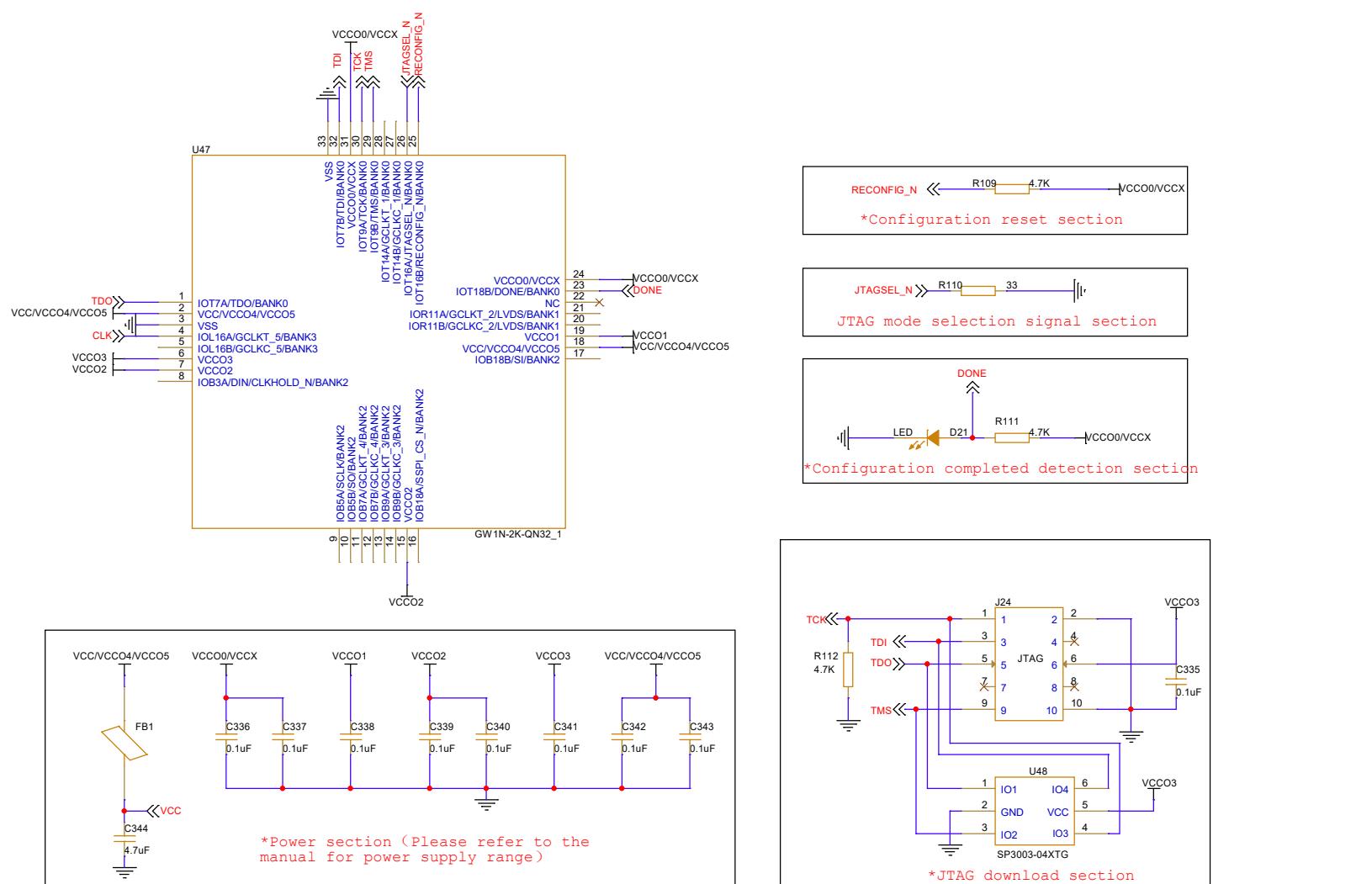
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GOWIN Minimum System Diagram		
Size	Document Number	Rev
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Date:	Friday, February 03, 2023	

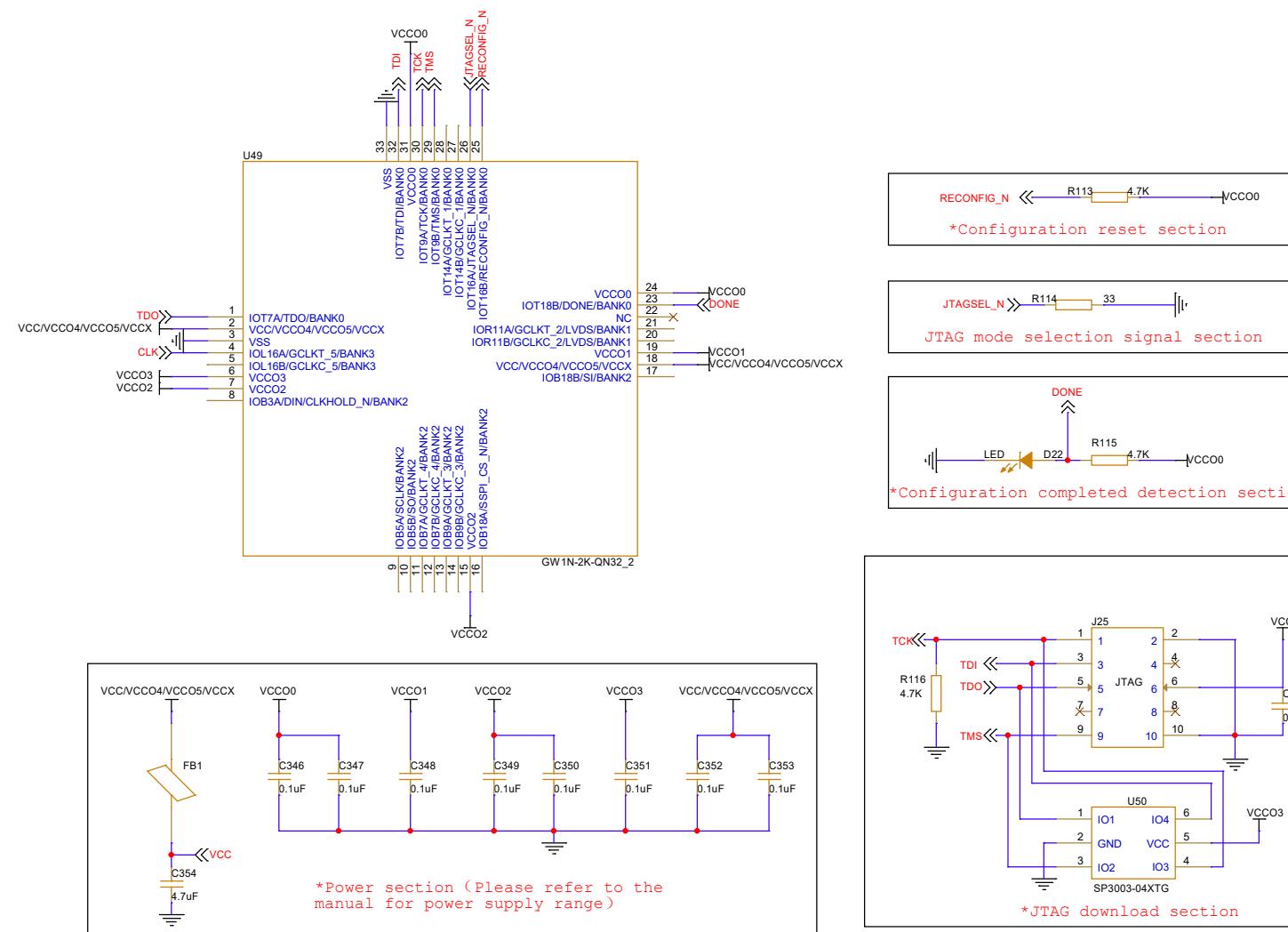
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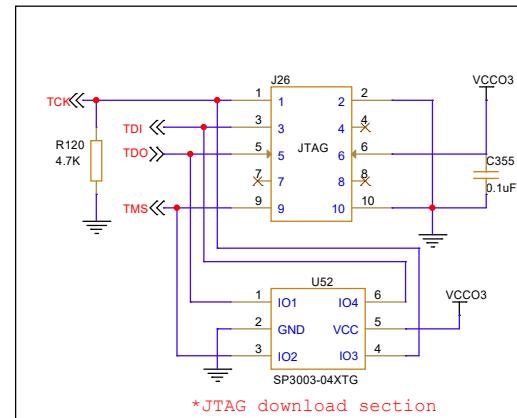
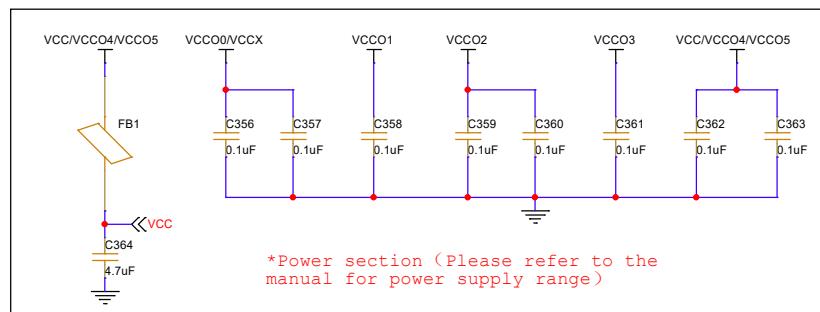
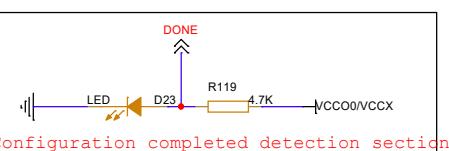
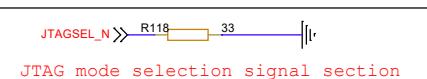
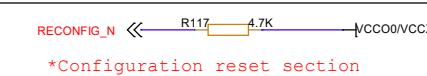
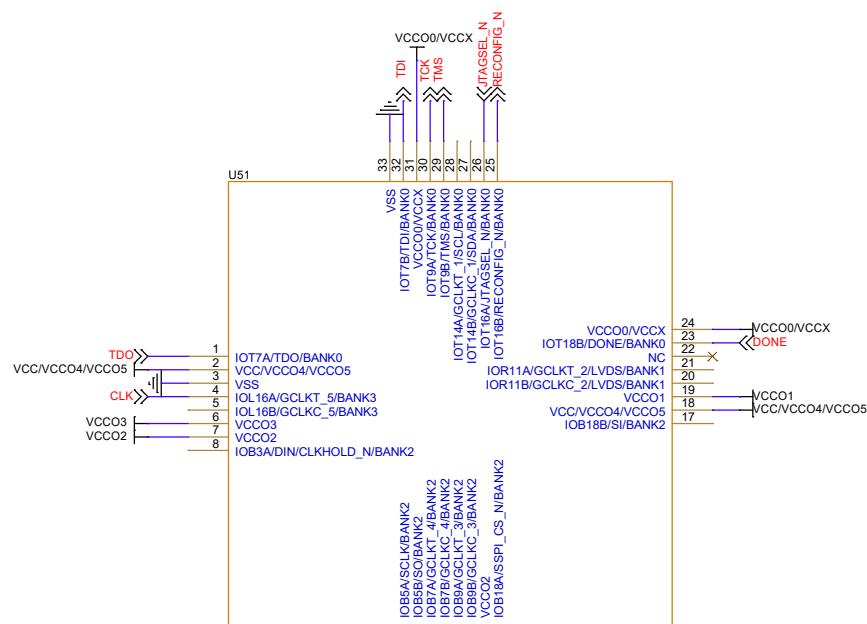


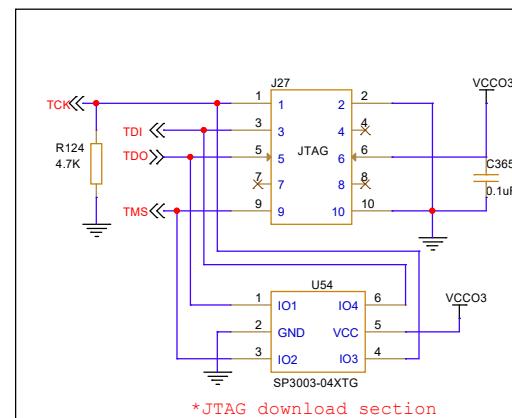
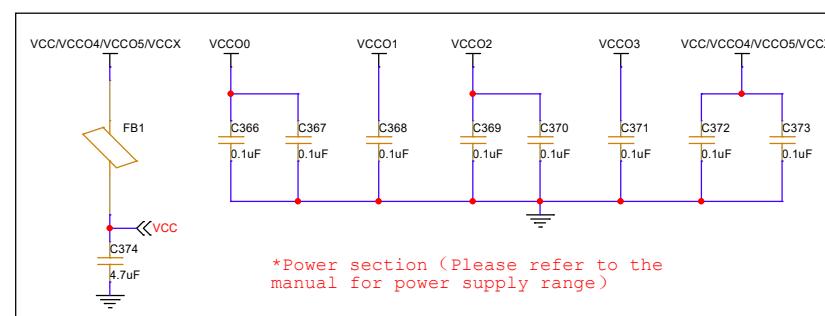
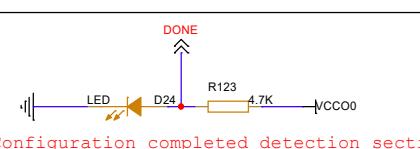
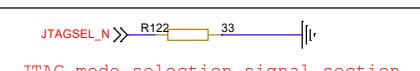
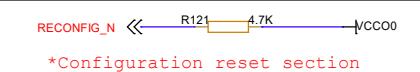
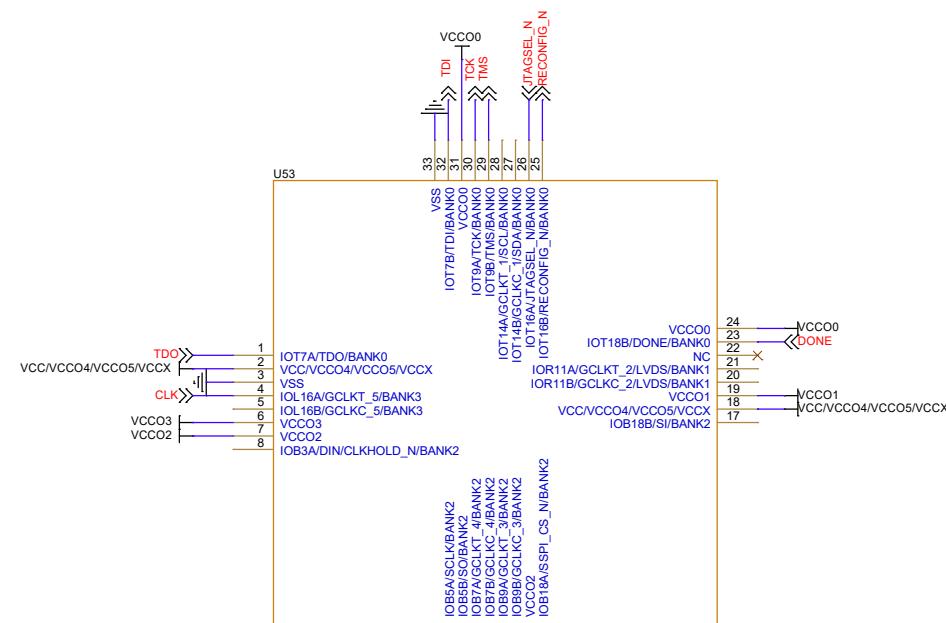
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Size: A3	Document Number: GW1N-LV2-MG49	Rev: V1.3
Date: Friday, February 03, 2023	Sheet: 23	of: 31

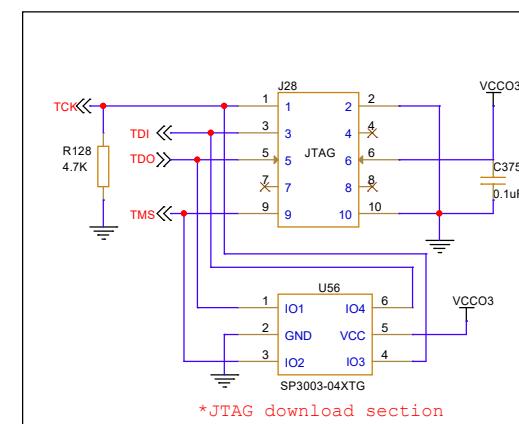
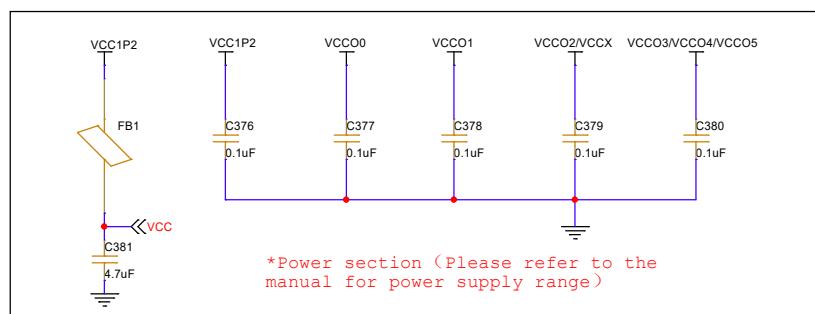
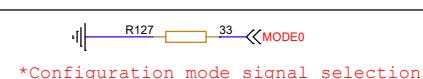
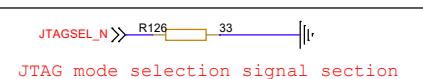
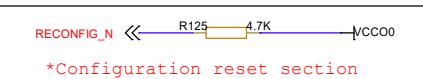
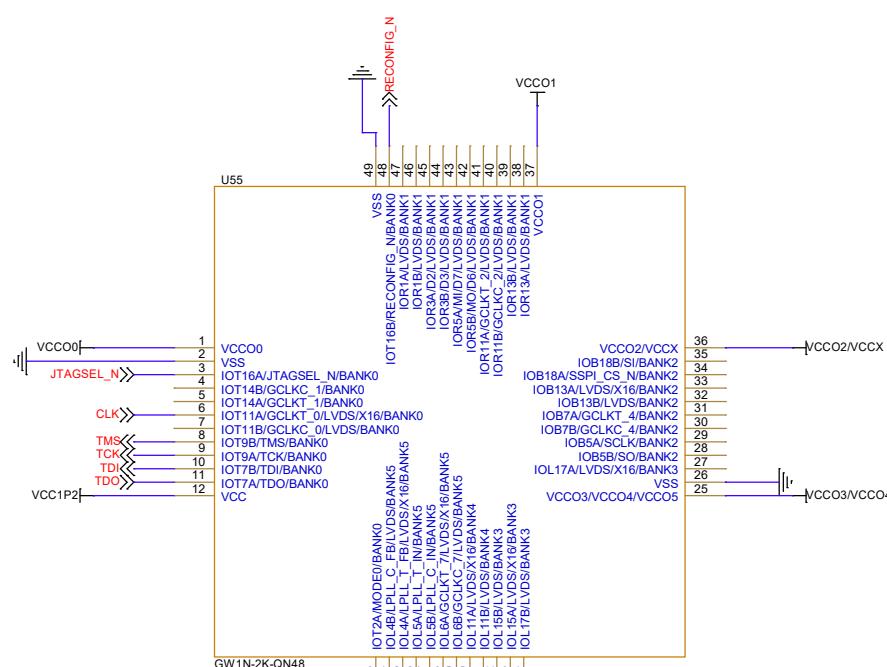
***GW1N-LV2-QN32\_1***

Title	
GOWIN Minimum System Diagram	
Size	Document Number
A3	GW1N-LV2-QN32_1
Date:	Rev
Friday, February 03, 2023	V1.3
Sheet	24 of 31

**GW1N-UV2-QN32\_2**







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