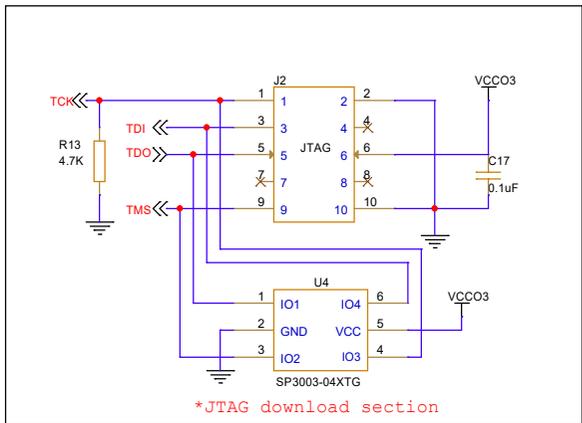
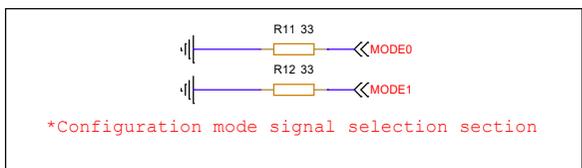
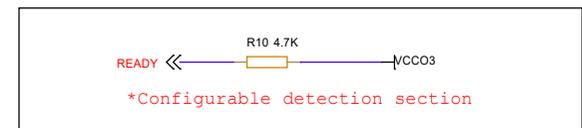
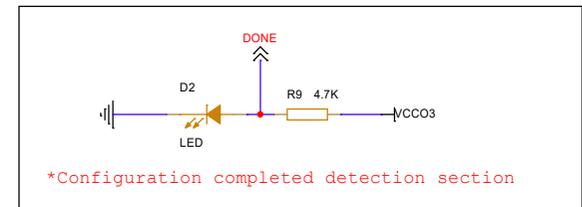
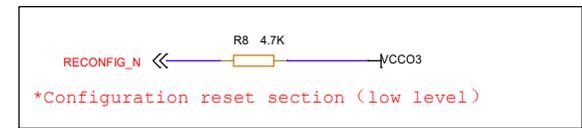
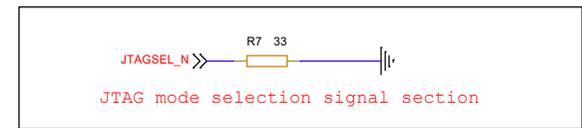
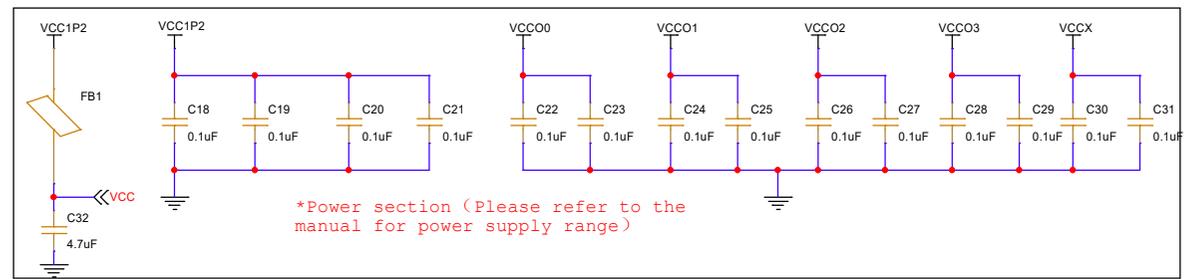
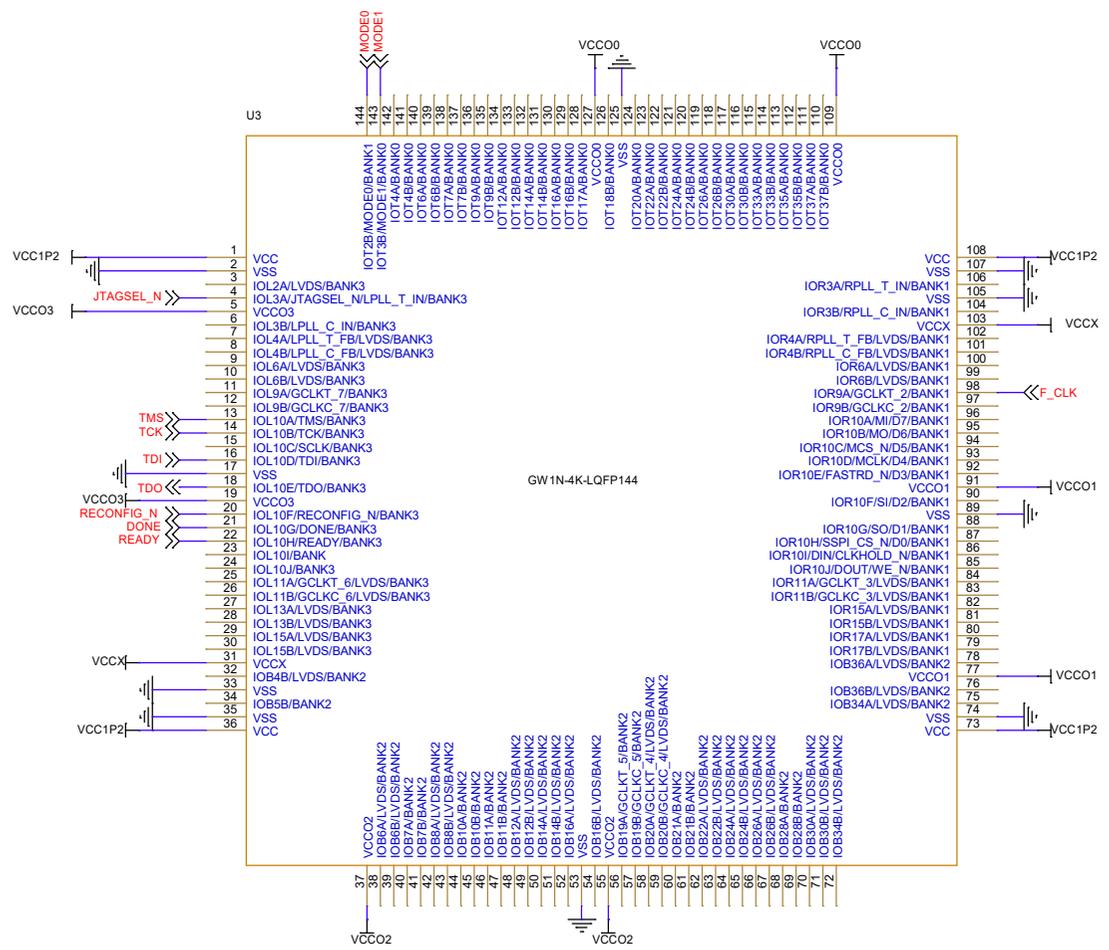


GW1N-LV4-LQ144

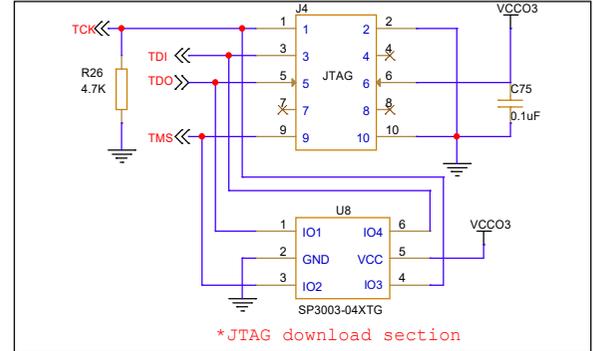
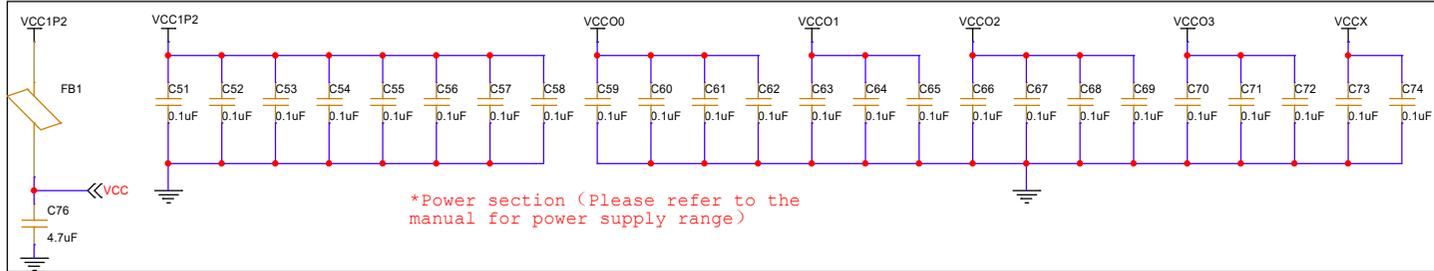
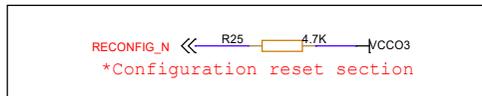
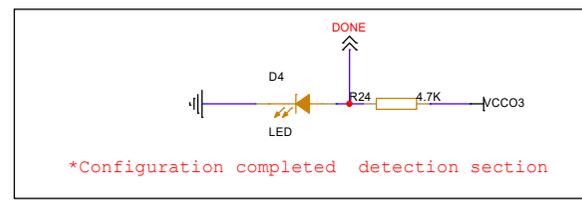
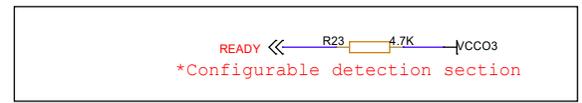
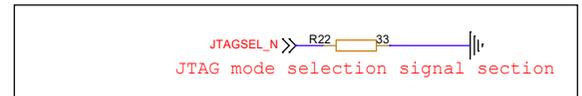
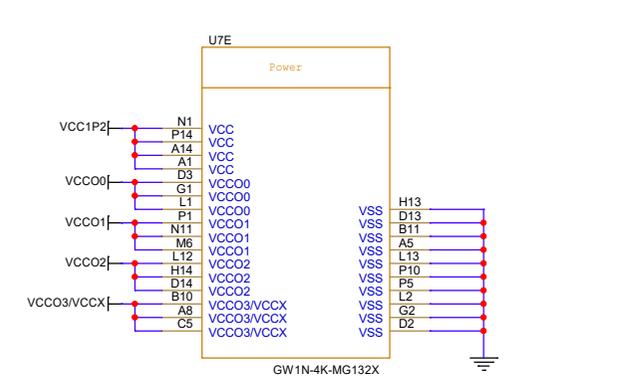
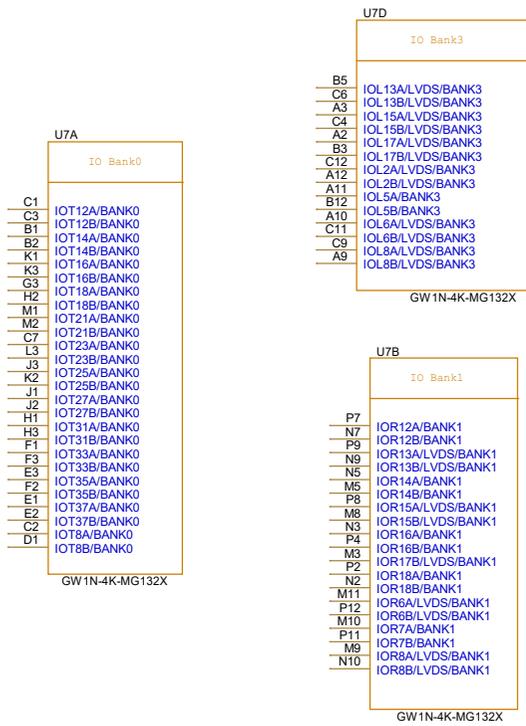
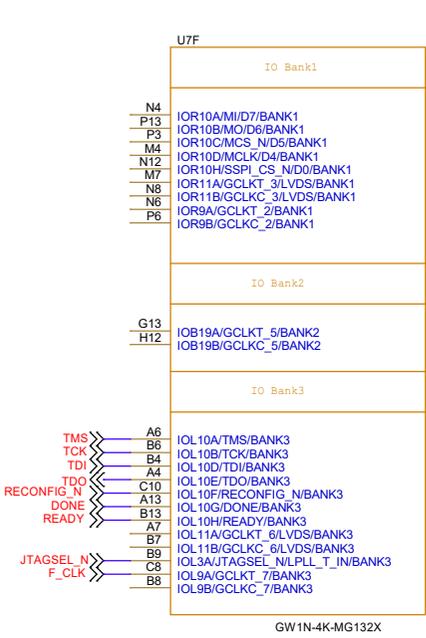


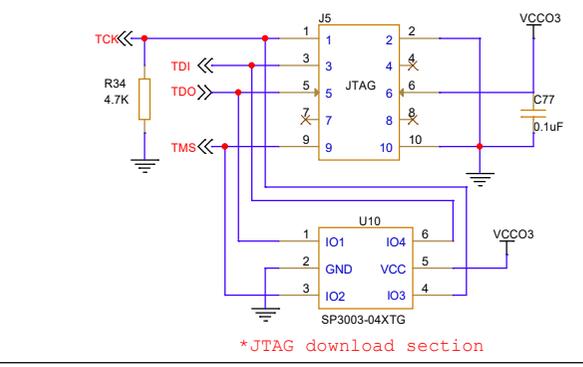
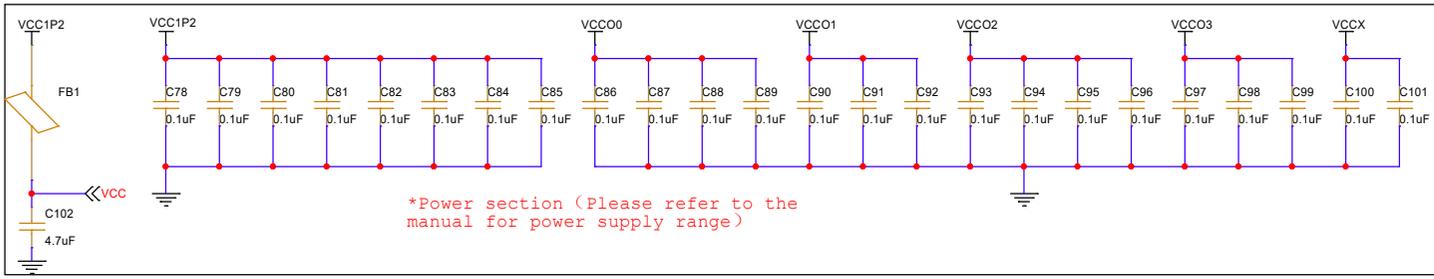
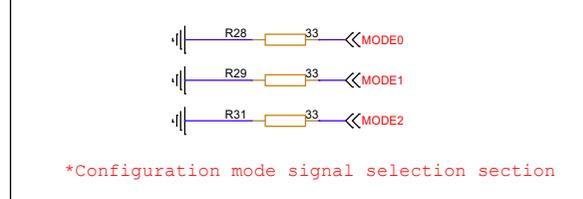
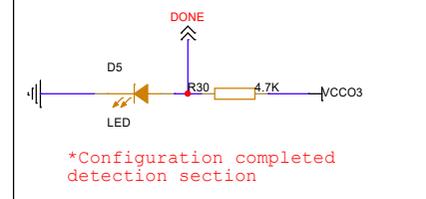
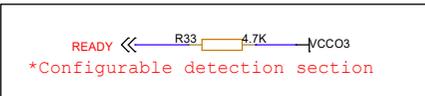
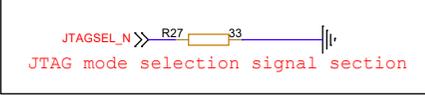
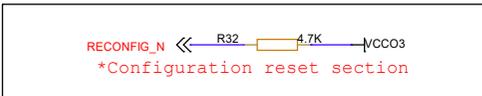
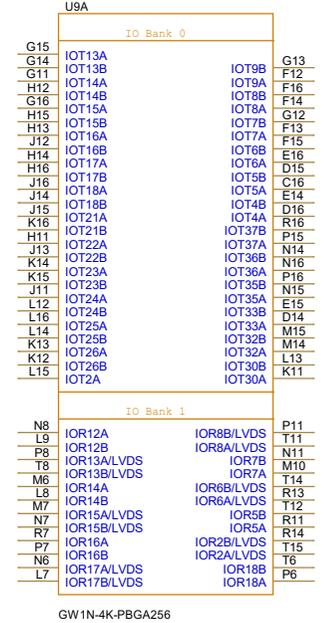
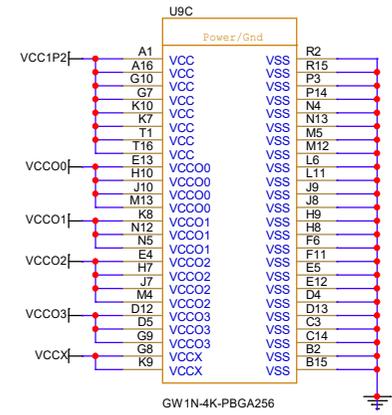
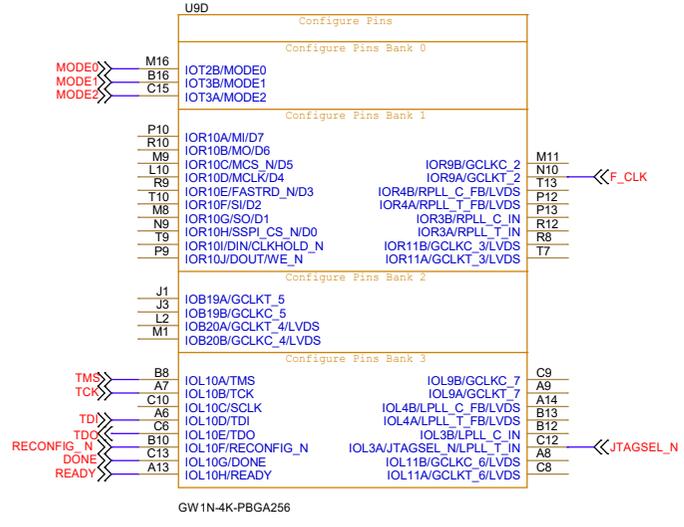
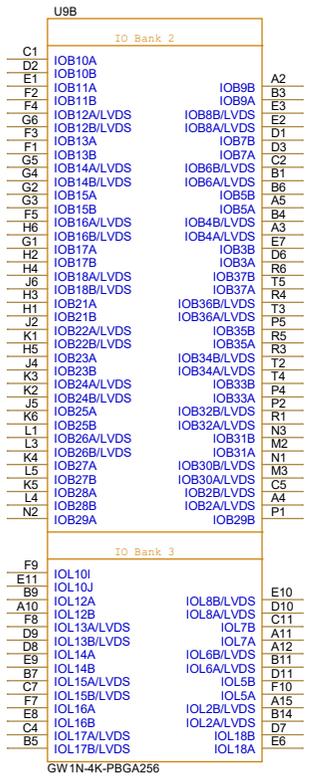
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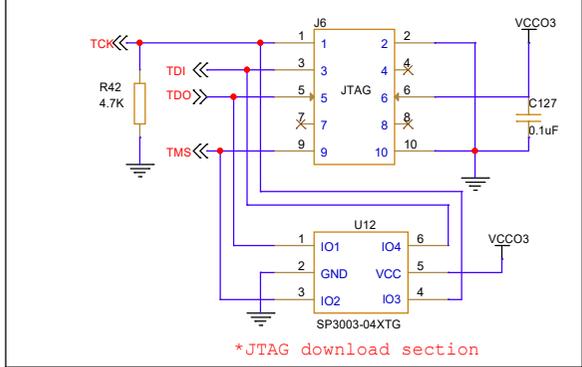
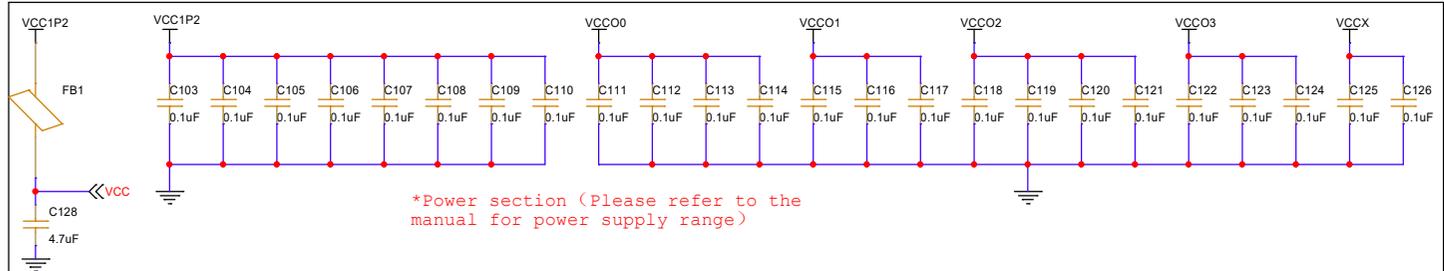
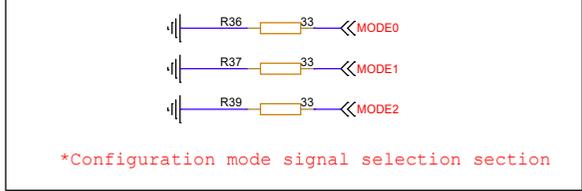
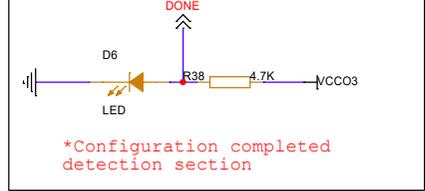
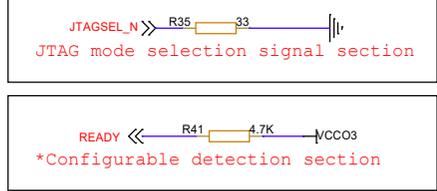
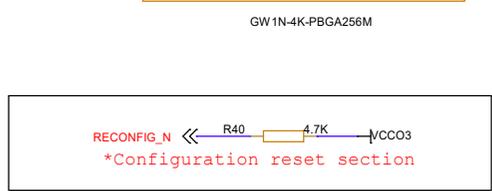
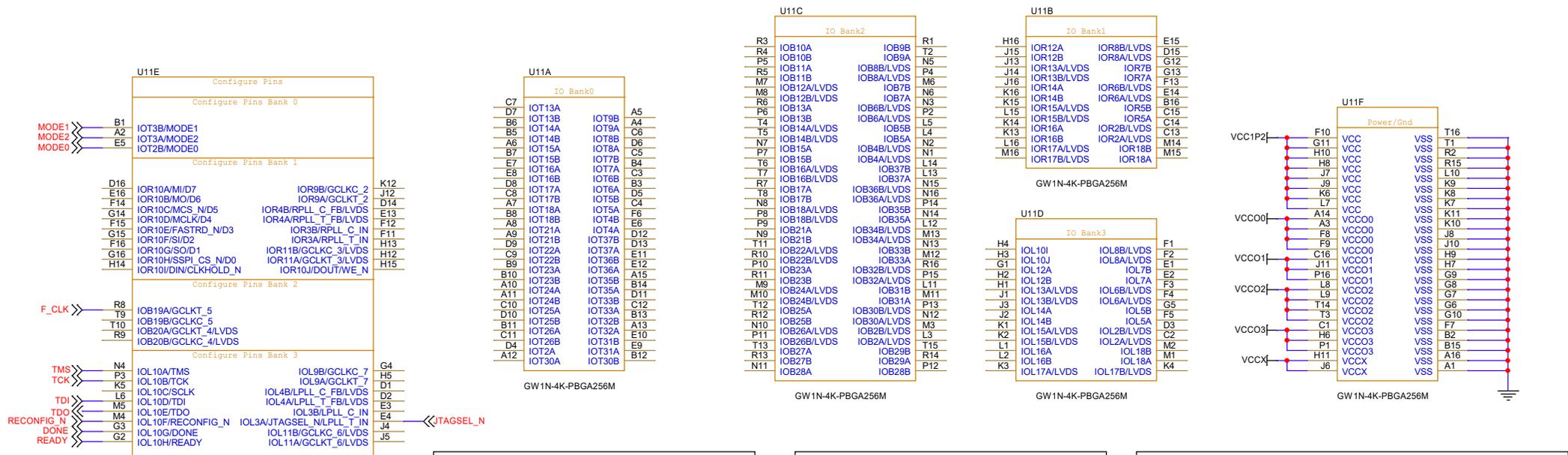
C

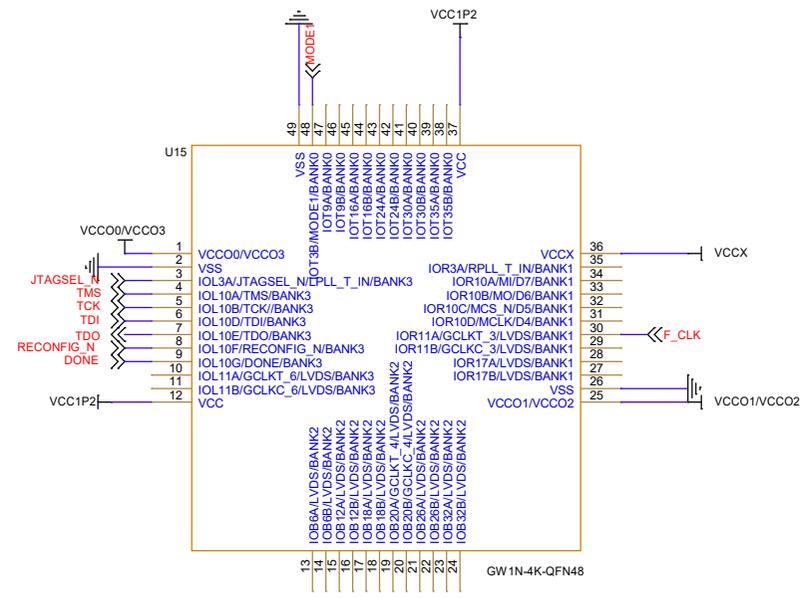
B

A

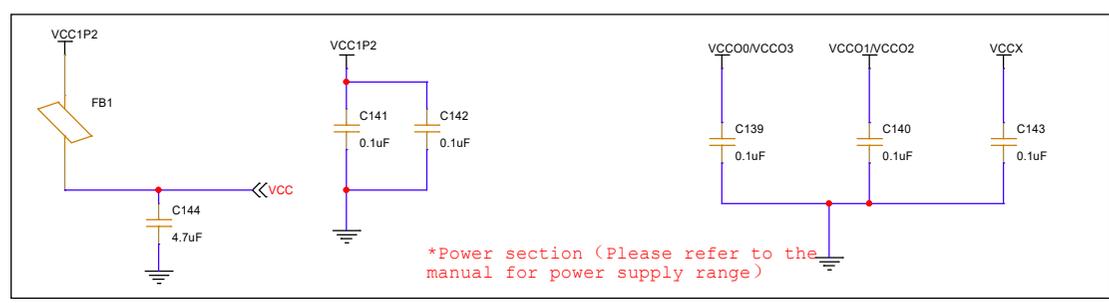




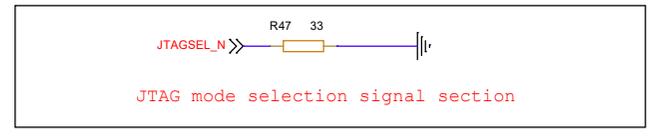




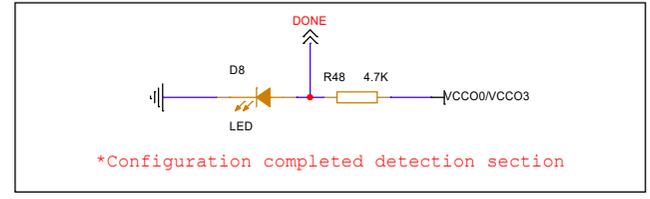
*The unconnected port of the chip is used as a single-ended signal port or multiplexing function
 *Configuration: JTAG.AUTO BOOT.MSPI



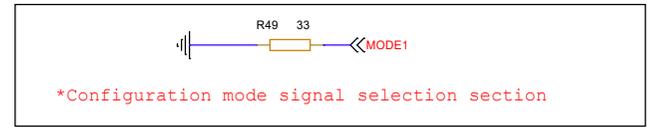
*Power section (Please refer to the manual for power supply range)



JTAG mode selection signal section



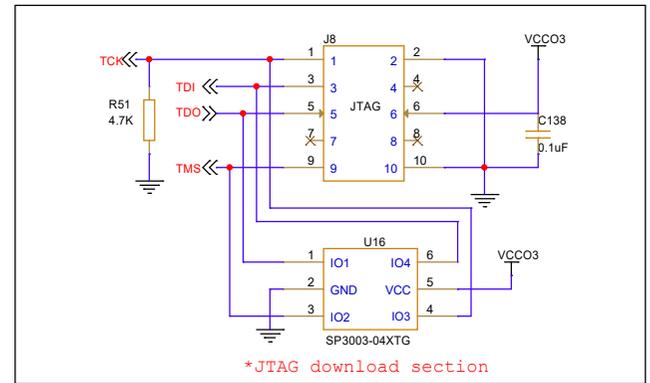
*Configuration completed detection section



*Configuration mode signal selection section



*Configuration reset section (low level)



*JTAG download section

