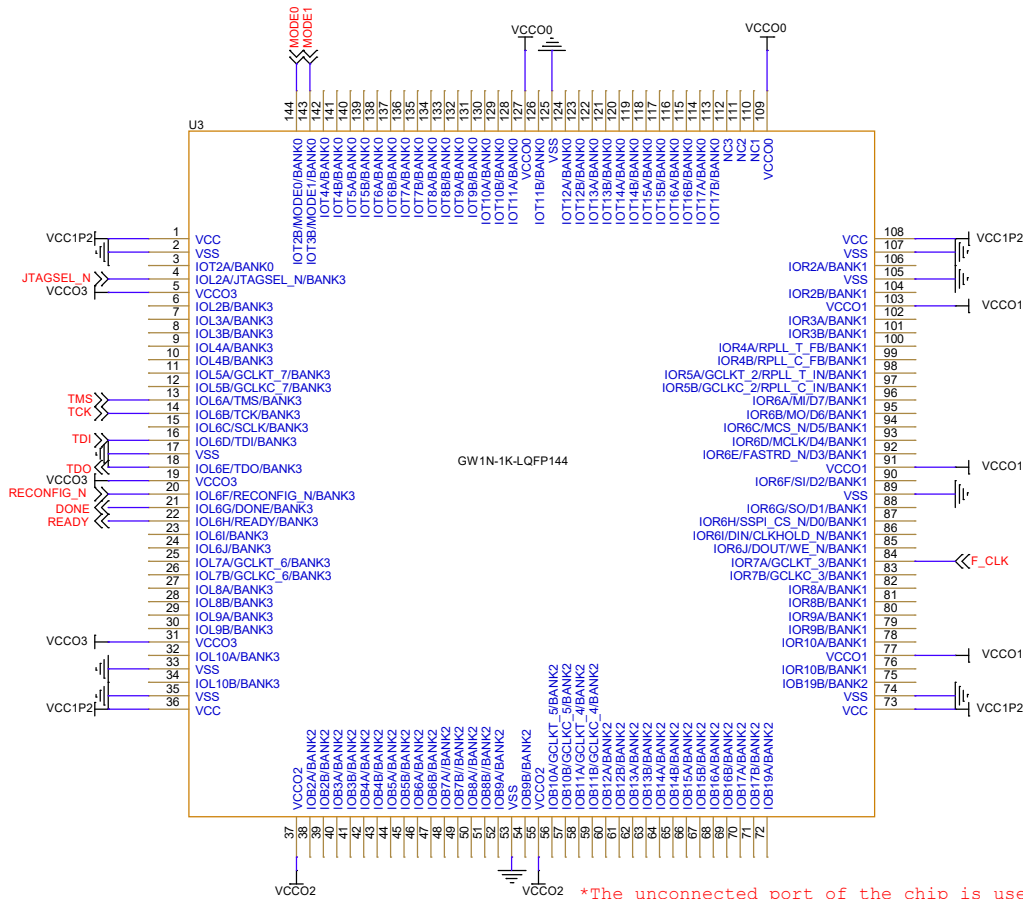
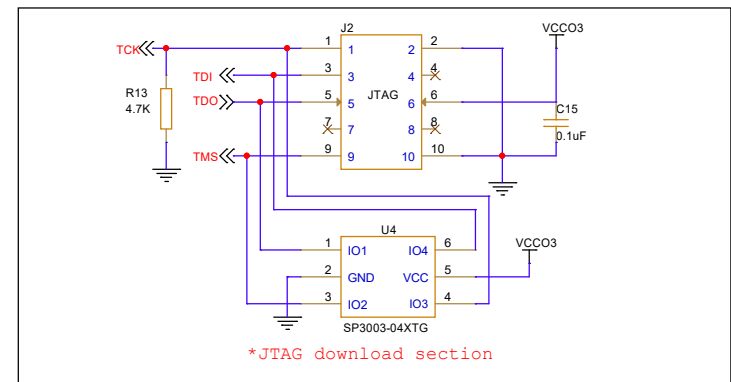
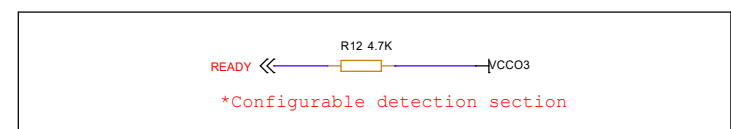
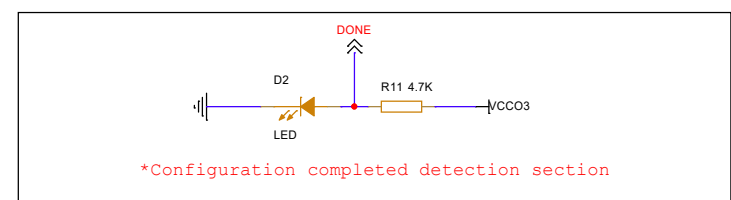
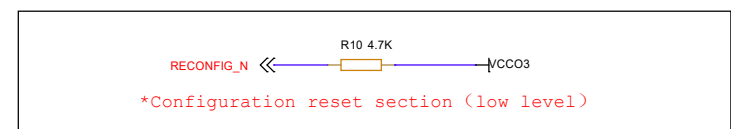
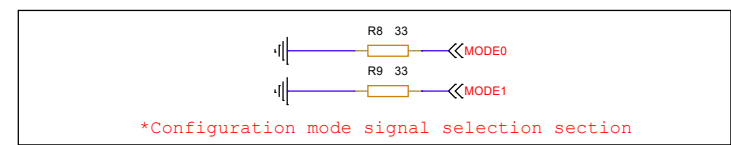
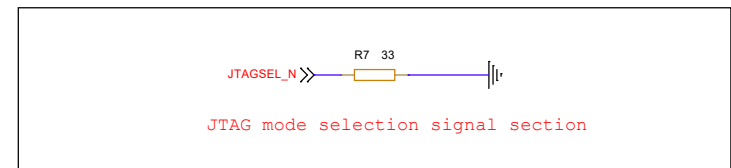
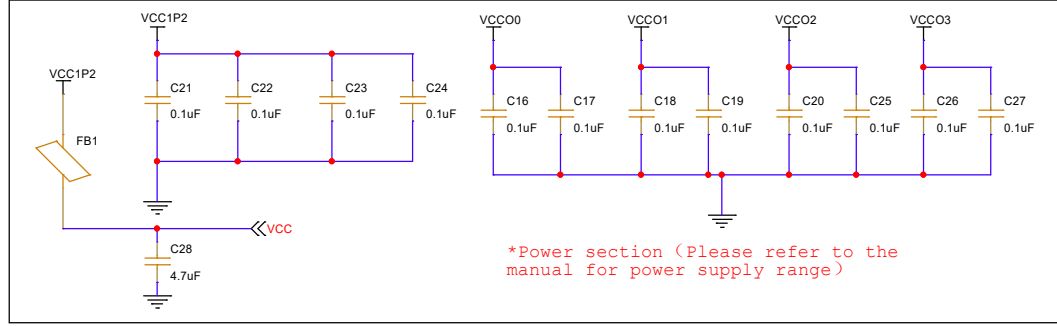
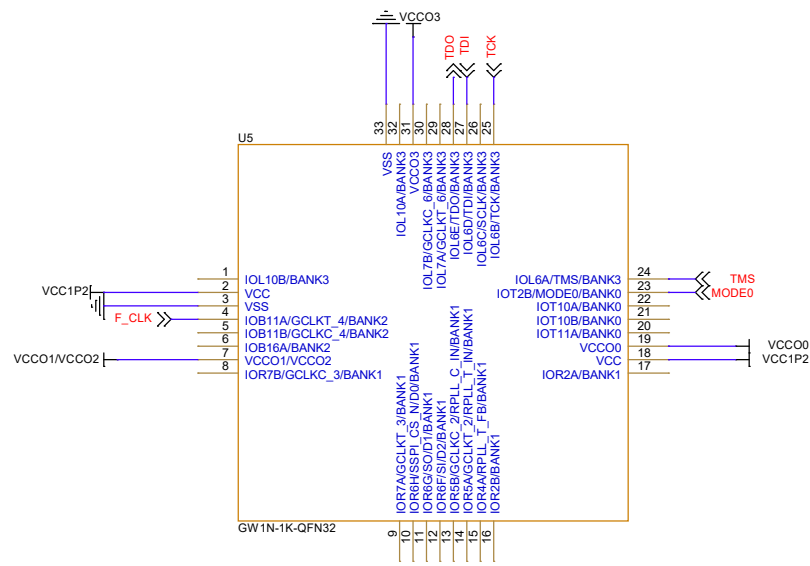


GW1N-LV1-LQFP144

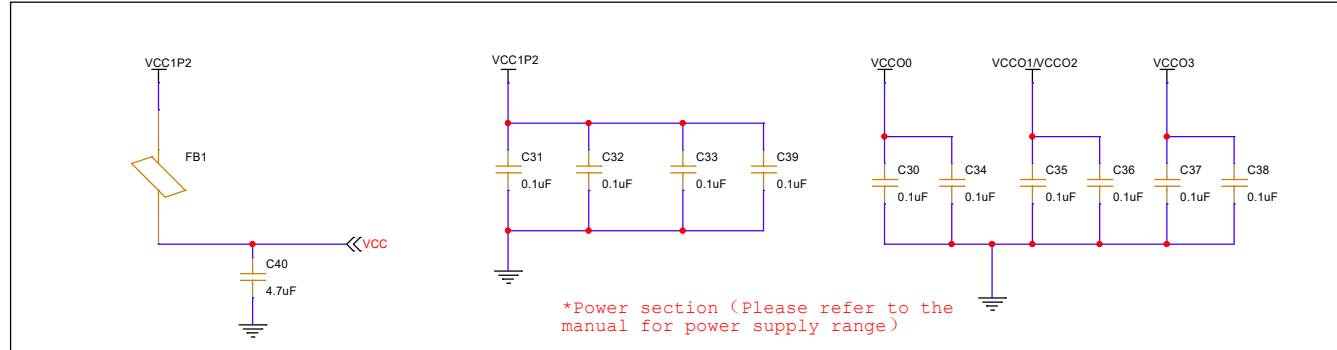
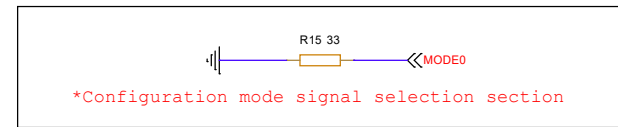
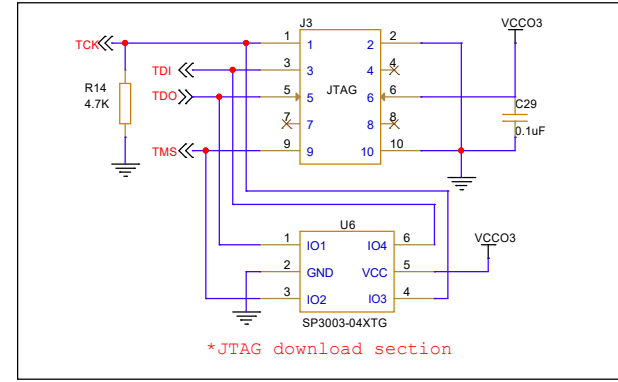


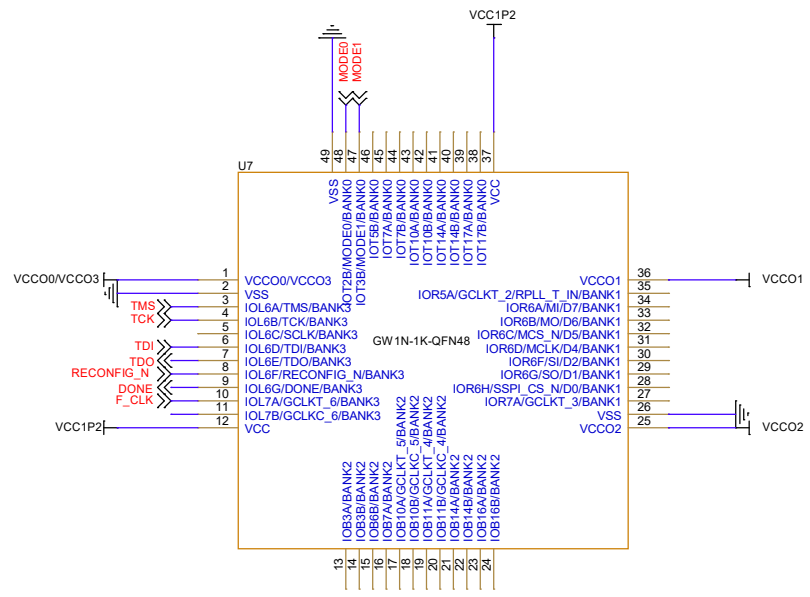
\*The unconnected port of the chip is used as a single-ended signal port or multiplexing function  
\*Configuration: JTAG.AUTO BOOT.SSPI.MSPI



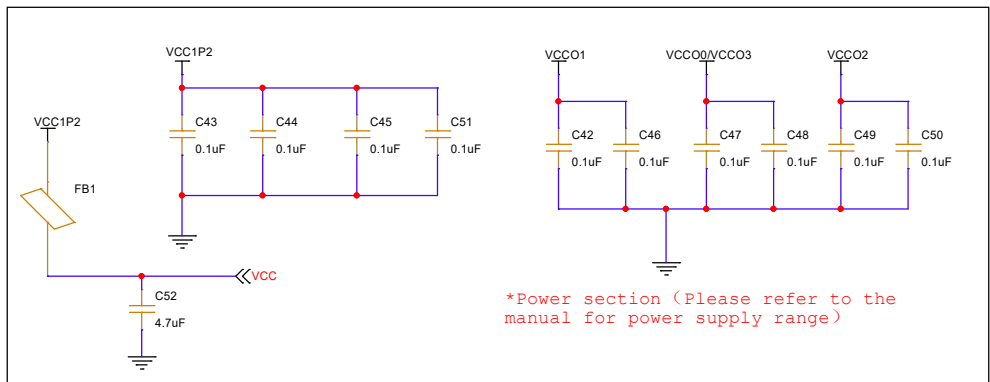


\*The unconnected port of the chip is used as a single-ended signal port or multiplexing function  
\*Configuration: JTAG.AUTO BOOT.SSPI

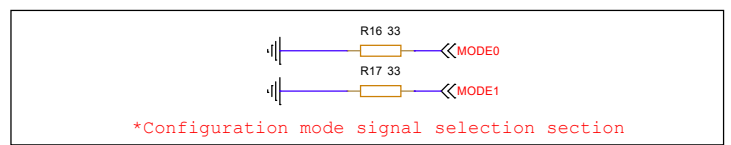




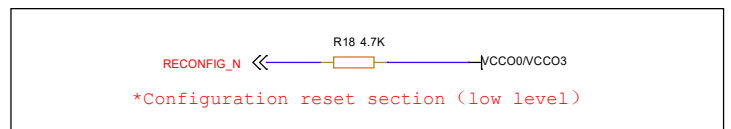
\*The unconnected port of the chip is used as a single-ended signal port or multiplexing function  
\*Configuration: JTAG.AUTO BOOT.SSPI.MSPI



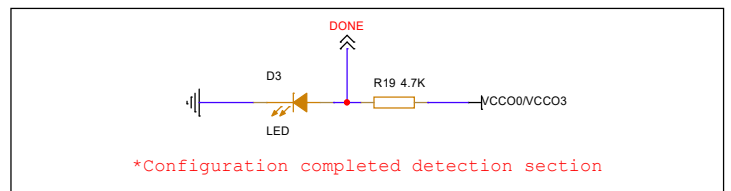
\*Power section (Please refer to the manual for power supply range)



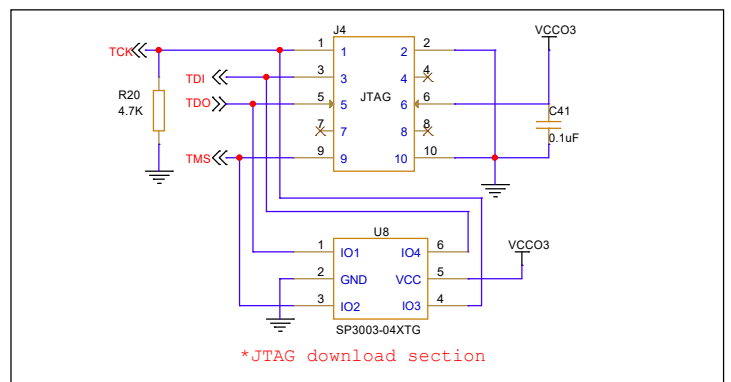
\*Configuration mode signal selection section



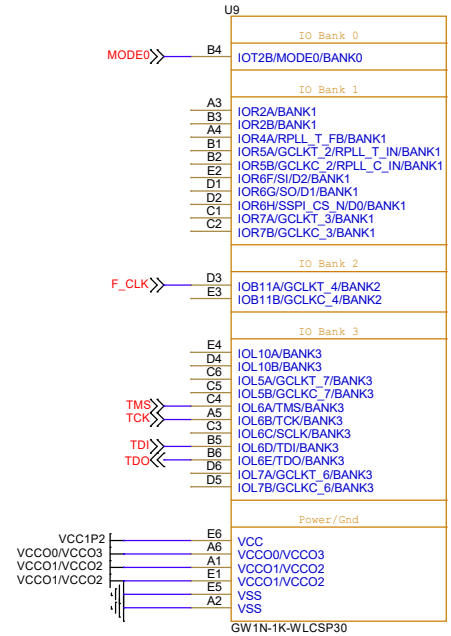
\*Configuration reset section (low level)



\*Configuration completed detection section



\*JTAG download section



\*The unconnected port of the chip is used as a single-ended signal port or multiplexing function

\*Configuration: JTAG.AUTO BOOT.SSPI

