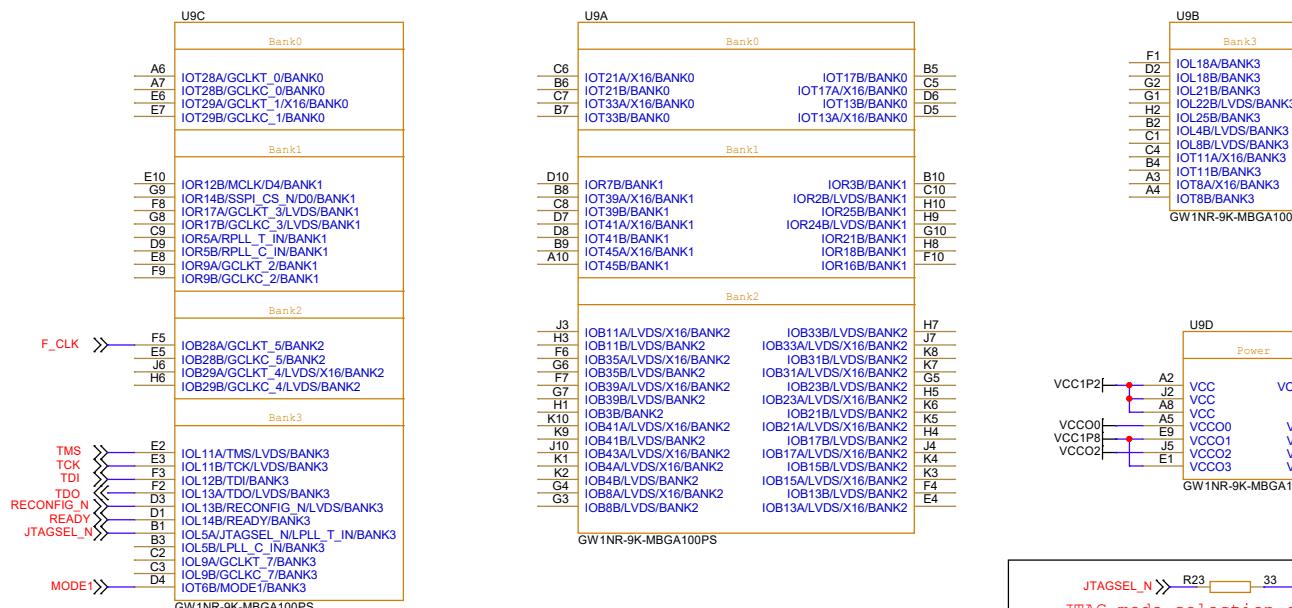
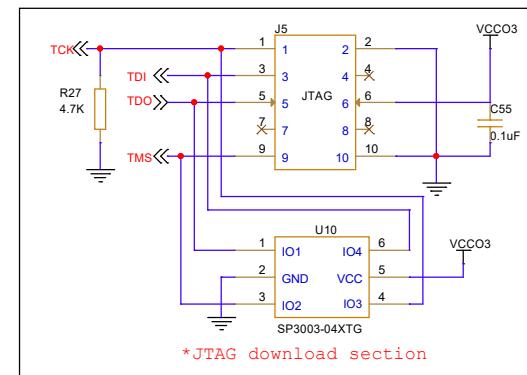
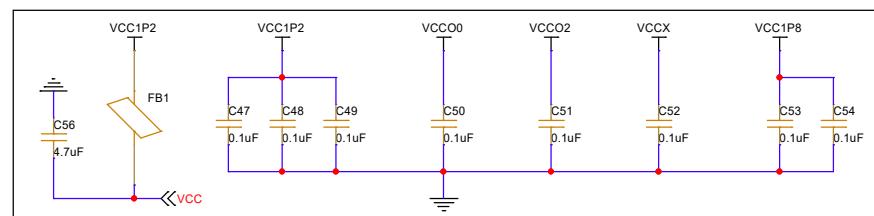


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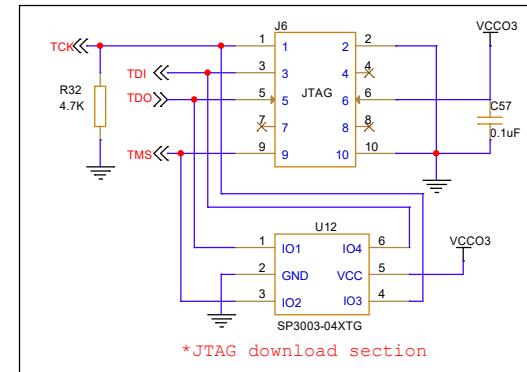
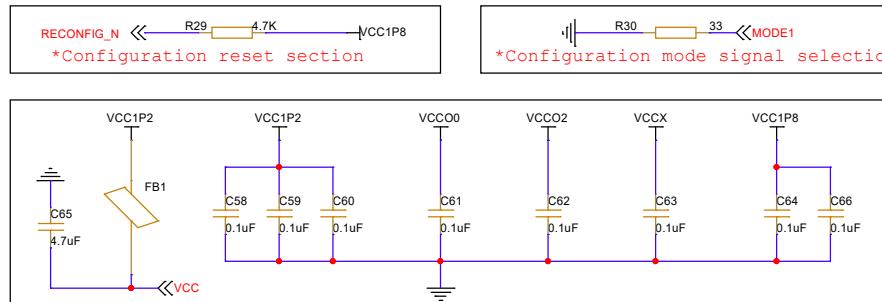
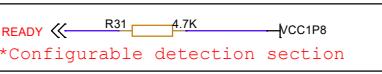
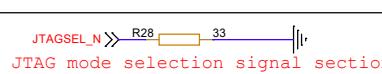
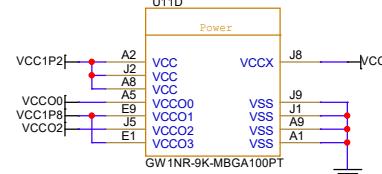
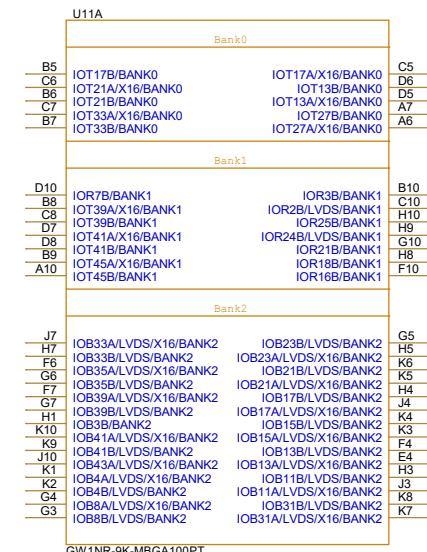
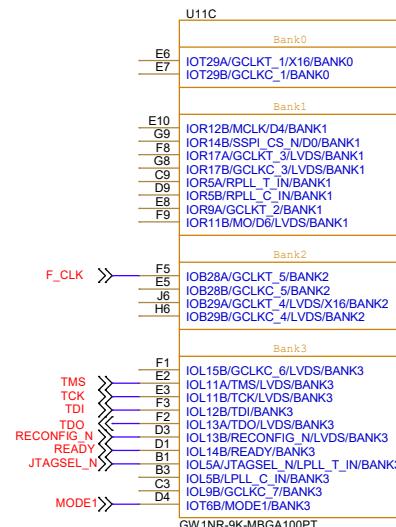


JTAGSEL_N \gg R23 33 \parallel
*JTAG mode selection signal section

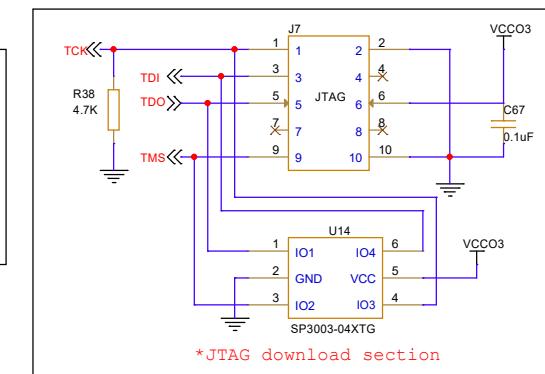
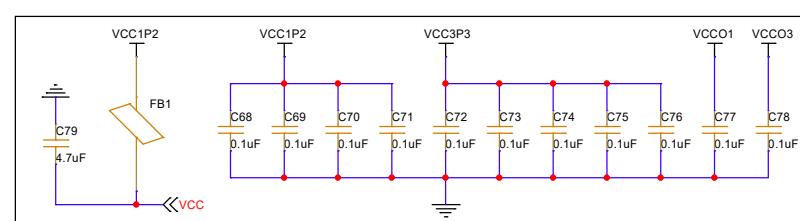
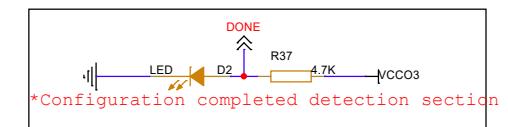
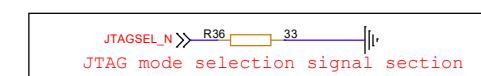
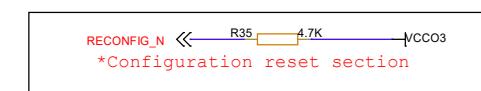
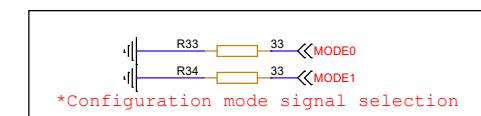
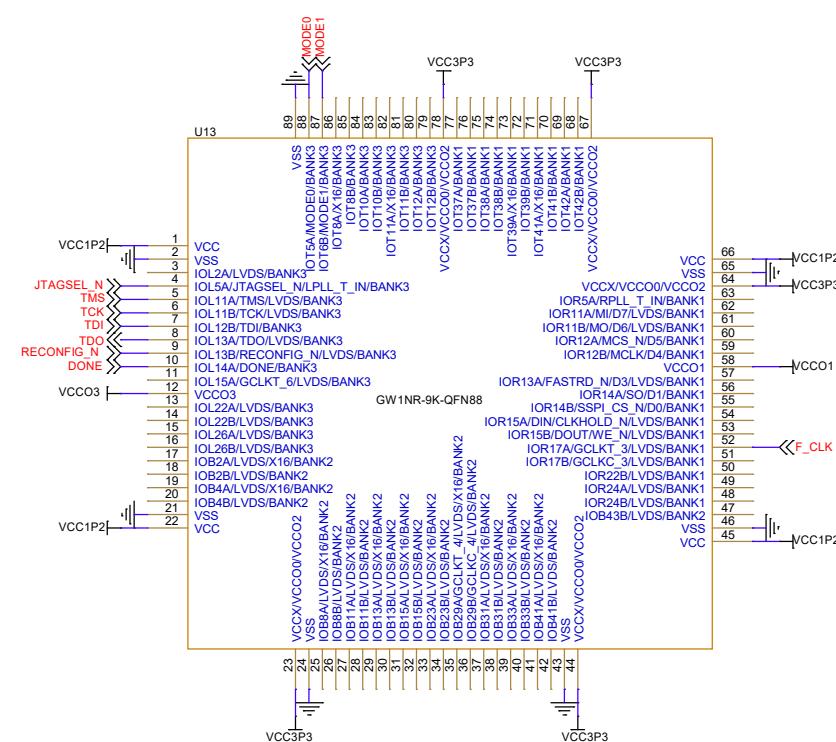
READY \ll R26 4.7K \rightarrow VCC1P8
*Configurable detection section



Title	
GW1NR Minimum System Diagram	
Size A3	Document Number GW1NR-LV9K-MBGA100PS
Date: Friday, February 03, 2023	Rev V1.1
Sheet 5	of 8

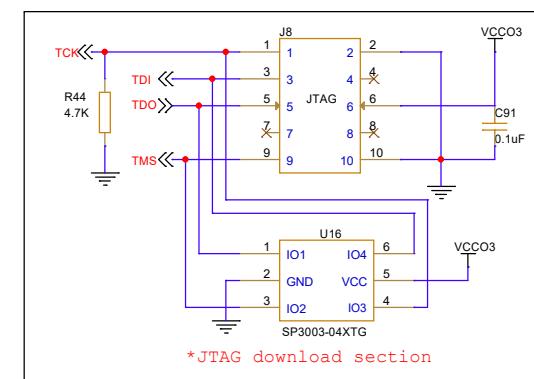
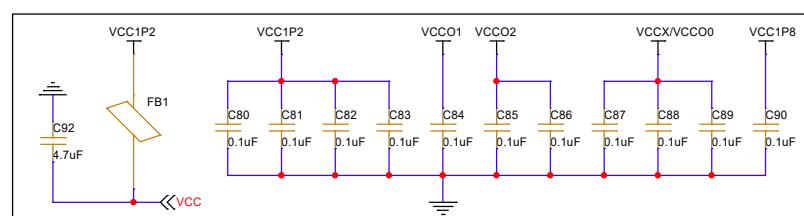
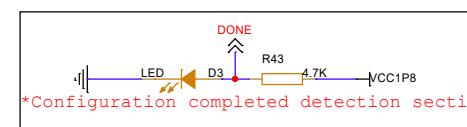
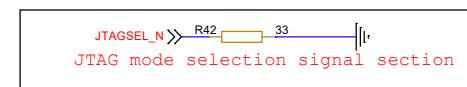
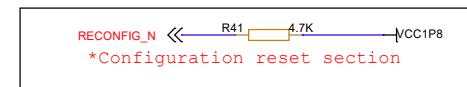
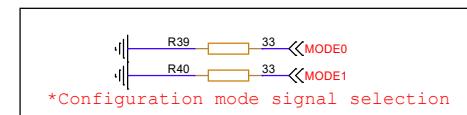
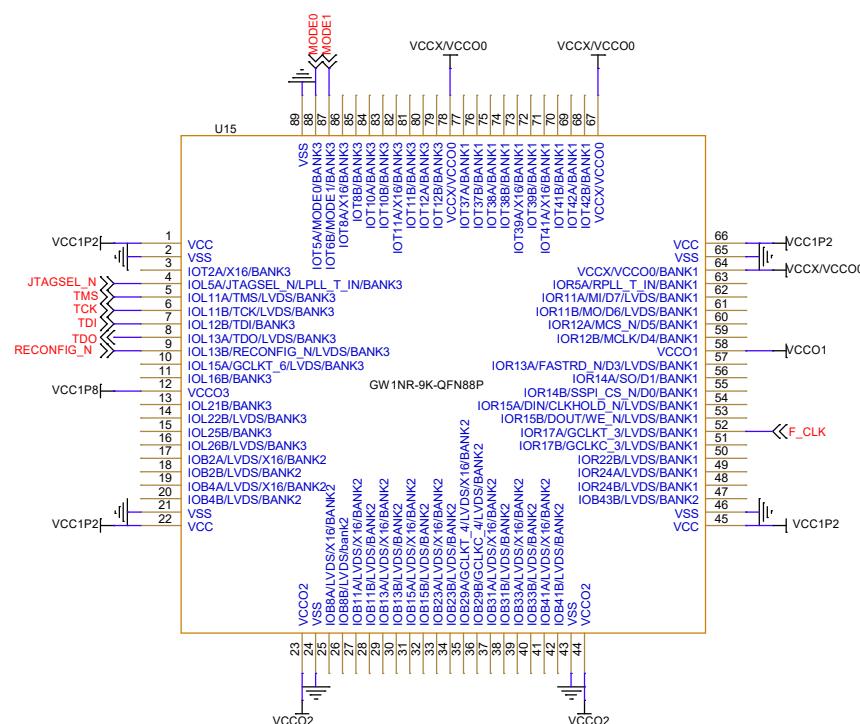


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Title	
GOWIN Minimum System Diagram	
Size	Document Number
A3	GW1NR-LV9K-QFN88
Date:	Rev
Friday, February 03, 2023	V1.1
Sheet	1
of 8	

D



Title	
GOWIN Minimum System Diagram	
Size A3	Document Number GW1NR-LV9K-QFN88P
Date: Friday, February 03, 2023	Rev V1.1
Sheet 1	of 8