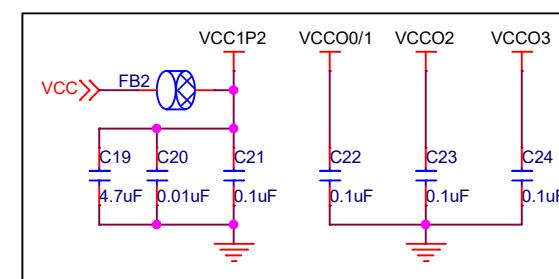
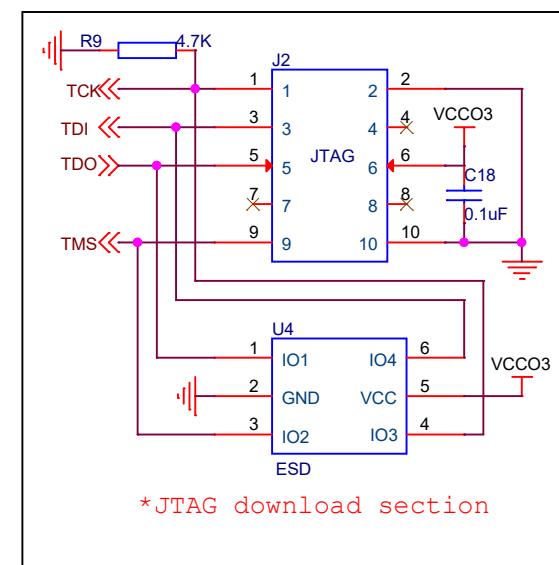
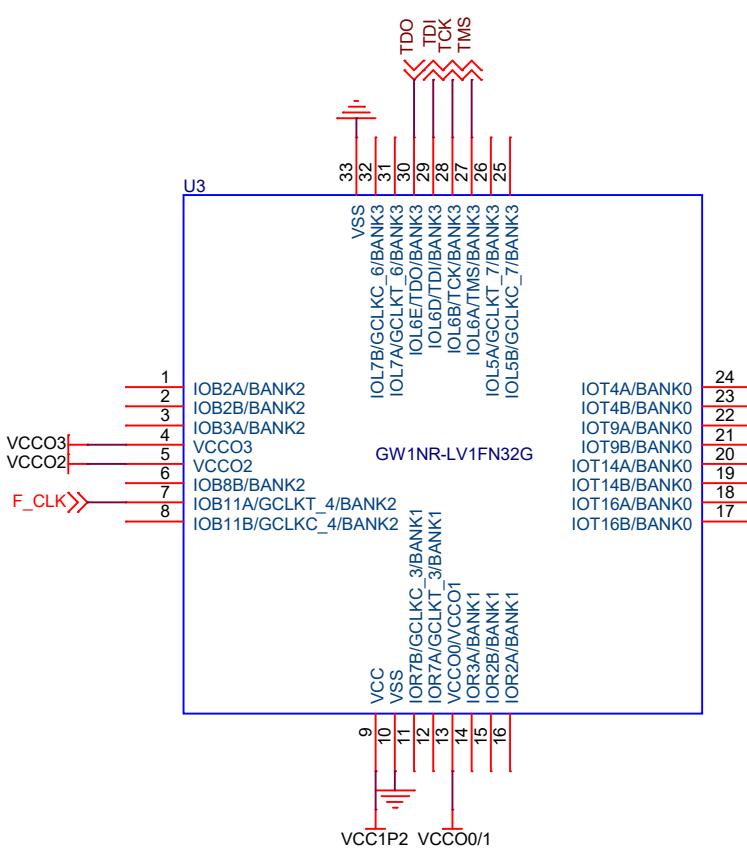
**Notes:**

1. F_CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
2. It is recommended that add an ESD protection chip to the JTAG download circuit.

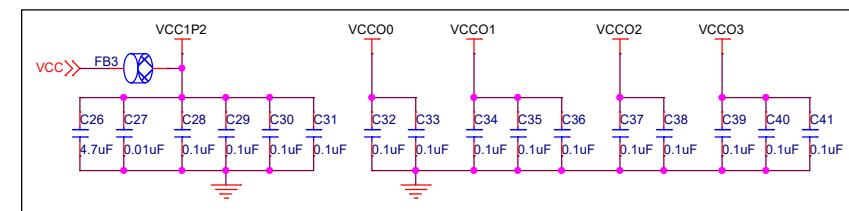
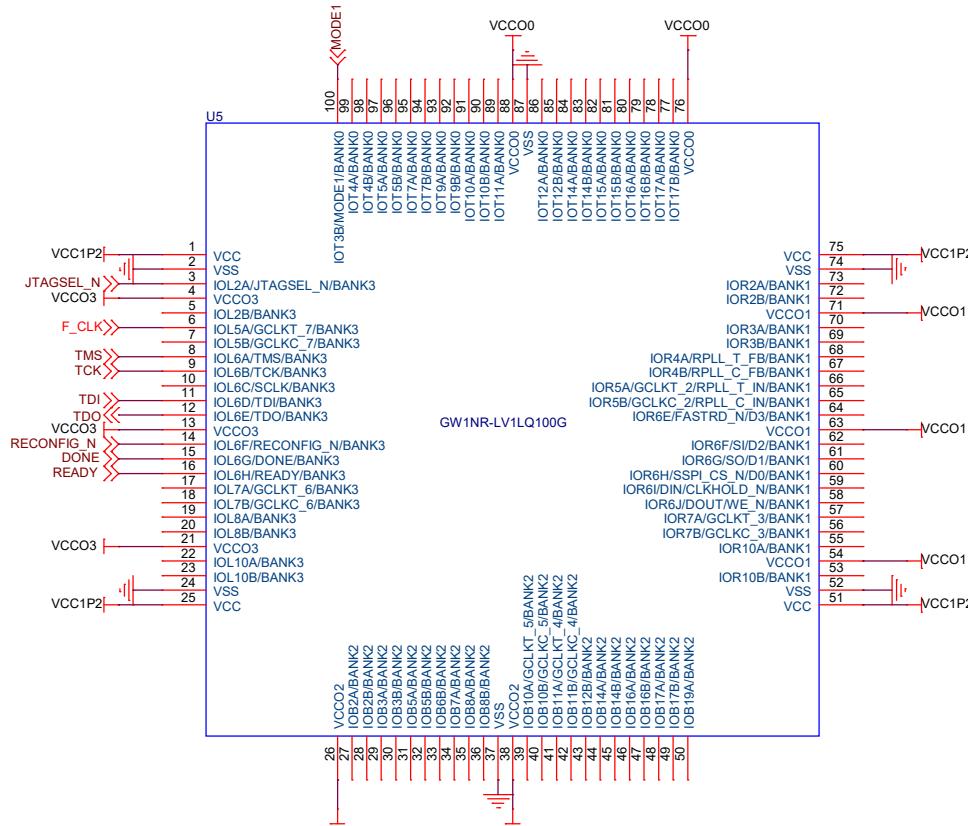
Title		
GOWIN Minimum System Diagram		
Size	Document Number	Rev
B	GW1NR-LV1EQ144G	2.0
Date:	Sunday, April 23, 2023	Sheet 1 of 5

GW1NR-LV1FN32G



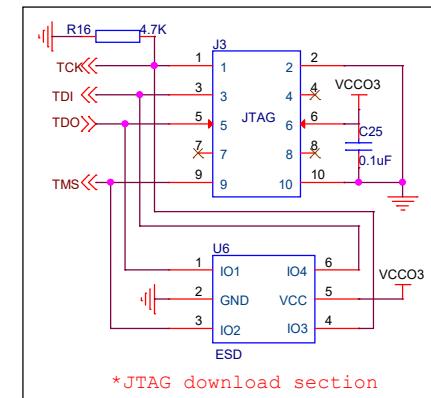
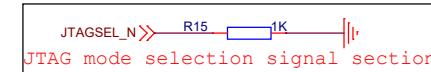
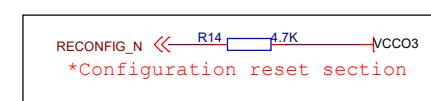
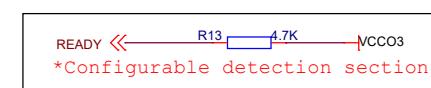
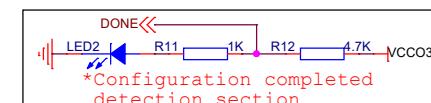
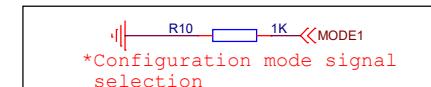
Notes:

1. F_CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
2. It is recommended that add an ESD protection chip to the JTAG download circuit.



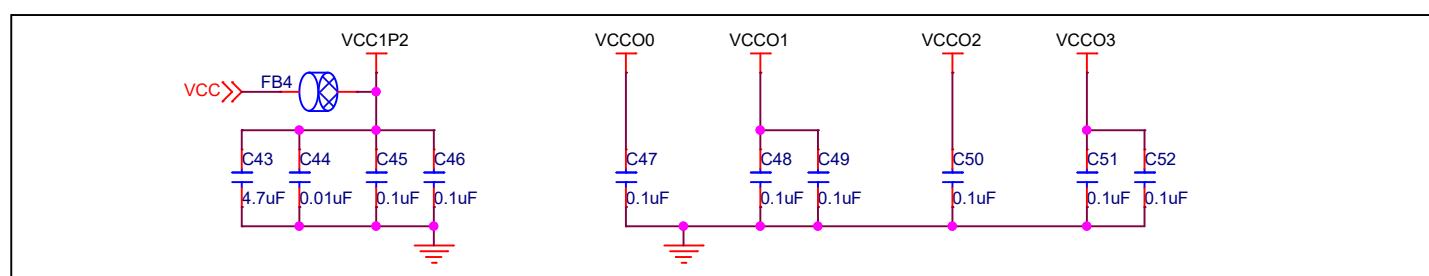
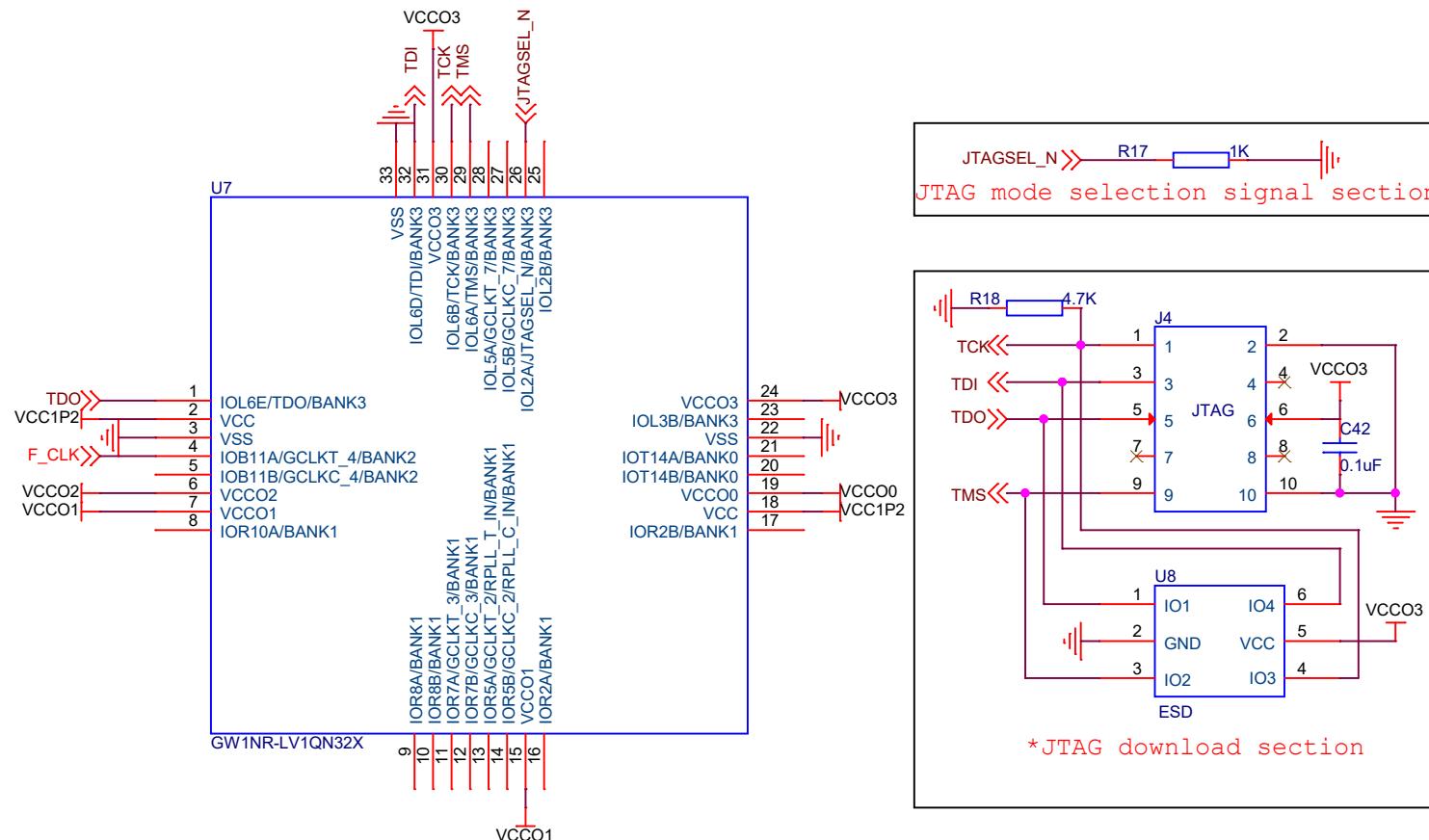
Notes:

1. F_CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
2. It is recommended that add an ESD protection chip to the JTAG download circuit.



Title		
GOWIN Minimum System Diagram		
Size	Document Number	Rev
B	GW1NR-LV1LQ100G	2.0
Date:	Sunday, April 23, 2023	Sheet 3 of 5

GW1NR-LV1QN32X

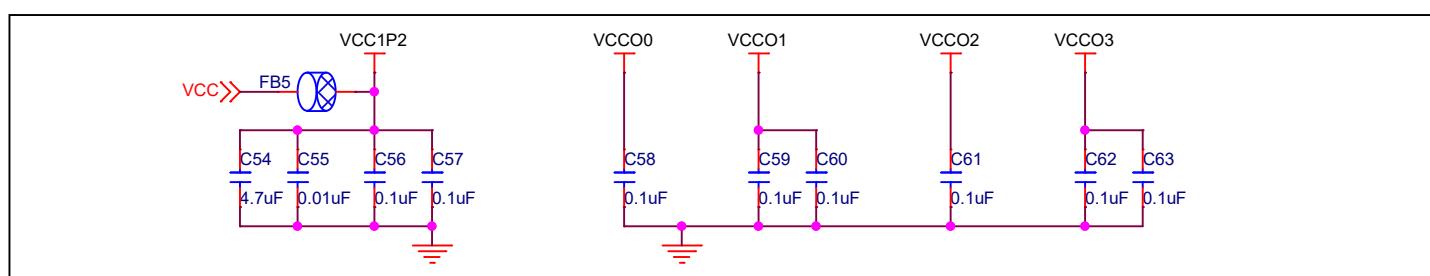
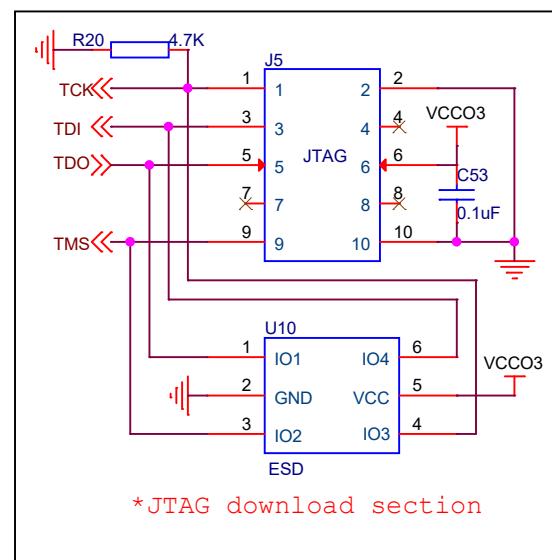
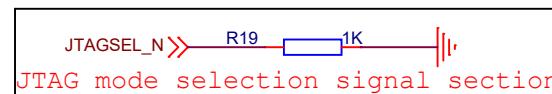
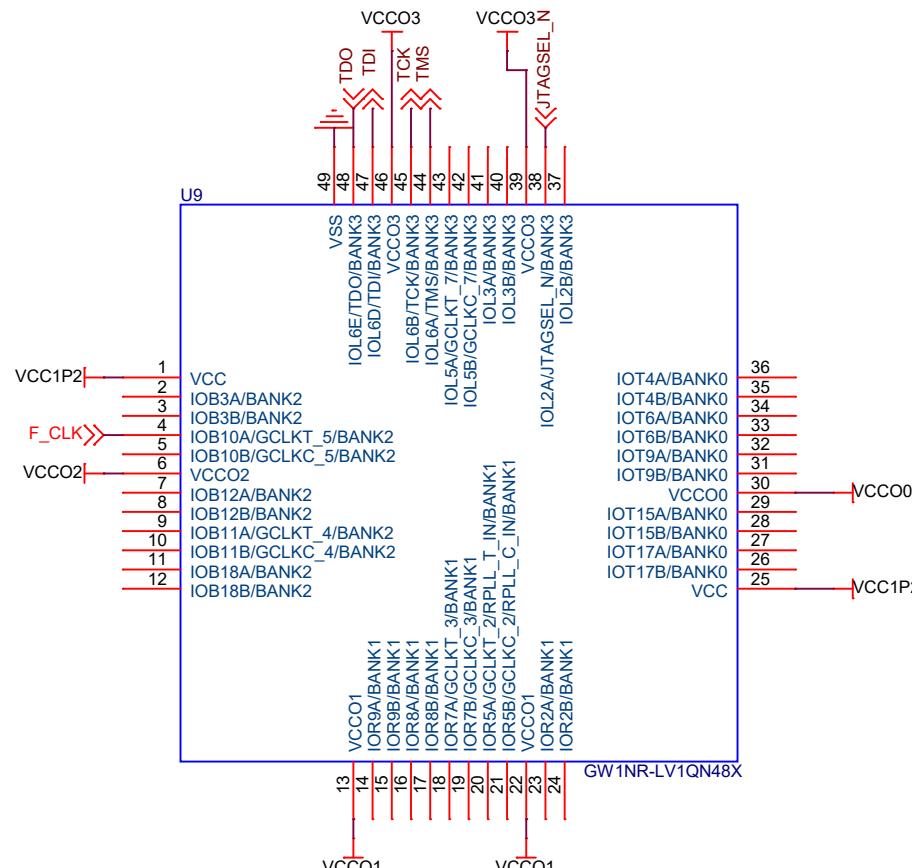


Notes:

1. F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
2. It is recommended that add an ESD protection chip to the JTAG download circuit.

Title	
GOWIN Minimum System Diagram	
Date:	Sunday, April 23, 2023
Sheet	4 of 5

GW1NR-LV1QN48X



Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
2.It is recommended that add an ESD protection chip to the JTAG download circuit.

Title	
GOWIN Minimum System Diagram	
Size A4	Document Number GW1NR-LV1QN48X
Date:	Sunday, April 23, 2023