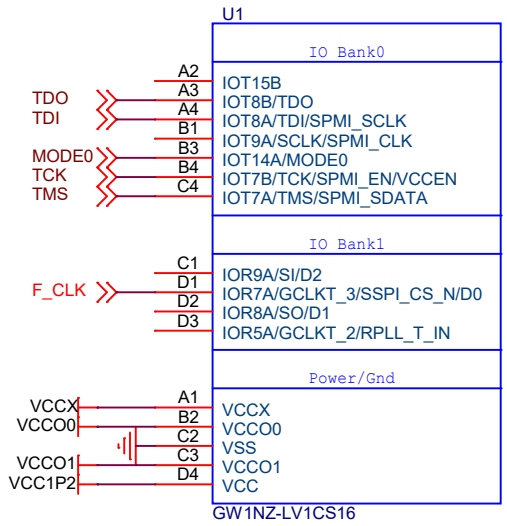
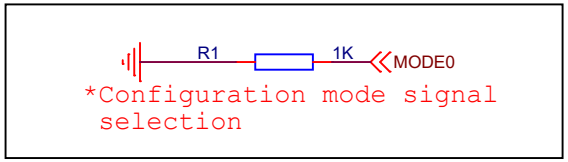
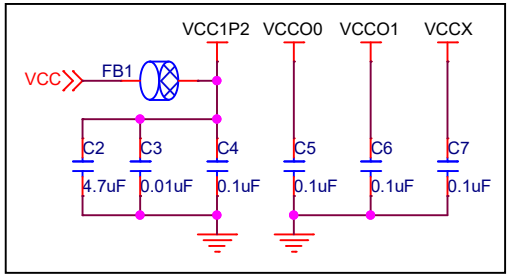
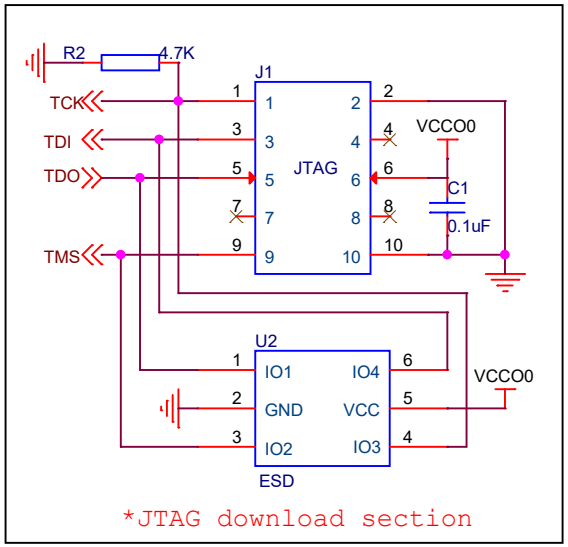
GW1NZ-LV1CS16





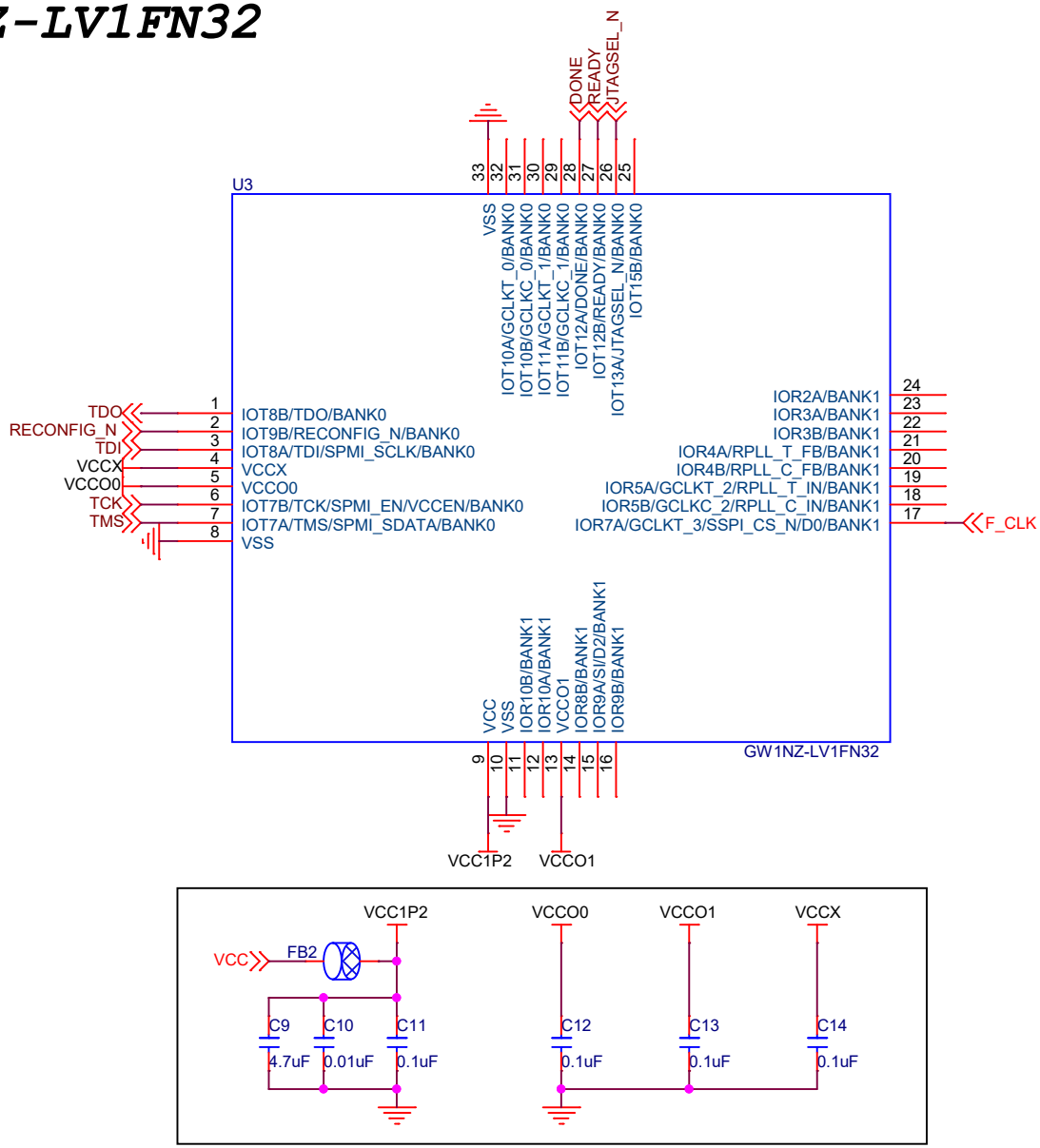


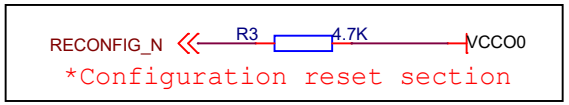


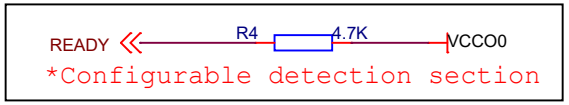
Notes:
1.F_CLK signal is an external input clock signal.
  It is recommended that F_CLK signal be provided through an active oscillator crystal.
2.It is recommended that add an ESD protection chip to the JTAG download circuit.

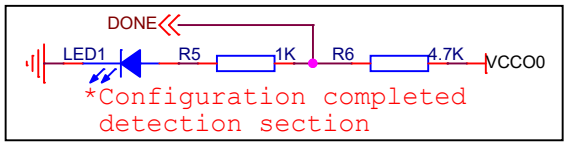
Title: GOWIN Minimum System Diagram		
Size: A4	Document Number: GW1NZ-LV1CS16	Rev: 2.0
Date: Wednesday, April 26, 2023	Sheet 1 of 8	

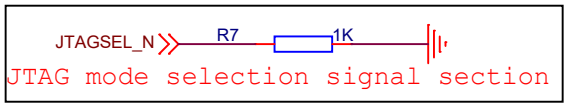
GW1NZ-LV1FN32

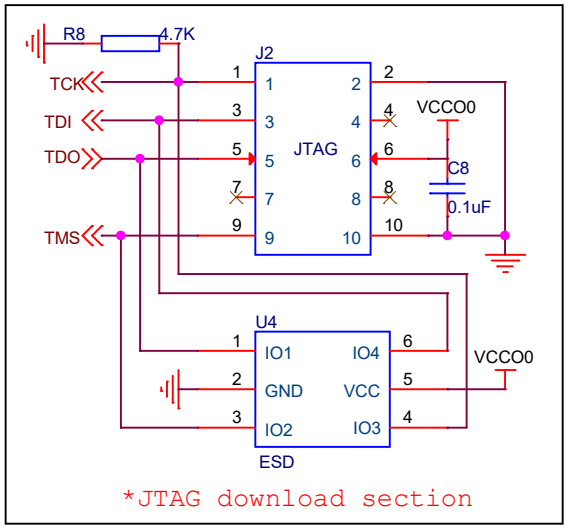








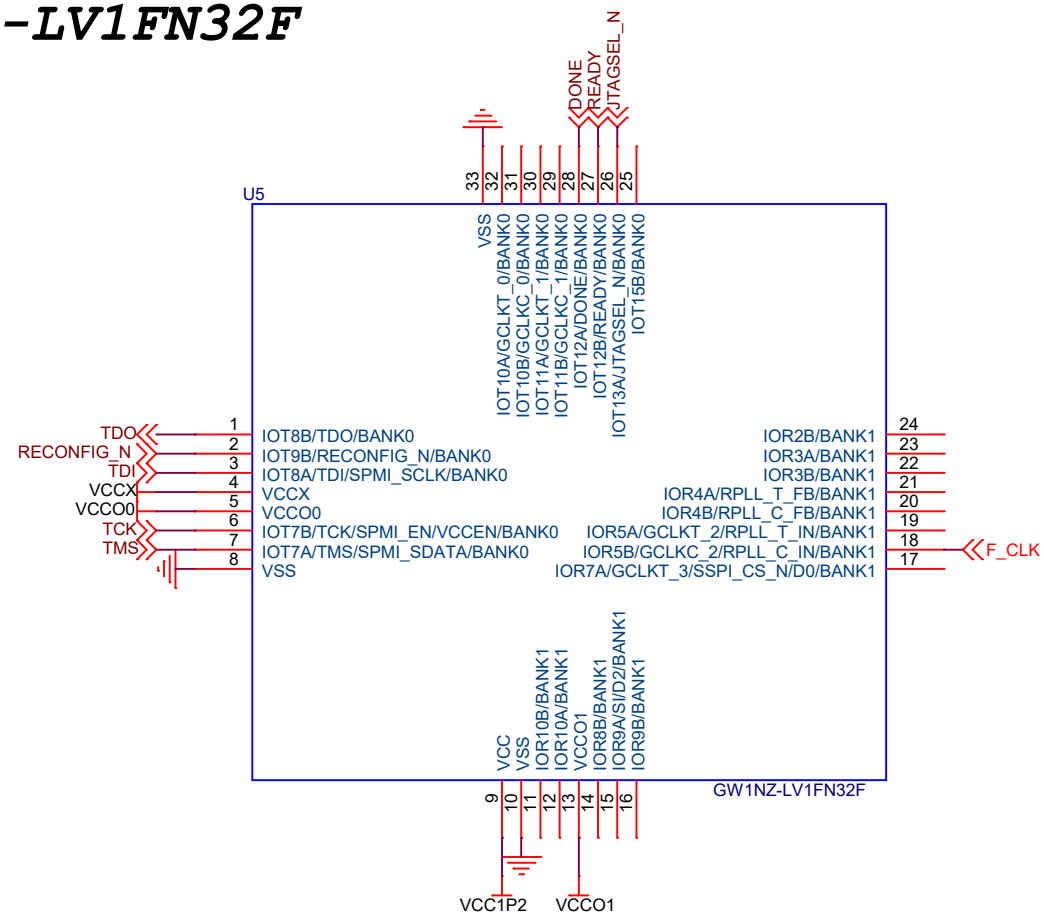


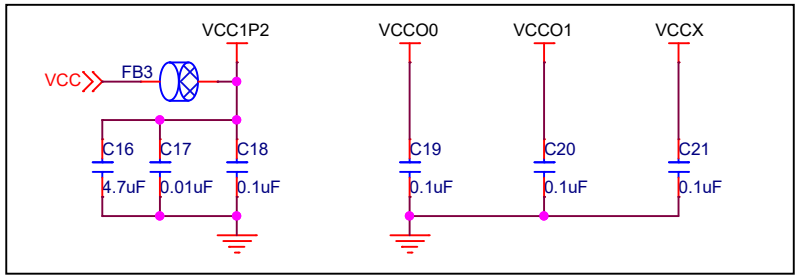


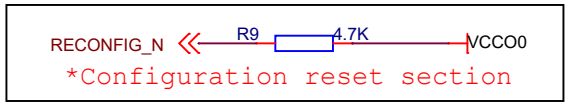
Notes:
1.F_CLK signal is an external input clock signal.
  It is recommended that F_CLK signal be provided through an active oscillator crystal.
2.It is recommended that add an ESD protection chip to the JTAG download circuit.

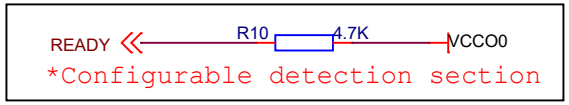
Title: GOWIN Minimum System Diagram		
Size: A4	Document Number: GW1NZ-LV1FN32	Rev: 2.0
Date: Wednesday, April 26, 2023	Sheet 2 of 8	

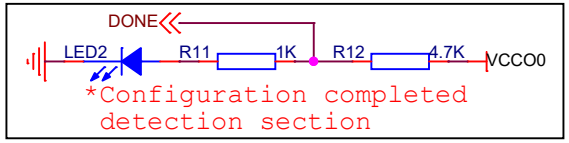
GW1NZ-LV1FN32F

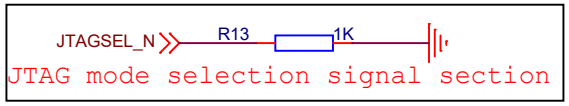


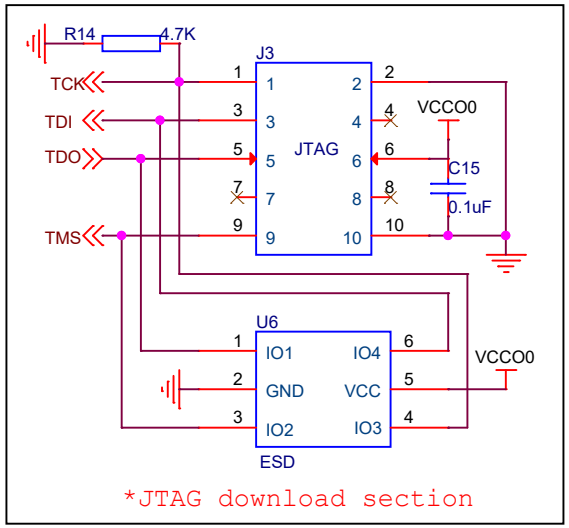








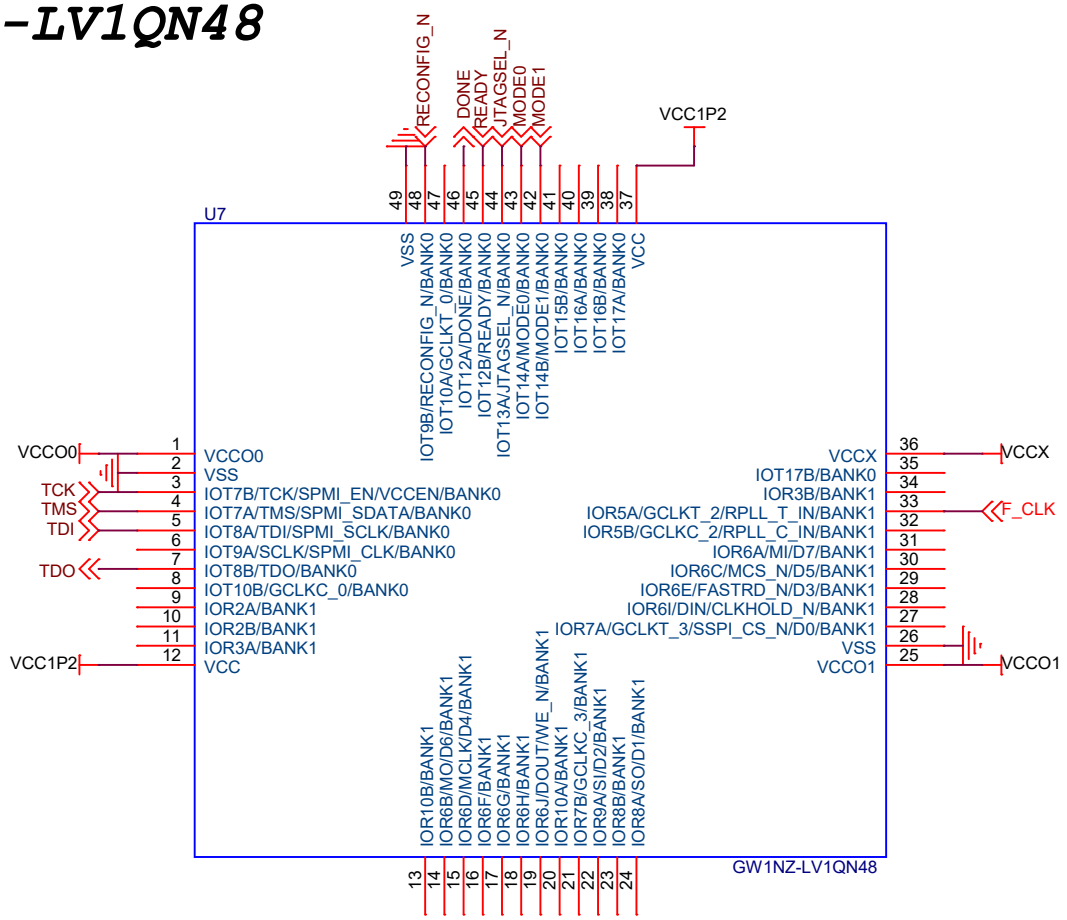


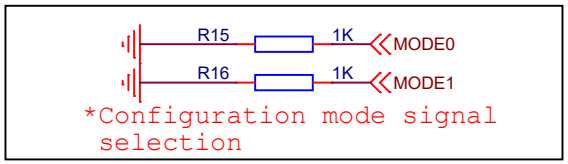


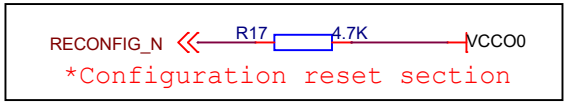
Notes:
1.F_CLK signal is an external input clock signal.
  It is recommended that F_CLK signal be provided through an active oscillator crystal.
2.It is recommended that add an ESD protection chip to the JTAG download circuit.

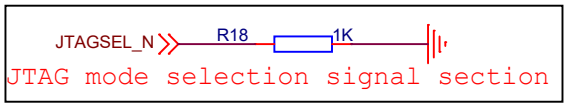
Title: GOWIN Minimum System Diagram		
Size: A4	Document Number: GW1NZ-LV1FN32F	Rev: 2.0
Date: Wednesday, April 26, 2023	Sheet 3 of 8	

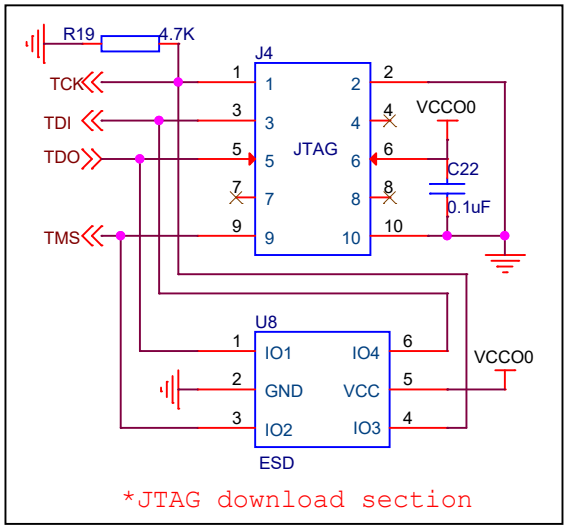
GW1NZ-LV1QN48

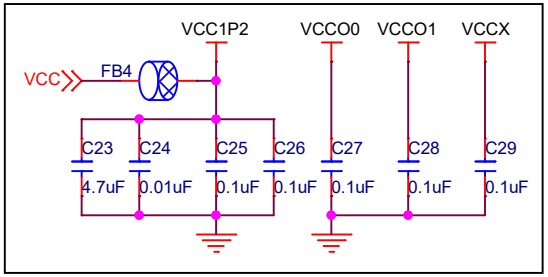


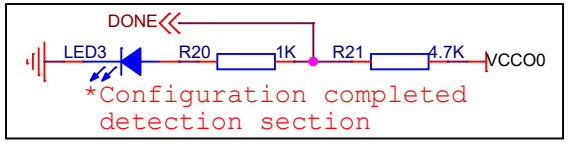


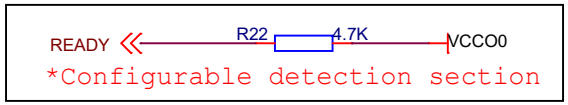








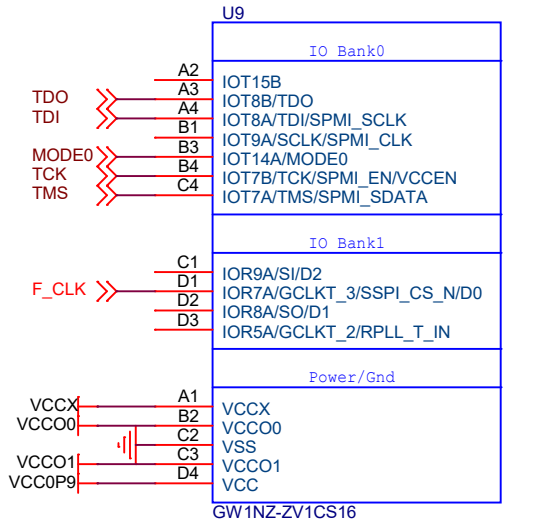


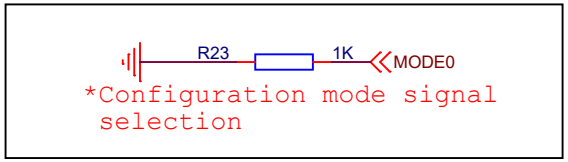


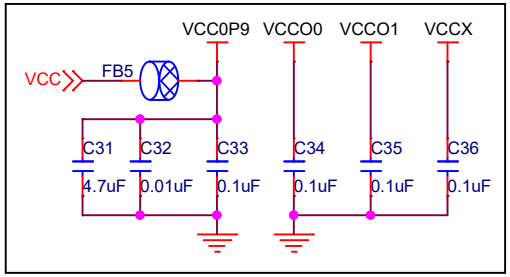
Notes:
1.F_CLK signal is an external input clock signal.
  It is recommended that F_CLK signal be provided through an active oscillator crystal.
2.It is recommended that add an ESD protection chip to the JTAG download circuit.

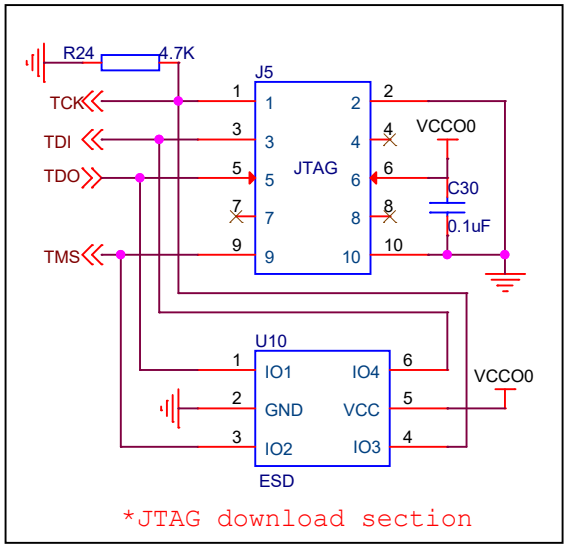
Title	GOWIN Minimum System Diagram	
Size A4	Document Number GW1NZ-LV1QN48	Rev 2.0
Date: Wednesday, April 26, 2023	Sheet 4 of 8	

GW1NZ-ZV1CS16





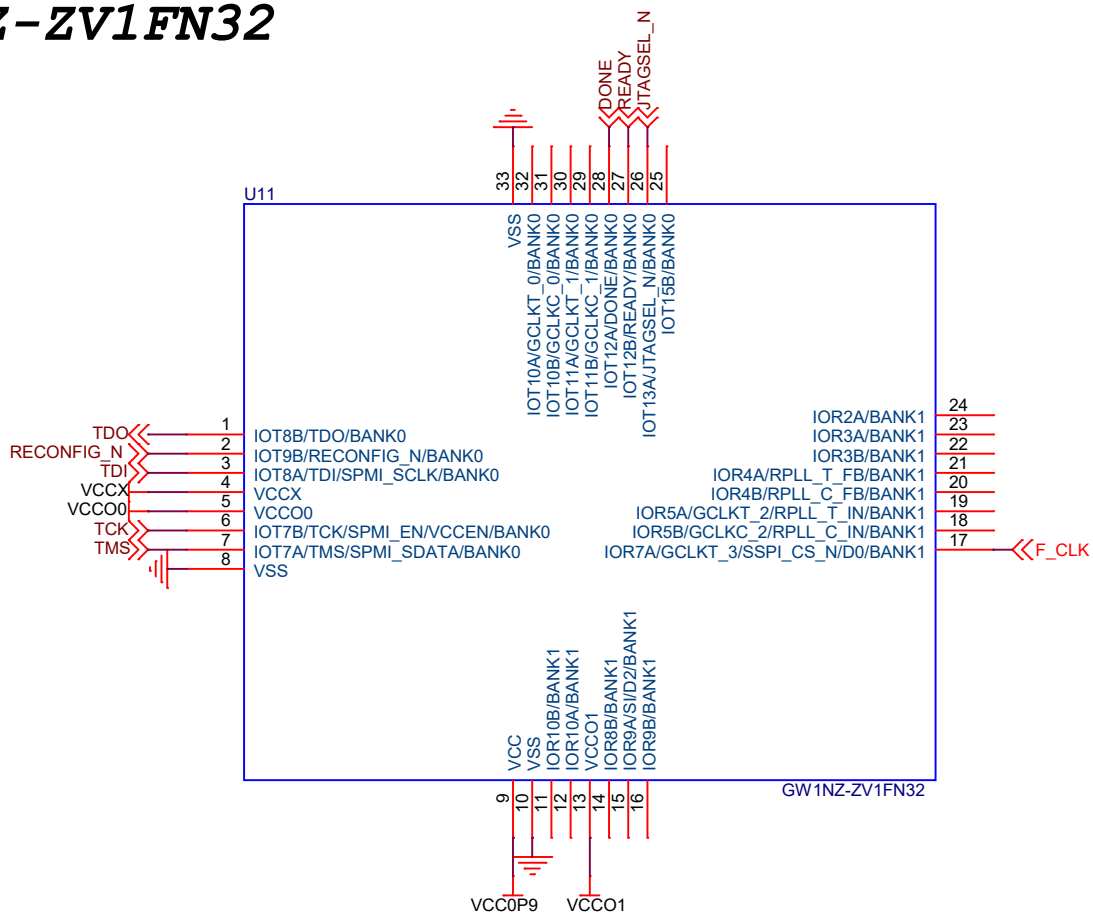


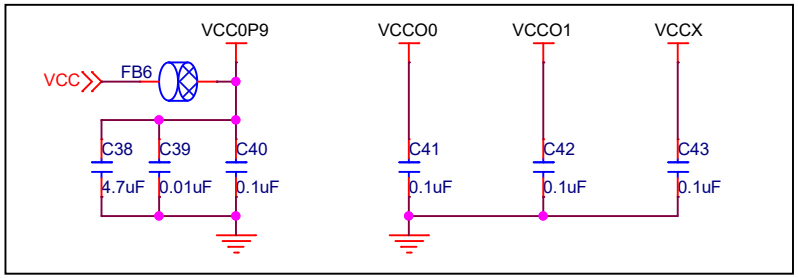


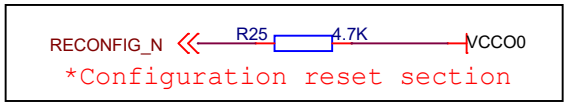
Notes:
1.F_CLK signal is an external input clock signal.
  It is recommended that F_CLK signal be provided through an active oscillator crystal.
2.It is recommended that add an ESD protection chip to the JTAG download circuit.

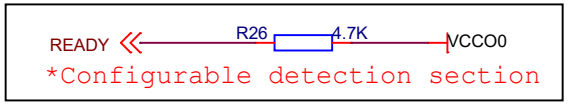
Title: GOWIN Minimum System Diagram		
Size: A4	Document Number: GW1NZ-ZV1CS16	Rev: 2.0
Date: Wednesday, April 26, 2023	Sheet 5 of 8	

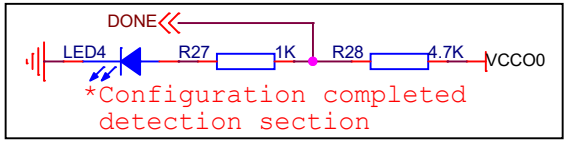
GW1NZ-ZV1FN32

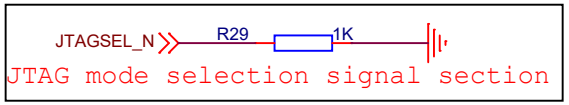


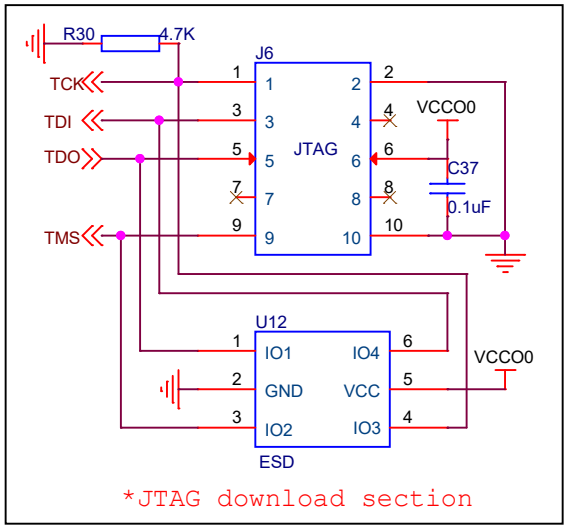








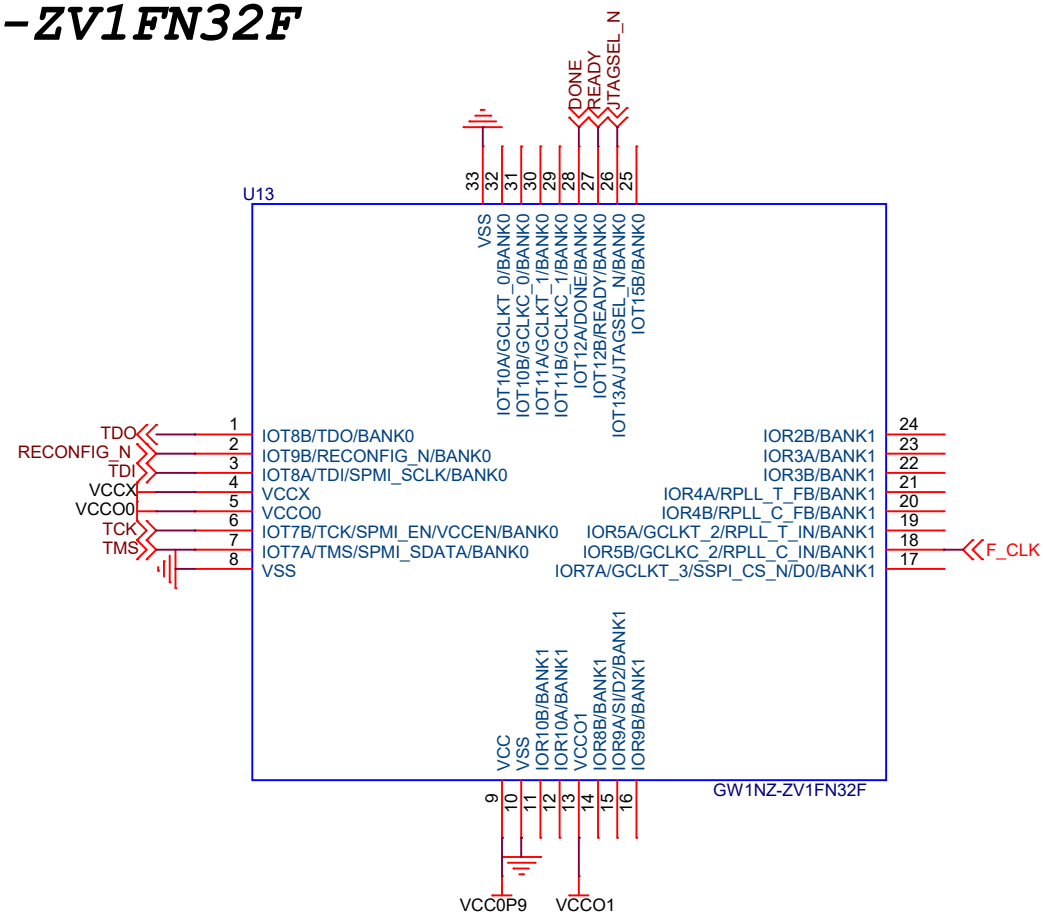


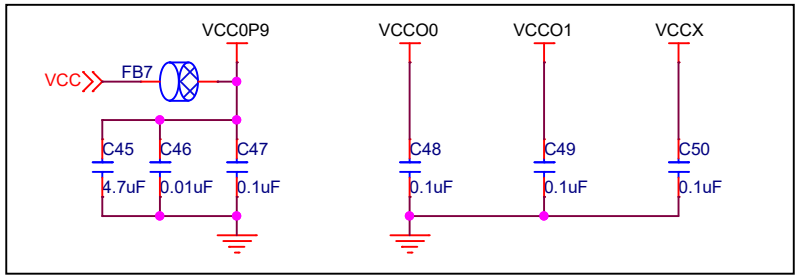


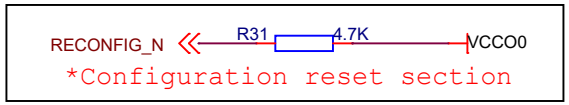
Notes:
1.F_CLK signal is an external input clock signal.
  It is recommended that F_CLK signal be provided through an active oscillator crystal.
2.It is recommended that add an ESD protection chip to the JTAG download circuit.

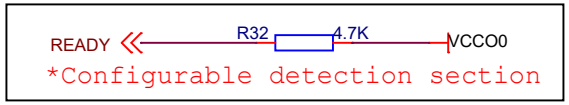
Title: GOWIN Minimum System Diagram		
Size: A4	Document Number: GW1NZ-ZV1FN32	Rev: 2.0
Date: Wednesday, April 26, 2023	Sheet 6 of 8	

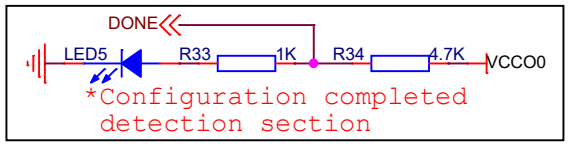
GW1NZ-ZV1FN32F

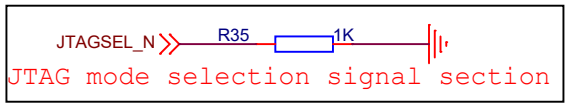


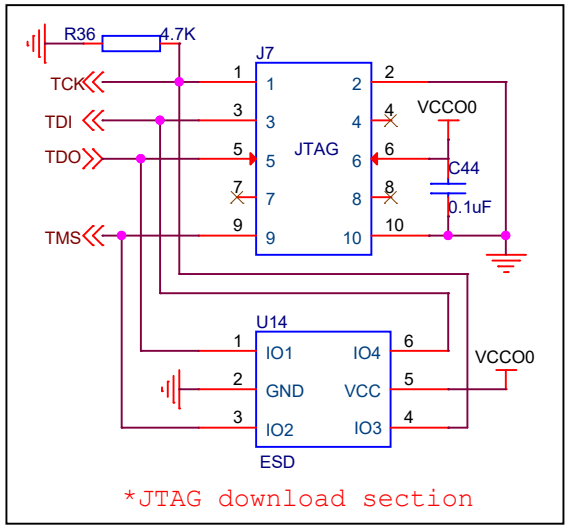








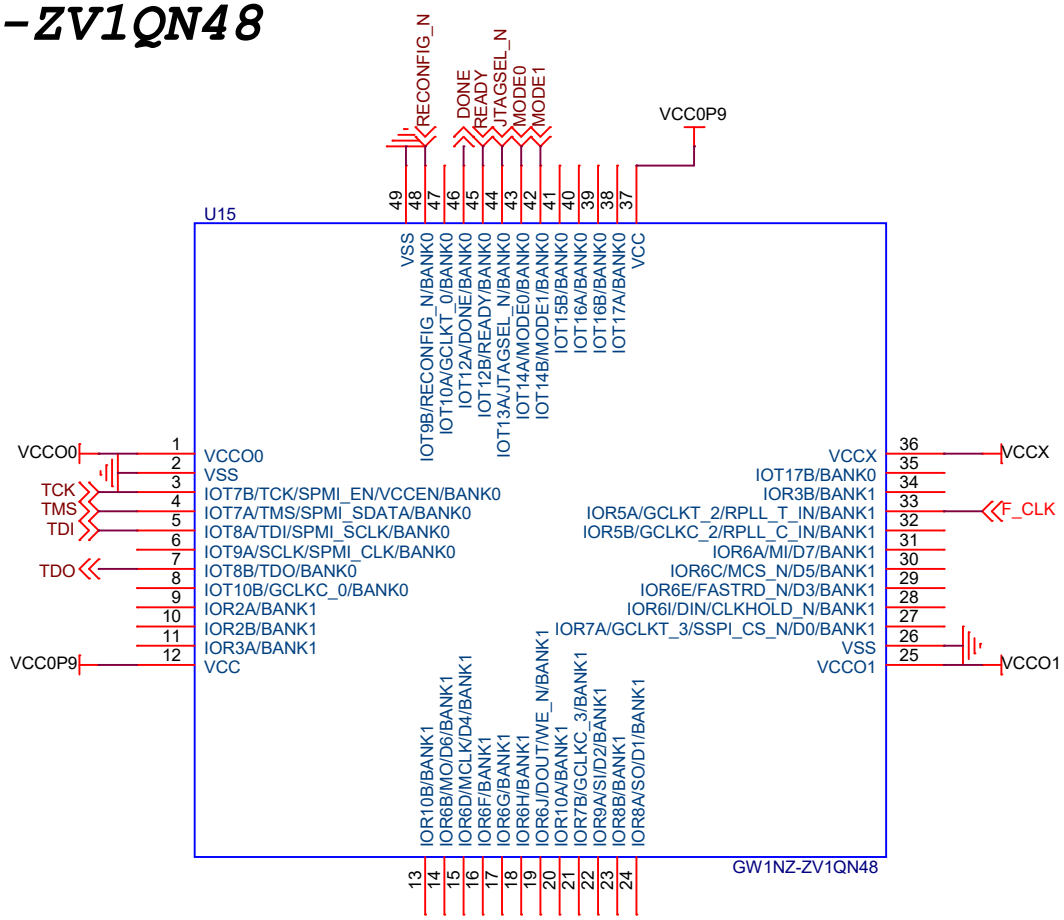


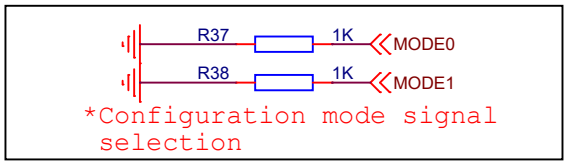


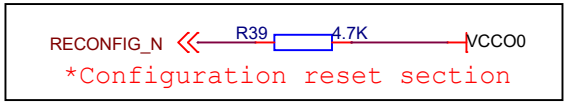
Notes:
1.F_CLK signal is an external input clock signal.
  It is recommended that F_CLK signal be provided through an active oscillator crystal.
2.It is recommended that add an ESD protection chip to the JTAG download circuit.

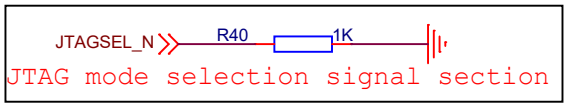
Title	GOWIN Minimum System Diagram	
Size A4	Document Number GW1NZ-ZV1FN32F	Rev 2.0
Date: Wednesday, April 26, 2023	Sheet 7 of 8	

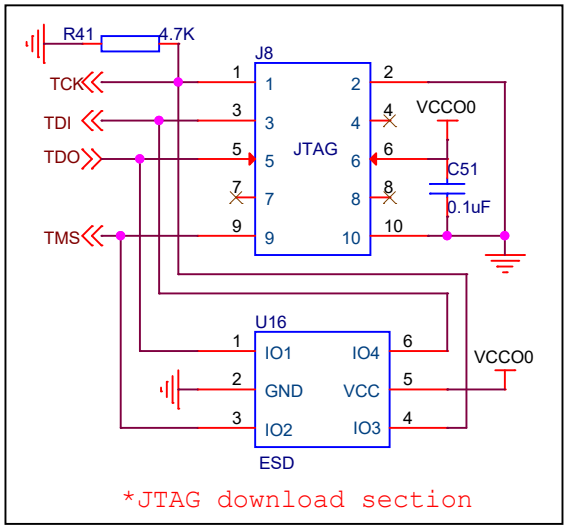
GW1NZ-ZV1QN48

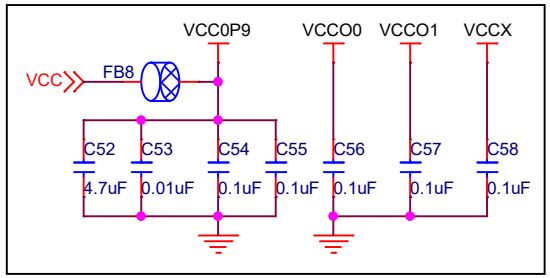


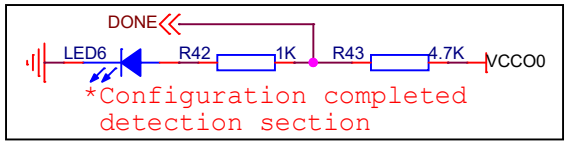


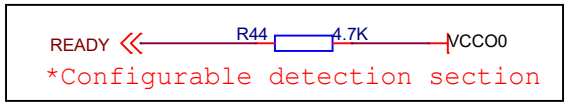












Notes:
1.F_CLK signal is an external input clock signal.
  It is recommended that F_CLK signal be provided through an active oscillator crystal.
2.It is recommended that add an ESD protection chip to the JTAG download circuit.

Title	GOWIN Minimum System Diagram	
Size A4	Document Number GW1NZ-ZV1QN48	Rev 2.0
Date: Wednesday, April 26, 2023	Sheet 8 of 8	