GW5A-LV138UG324A

Configuration mode signal selection

Flash
SPI Flash
External Flash, used to store downloaded programs

JTAG download section

Configuration reset section

Configurable detection section

Configuration completed detection section

Configurable Bank voltage section

Configurable GPIO mode section
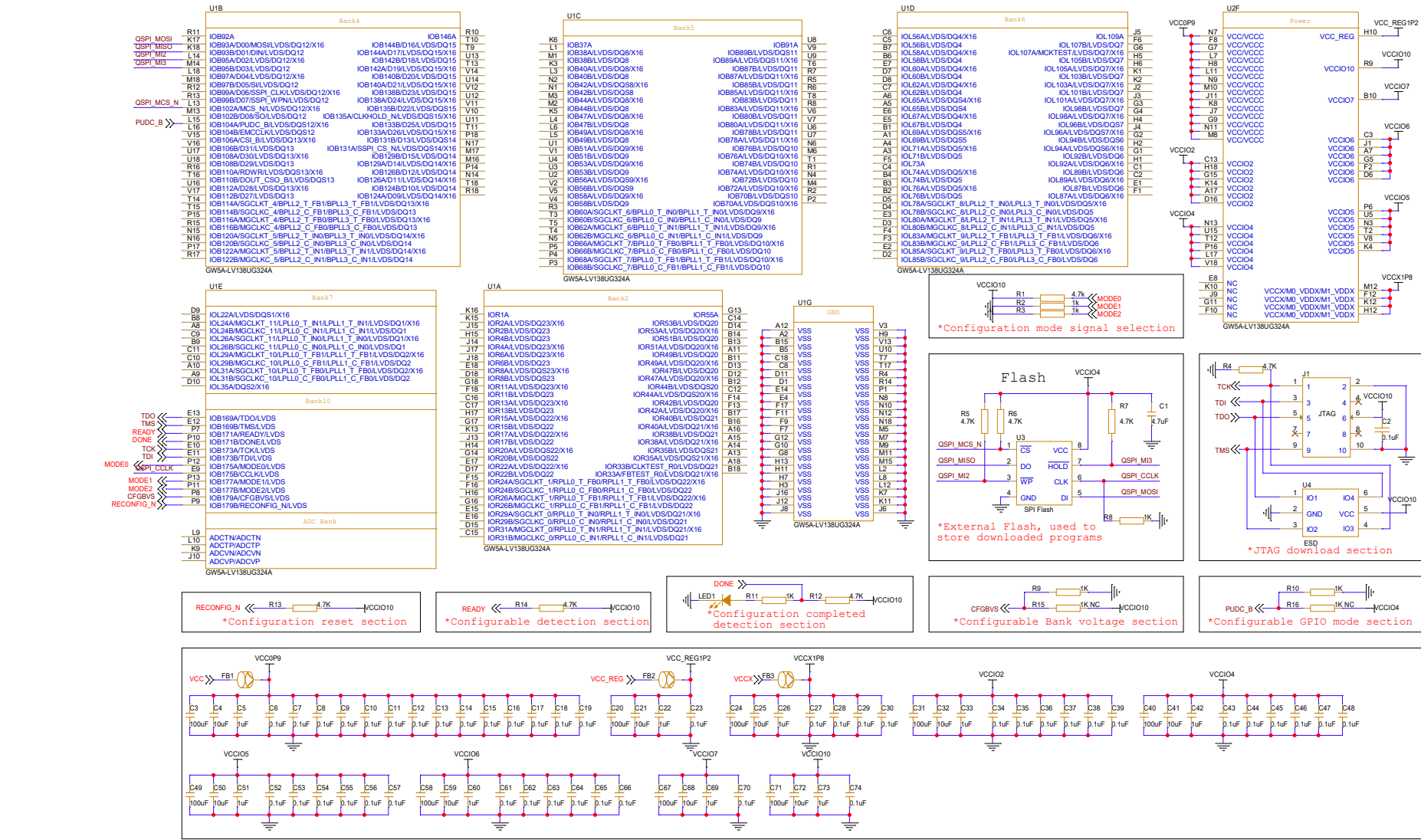
Notes:
1. F_CLK signal is an external input clock signal.
   It is recommended that F_CLK signal be provided through an active oscillator crystal.
2. External Flash memory is used to store downloaded programs.
   For details about SPI Flash model selection, see Chapter 10 SPI Flash Selection in UG290.
3. It is recommended that add an ESD protection chip to the JTAG download circuit.
4. VCC core voltage requires a large current, so it is recommended to supply power separately.