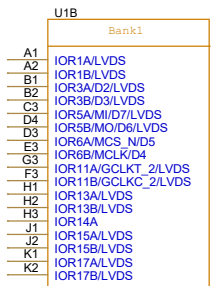
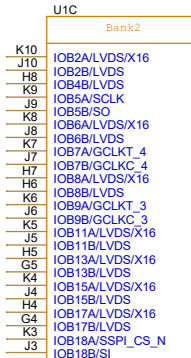


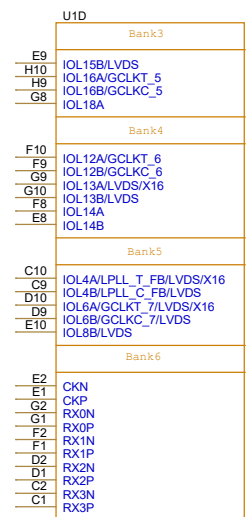
GW1NZ-2K-CS100H



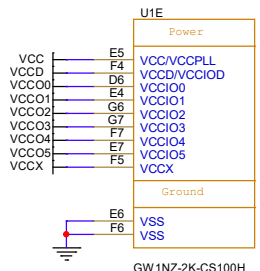
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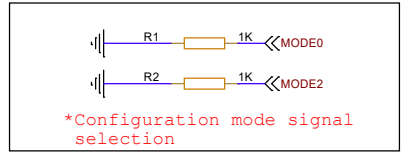
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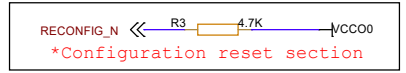
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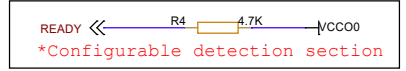
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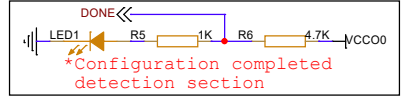
*Configuration mode signal selection



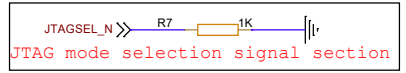
*Configuration reset section



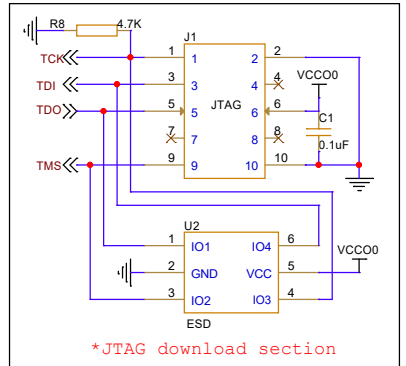
*Configurable detection section



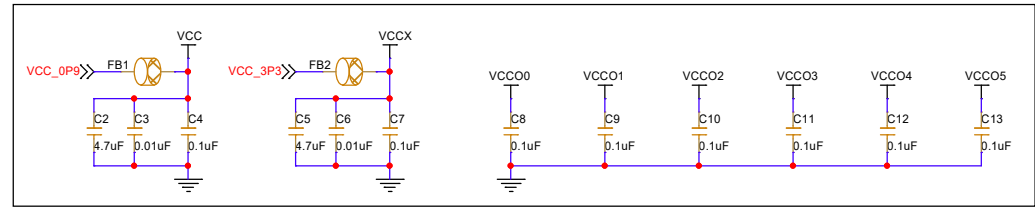
*Configuration completed detection section



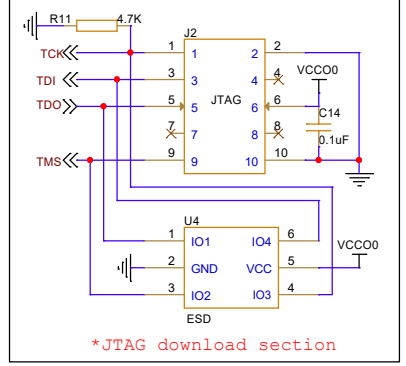
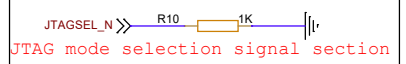
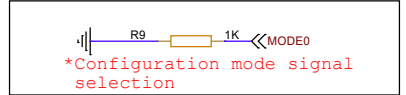
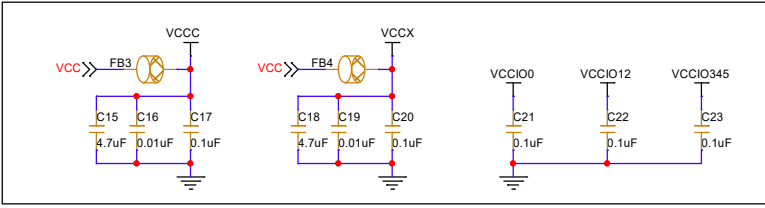
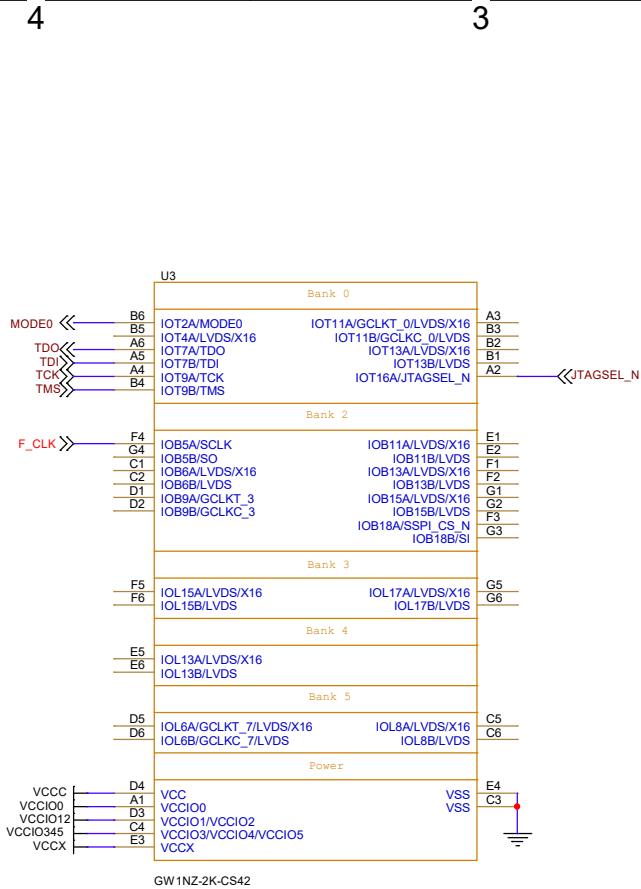
JTAG mode selection signal section



*JTAG download section



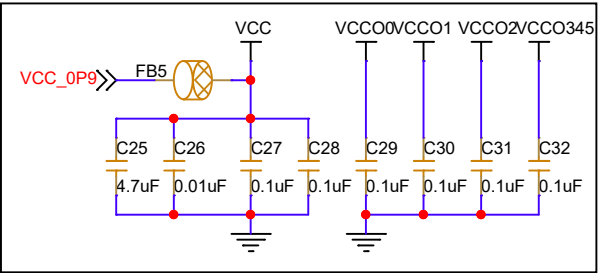
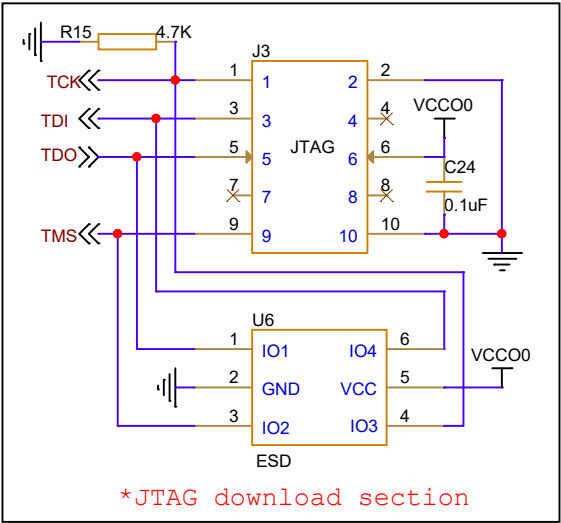
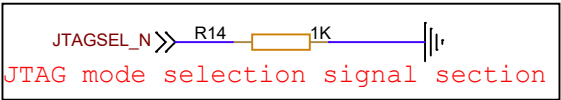
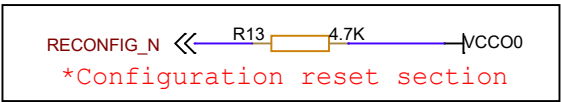
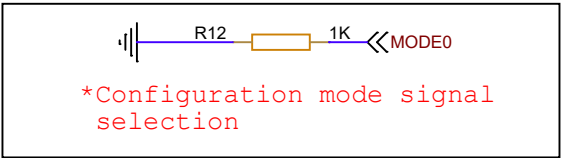
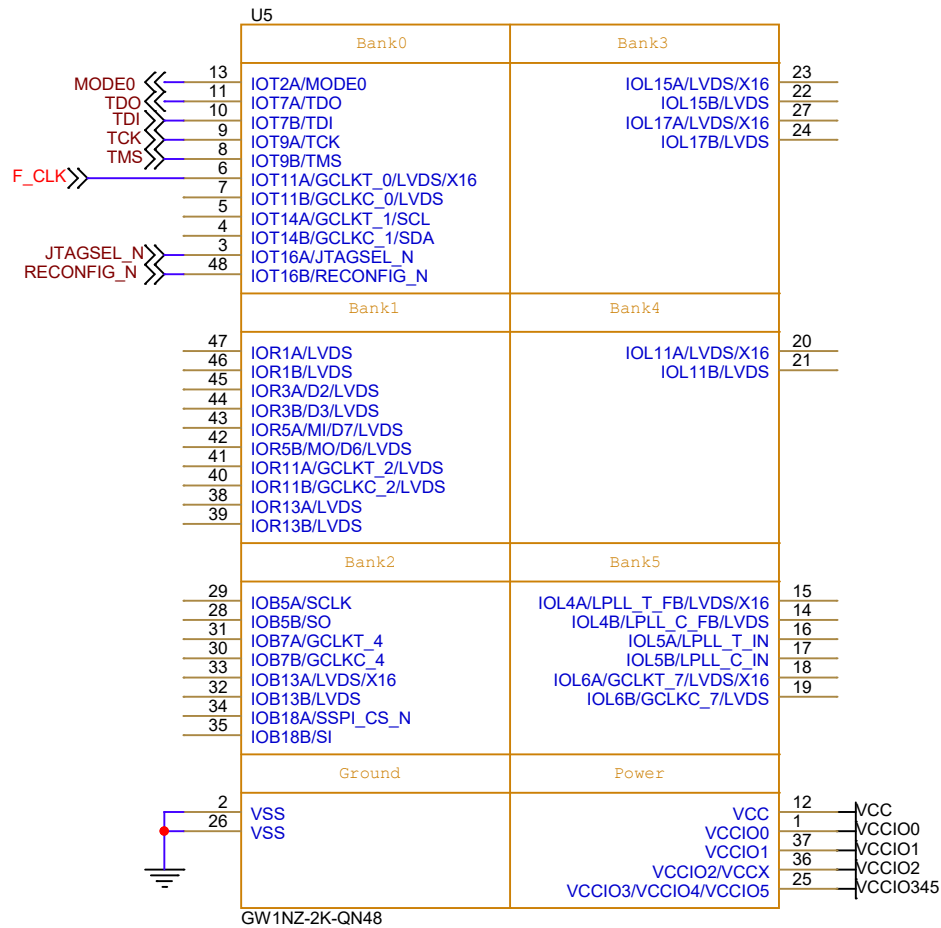
Notes:
1.F_CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
2.It is recommended that add an ESD protection chip to the JTAG download circuit.



Notes:

- F CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
- It is recommended that add an ESD protection chip to the JTAG download circuit.

GW1NZ-2K-QN48



- Notes:
- 1.F_CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
 - 2.It is recommended that add an ESD protection chip to the JTAG download circuit.