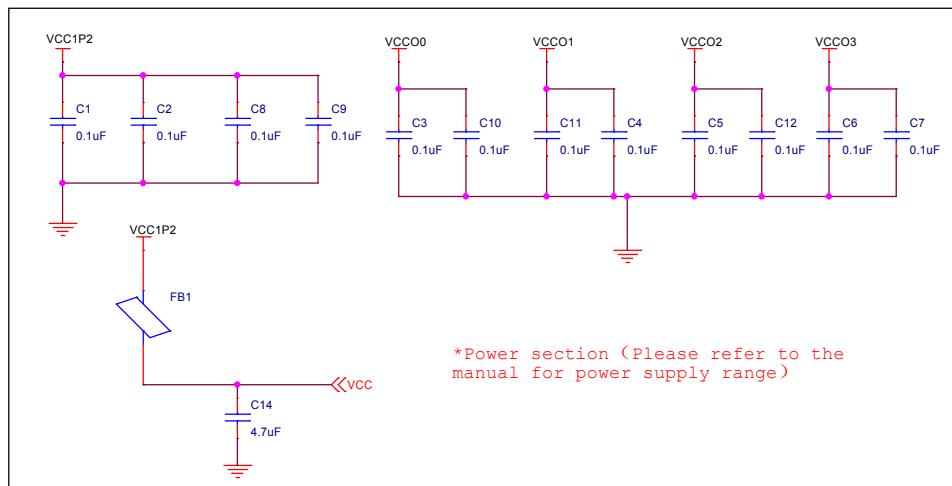
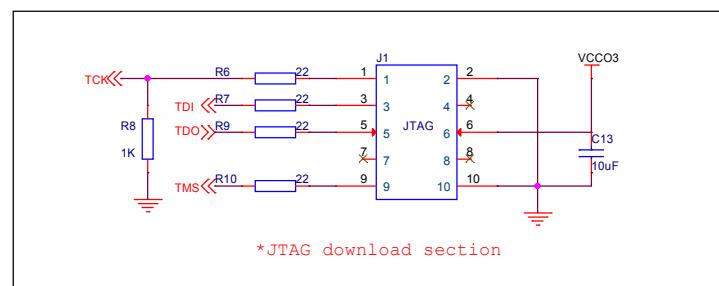
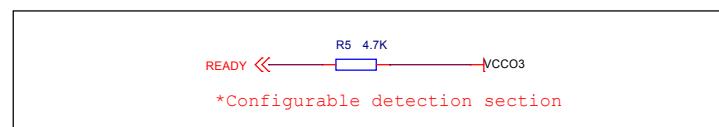
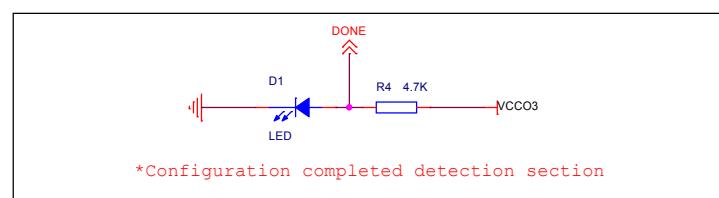
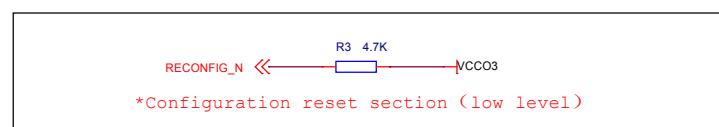
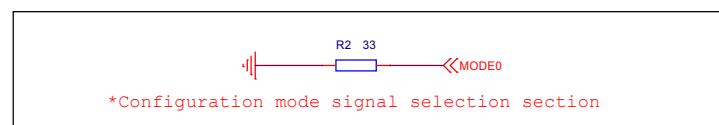
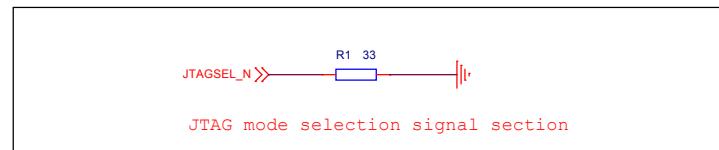


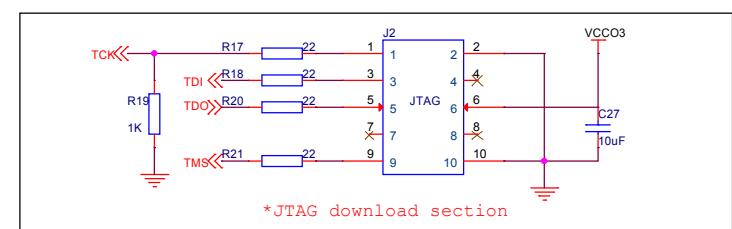
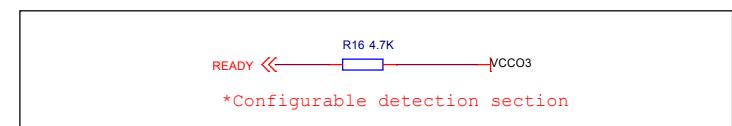
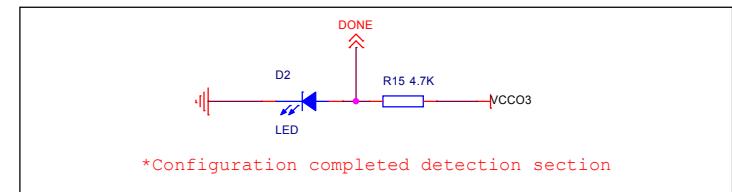
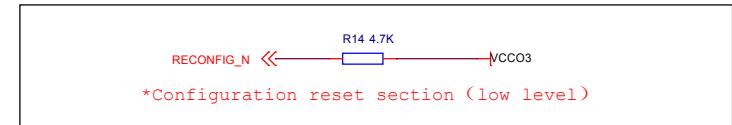
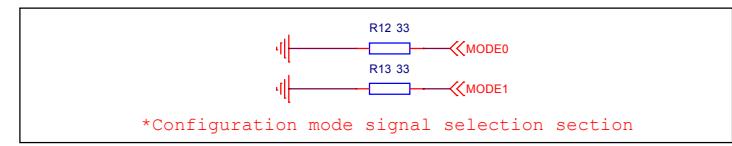
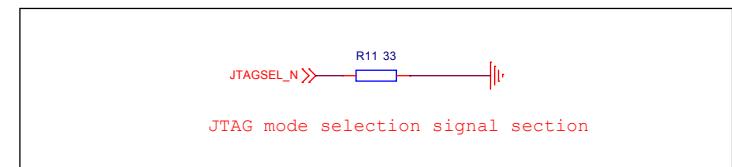
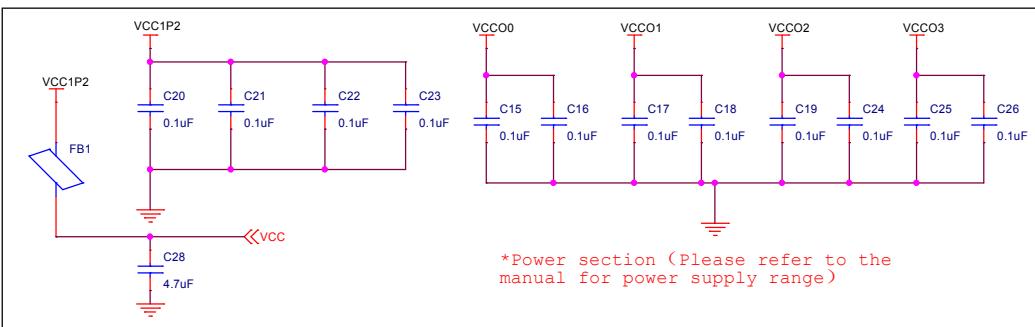
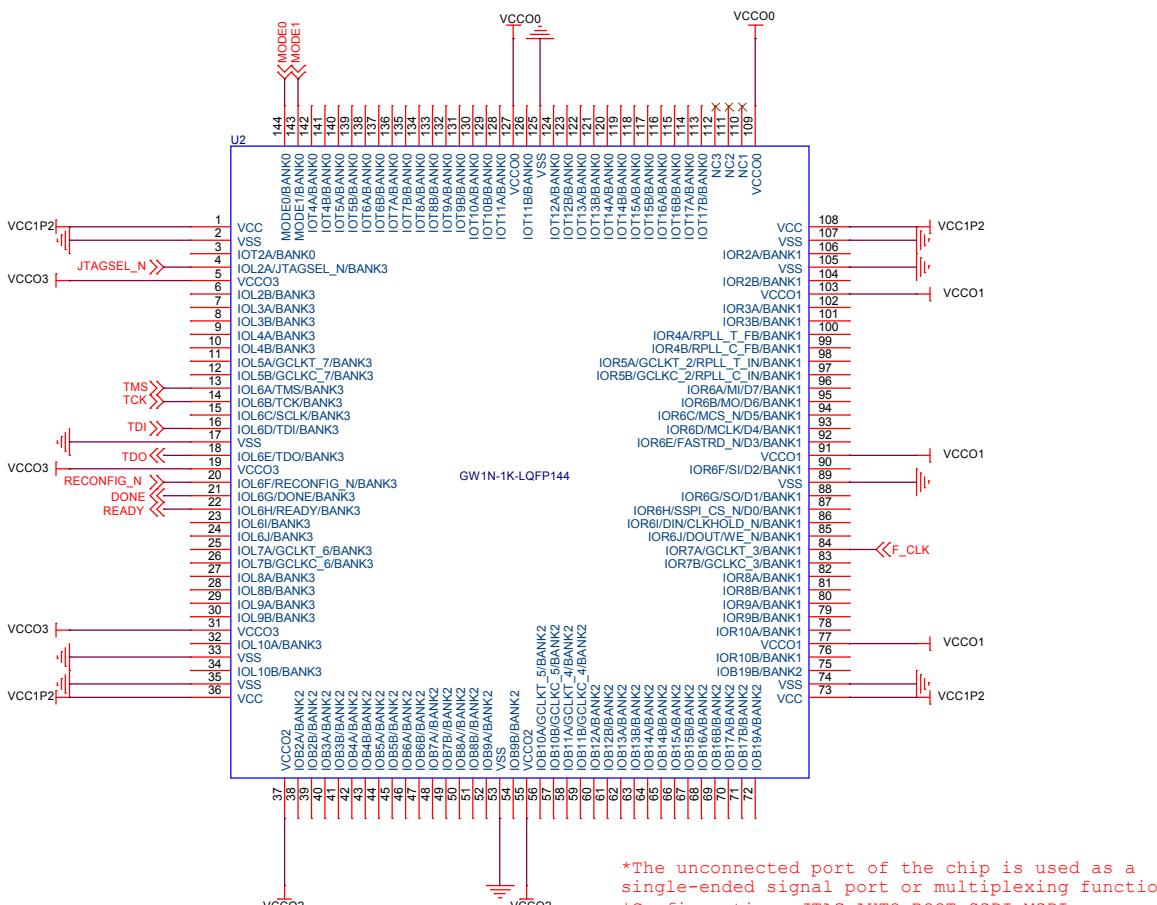
*The unconnected port of the chip is used as a single-ended signal port or multiplexing function
 *Configuration: JTAG.AUTO BOOT.SSPI



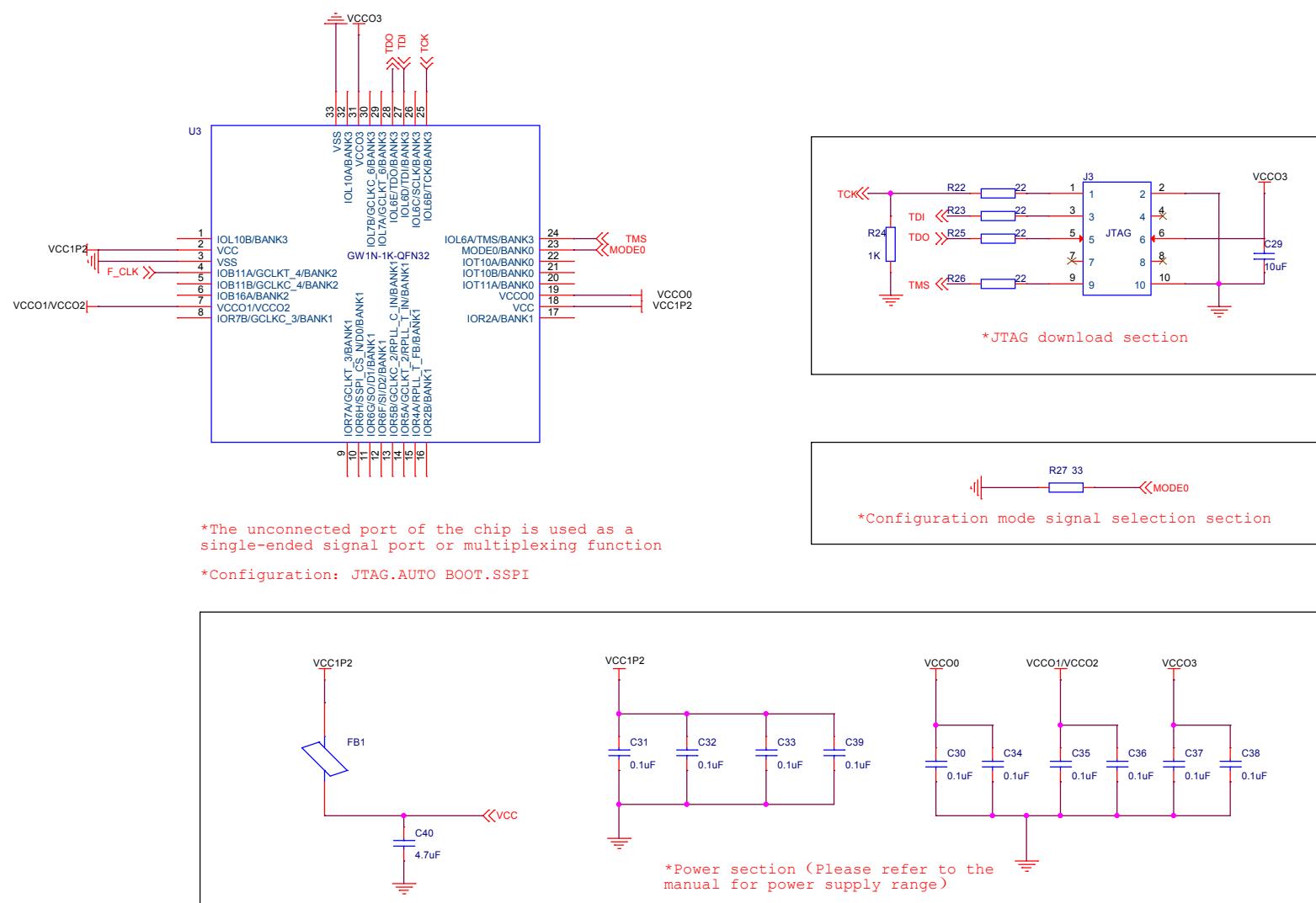
*Power section (Please refer to the manual for power supply range)



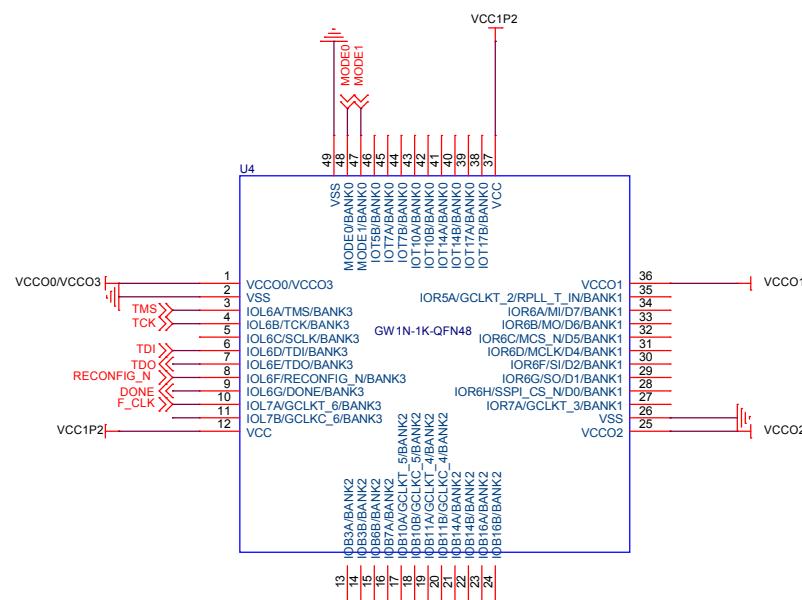
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Size	Document Number	Rev
A3	GW1N-LV1-LQFP100	1.0
Date:	Tuesday, September 28, 2021	Sheet 1 of 5



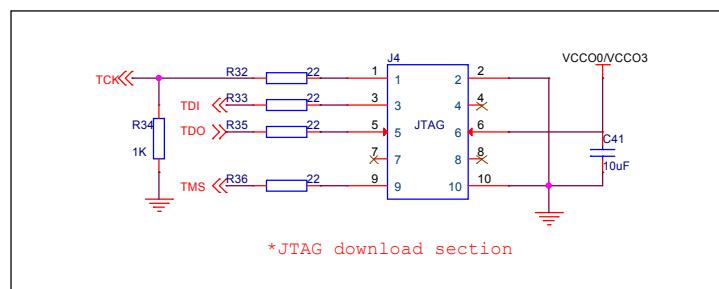
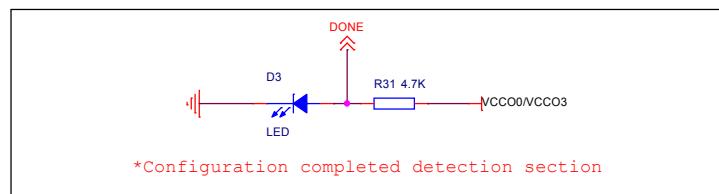
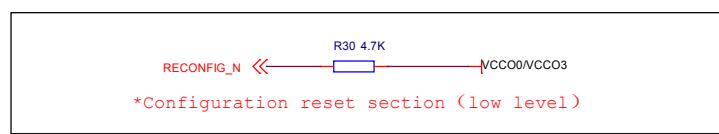
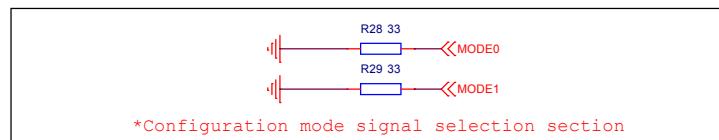
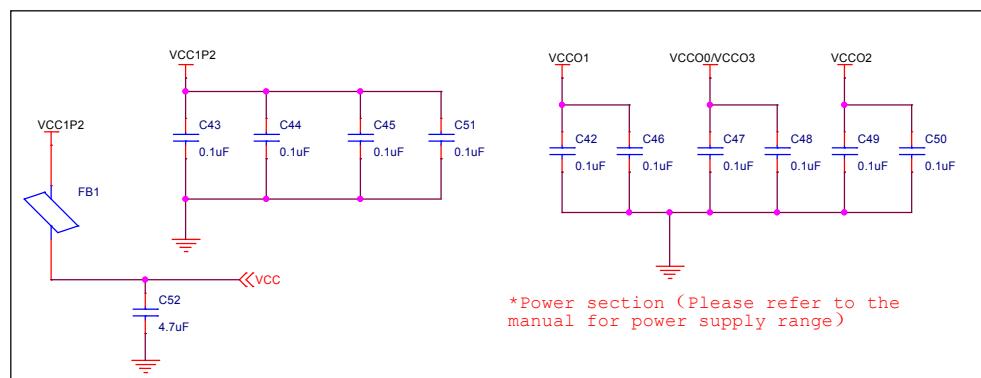
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Date:	Monday, September 27, 2021	Rev	1.0



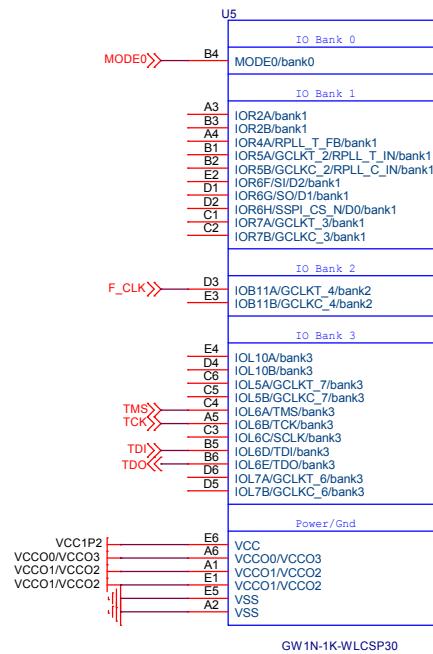
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Size A3	Document Number GW1N-LV1-QFN32	Rev 1.0
Date: Monday, September 27, 2021		Sheet 1 of 5



*The unconnected port of the chip is used as a single-ended signal port or multiplexing function
*Configuration: JTAG.AUTO BOOT.SSPI.MSPI



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*The unconnected port of the chip is used as a single-ended signal port or multiplexing function

*Configuration: JTAG.AUTO BOOT.SSPI

