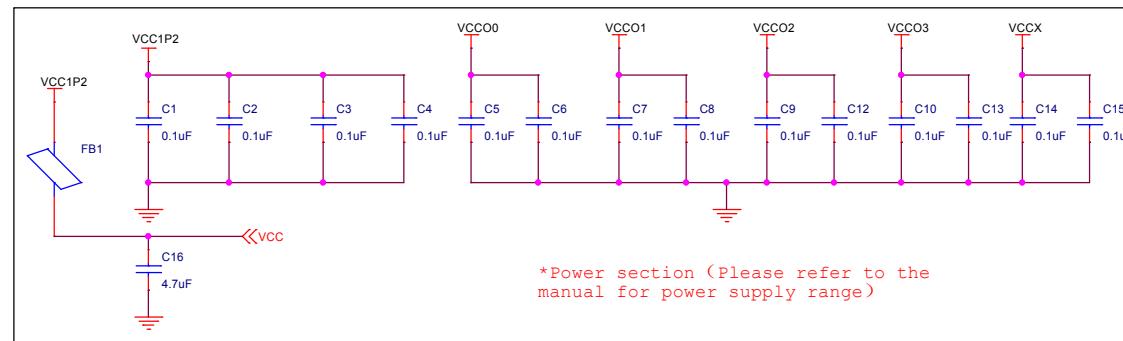
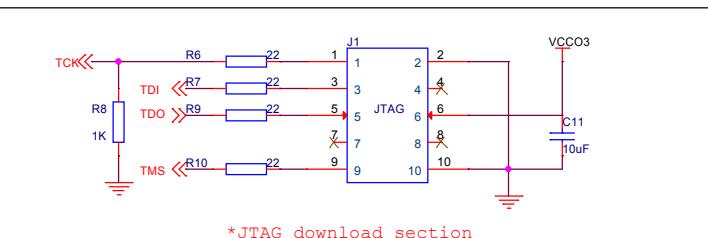
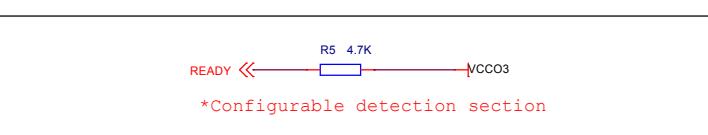
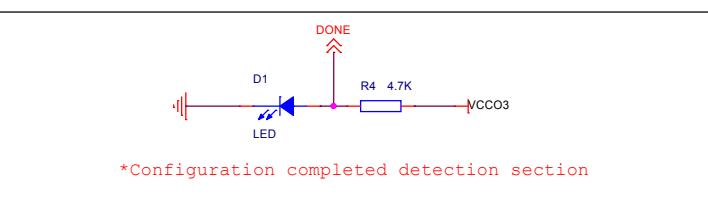
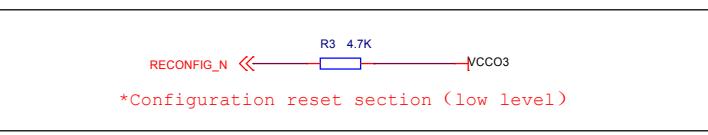
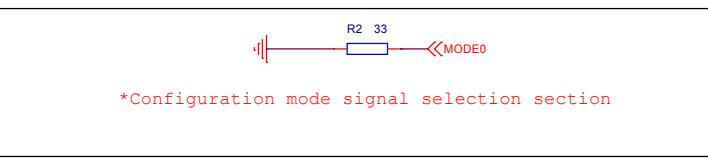
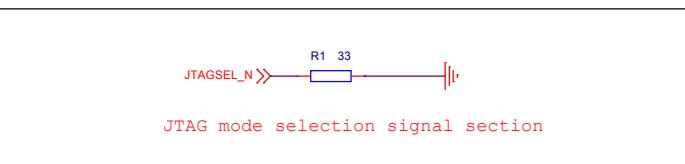
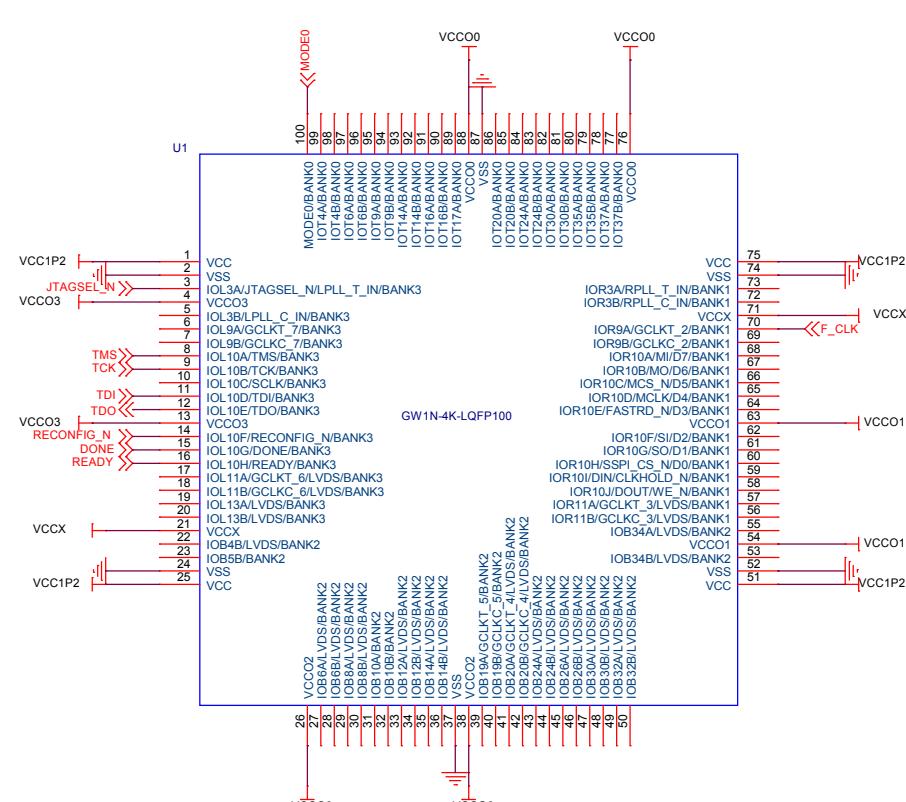
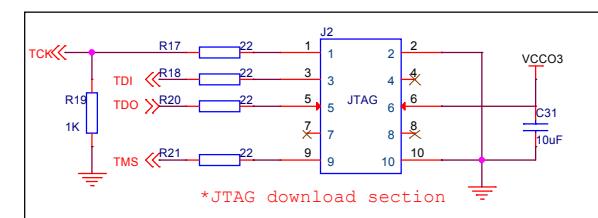
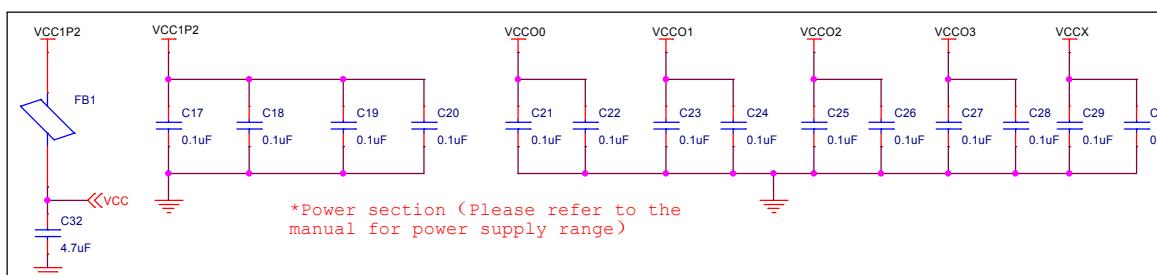
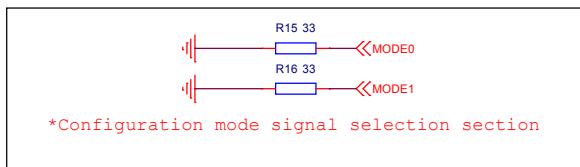
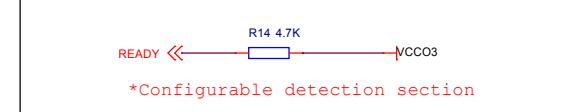
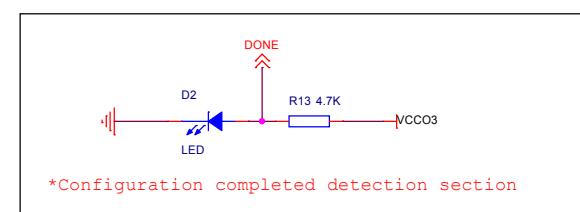
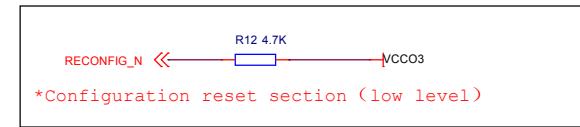
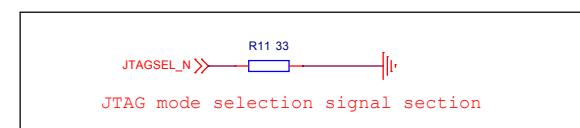
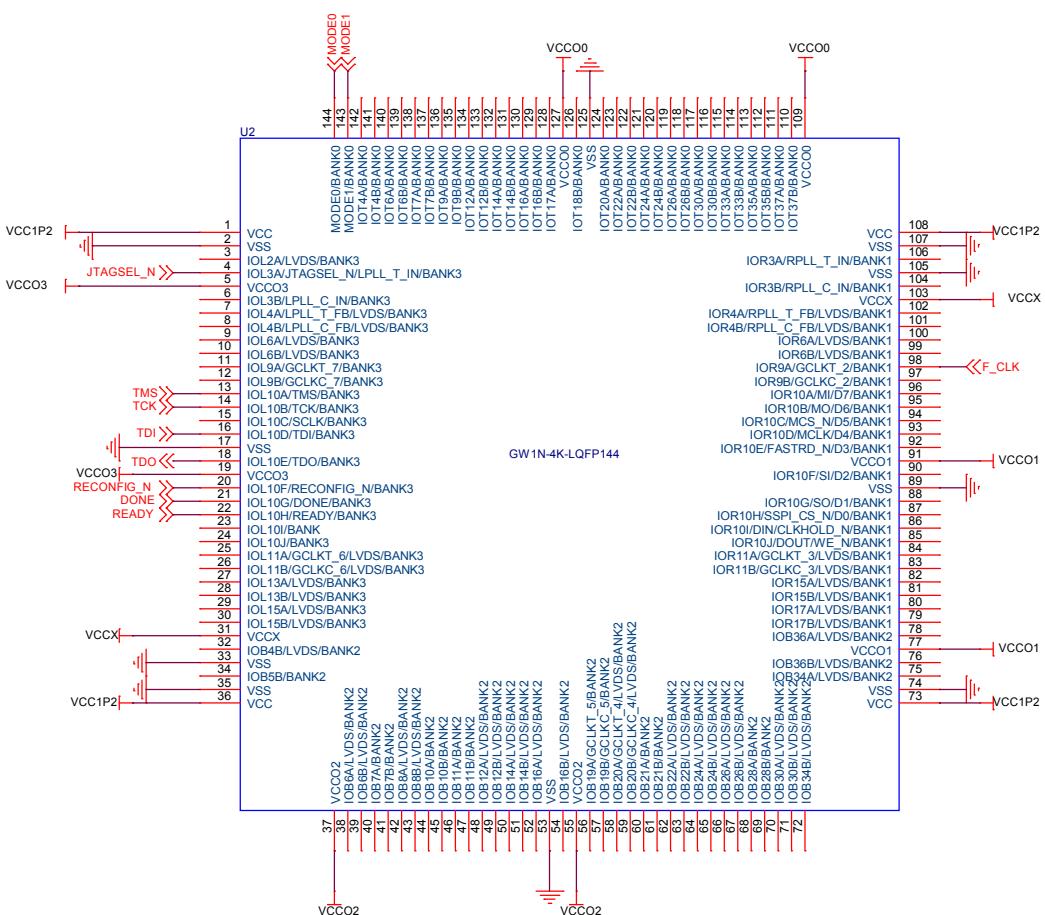
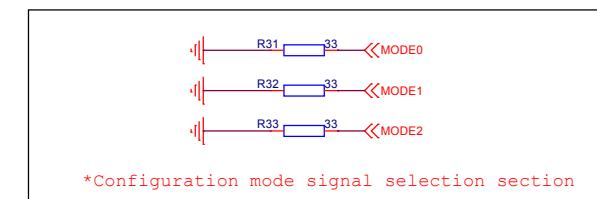
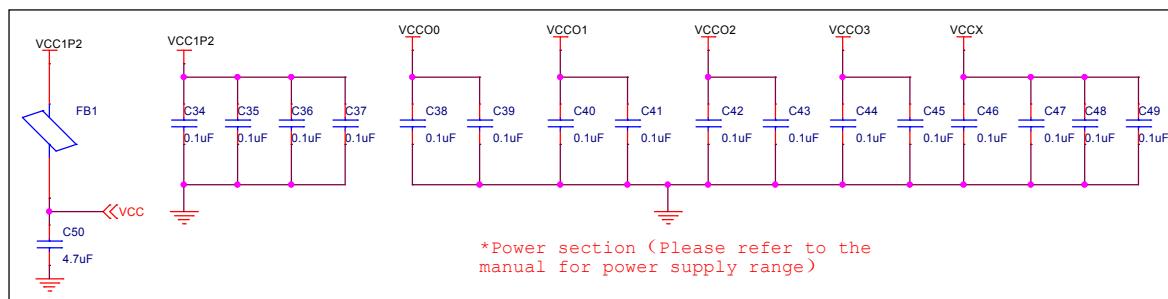
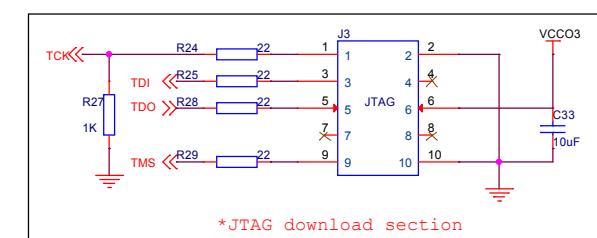
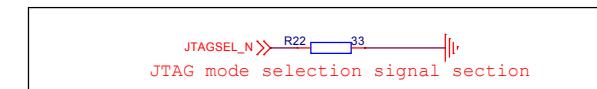
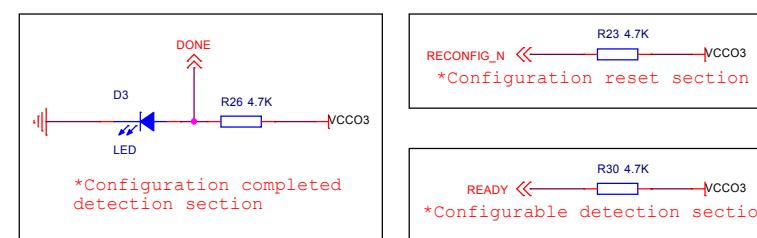
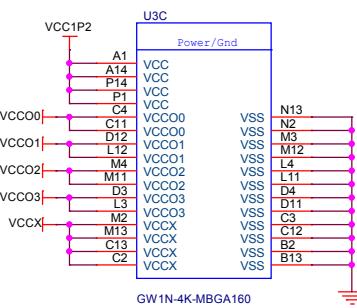
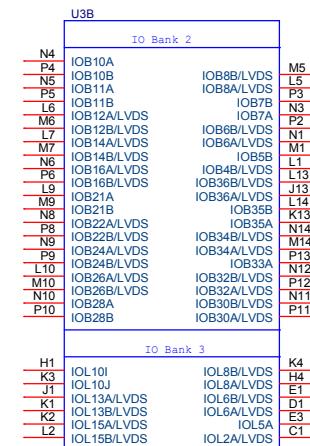
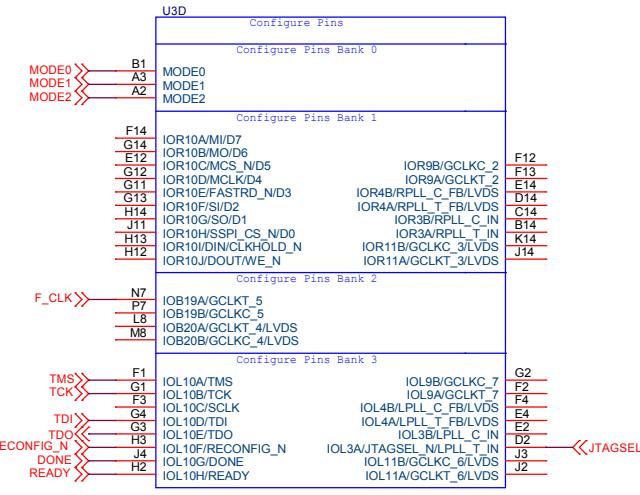
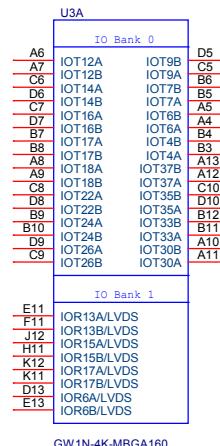


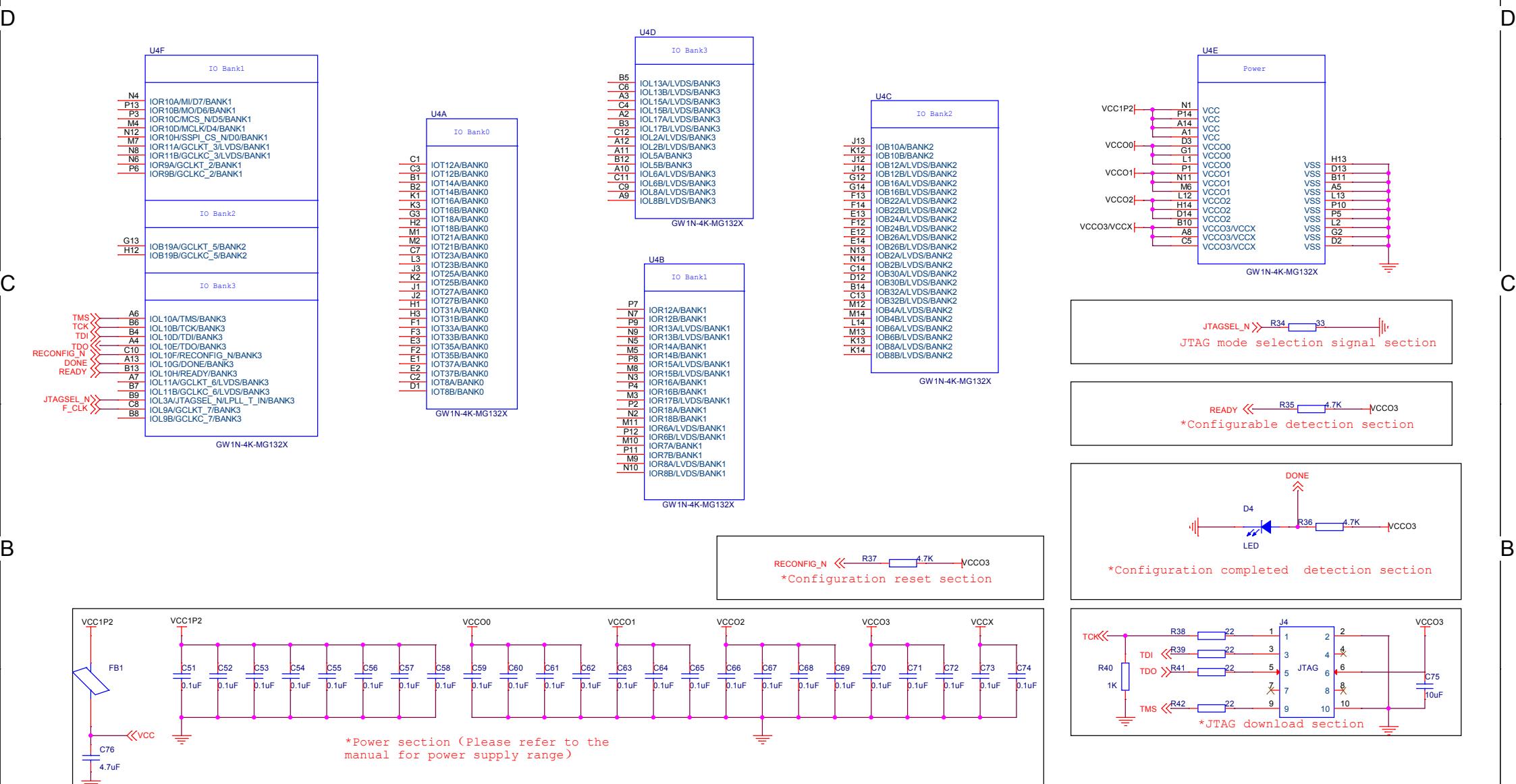
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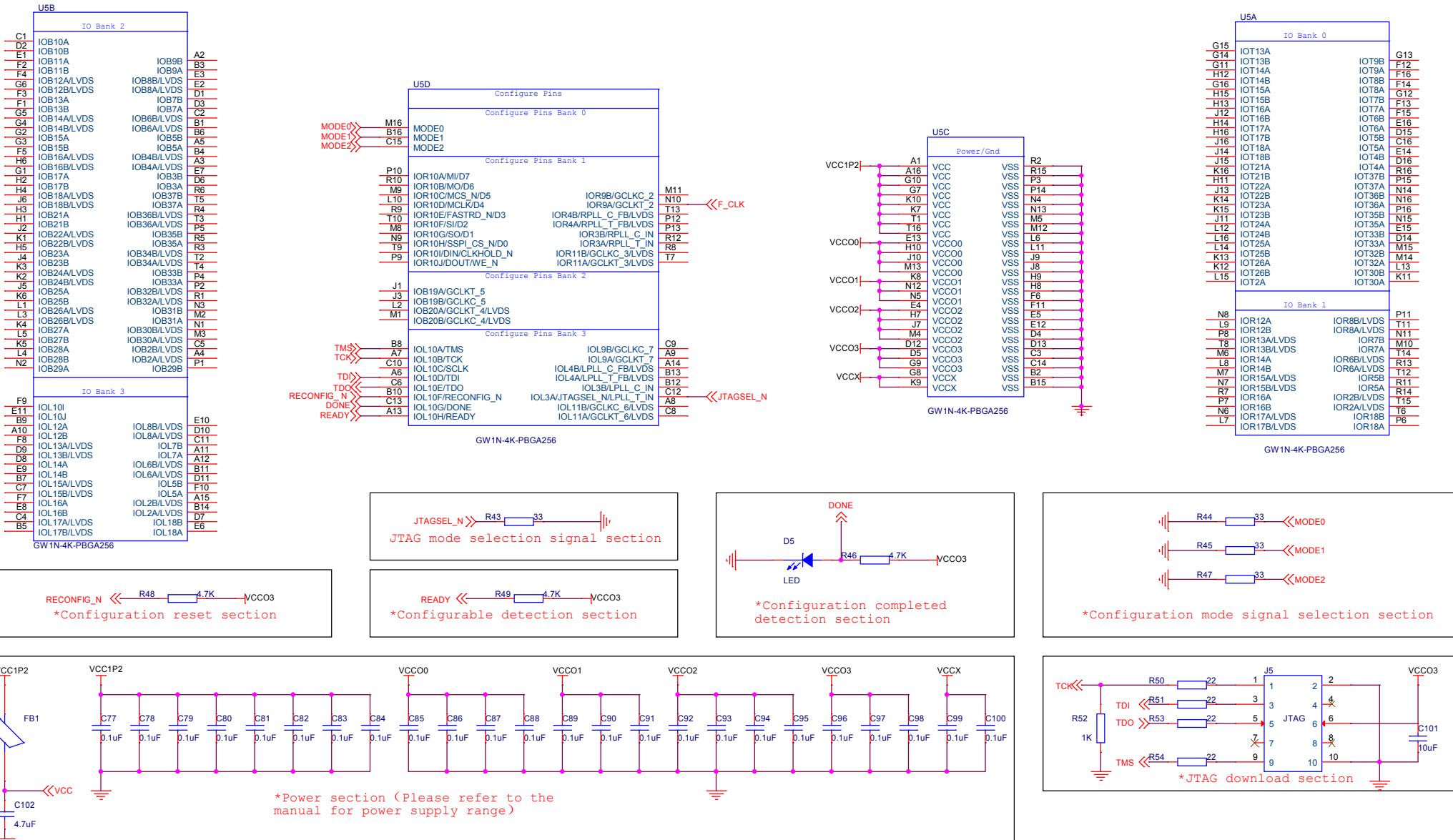


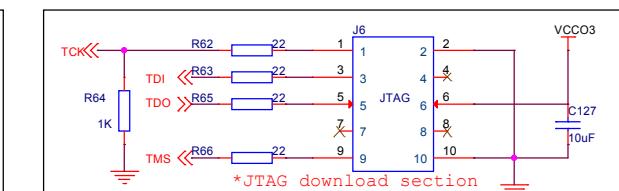
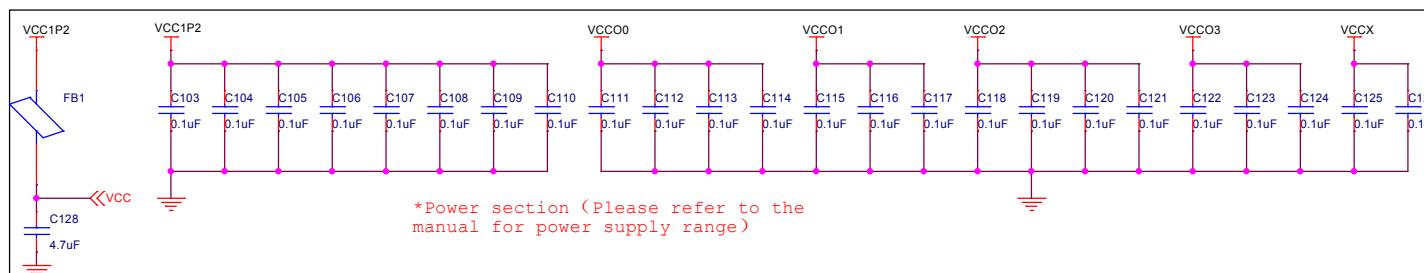
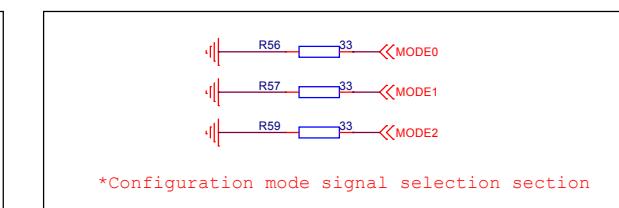
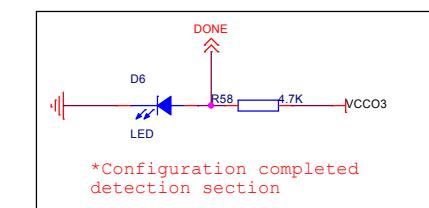
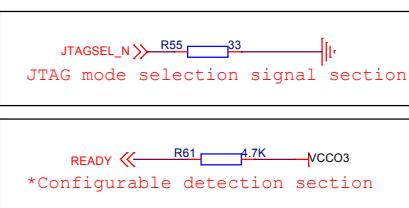
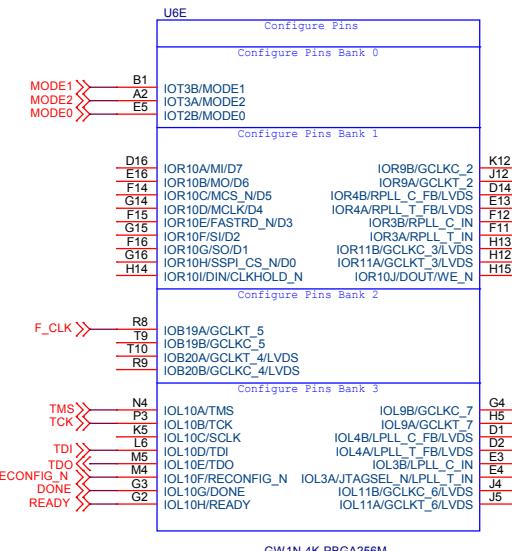


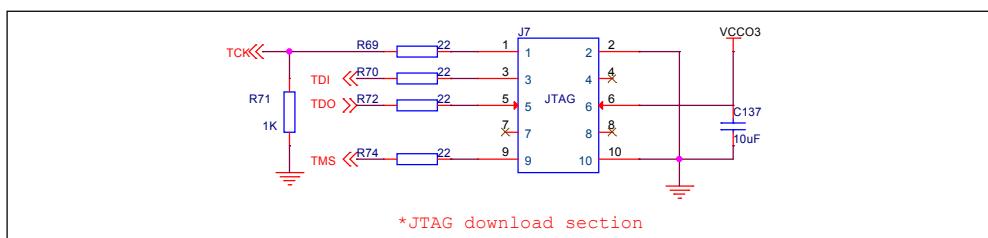
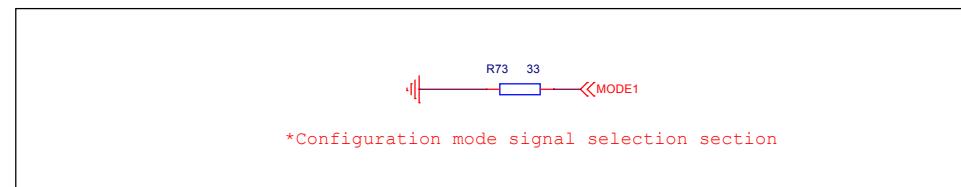
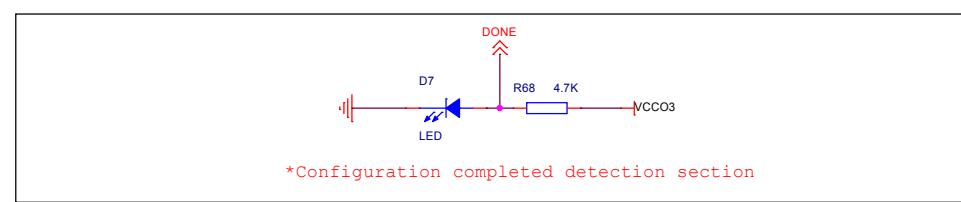
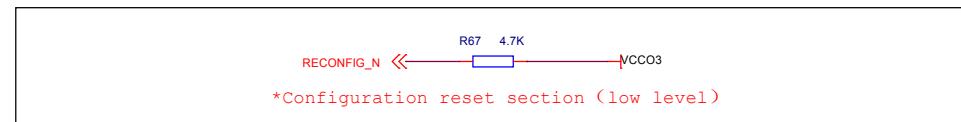
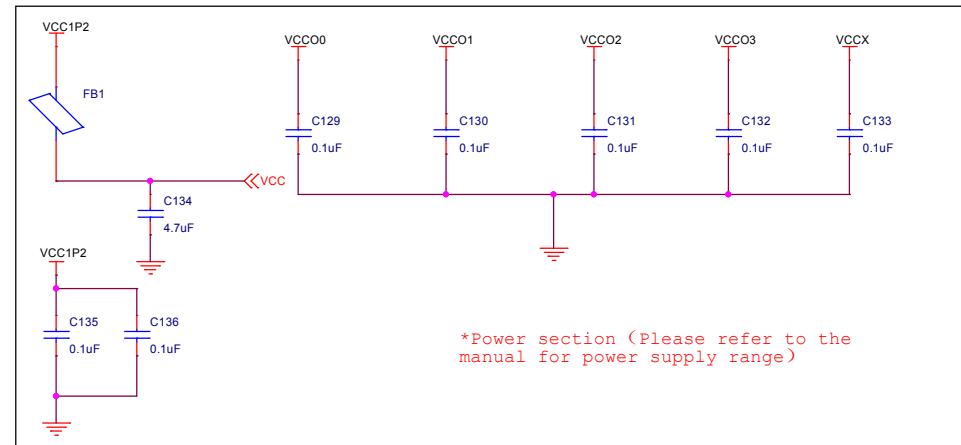
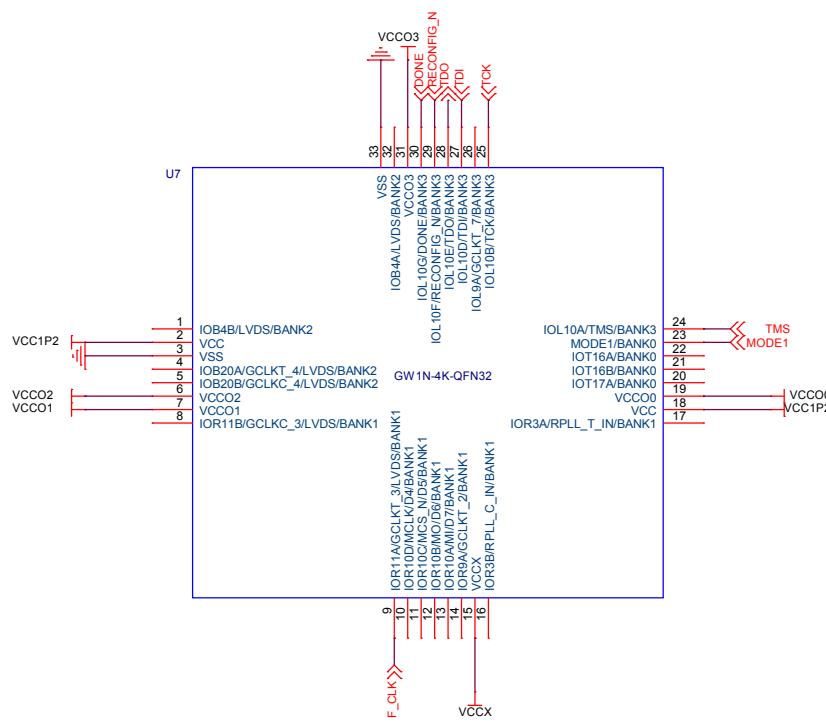
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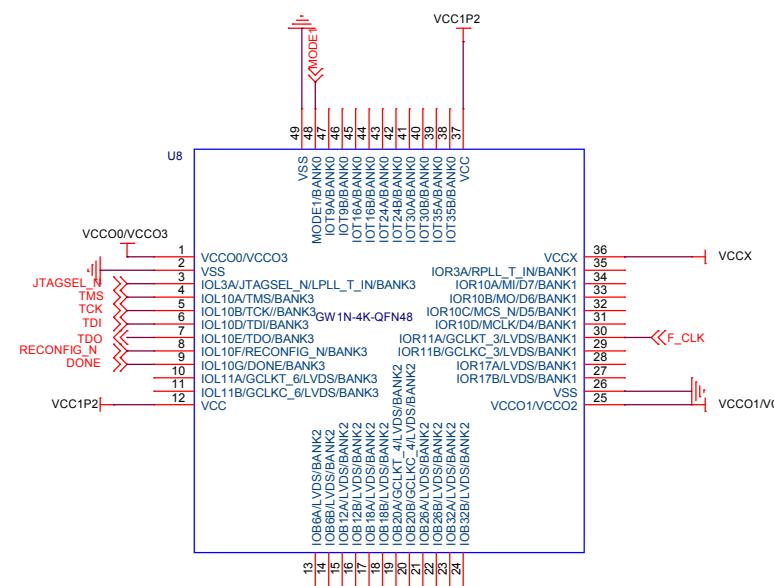




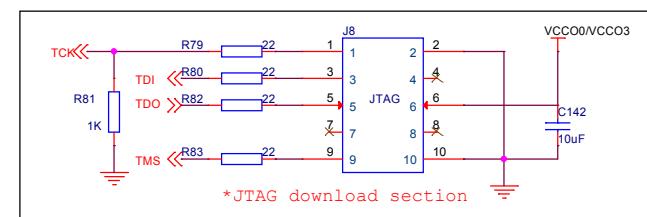
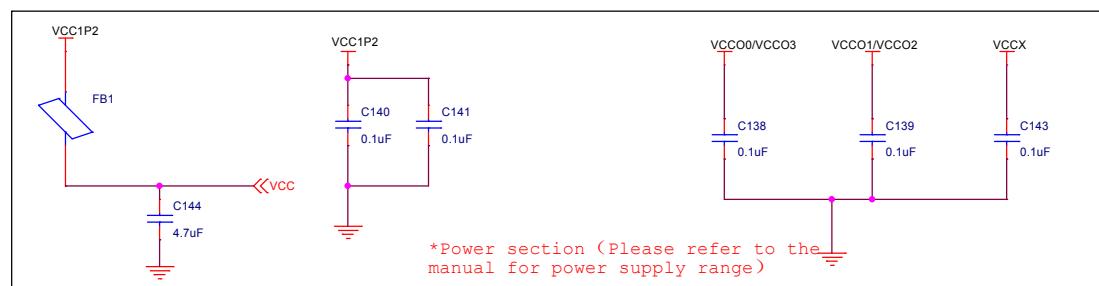
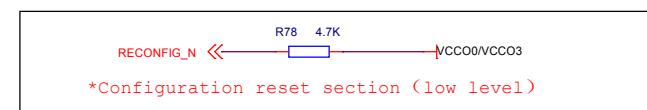
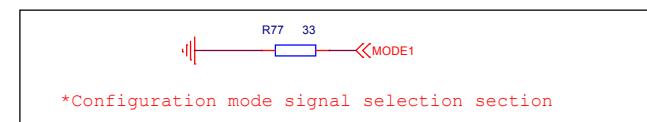
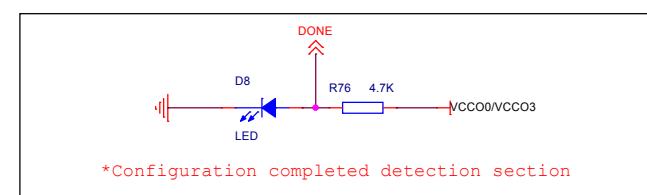
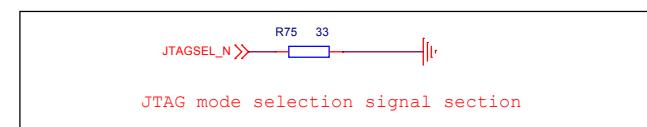




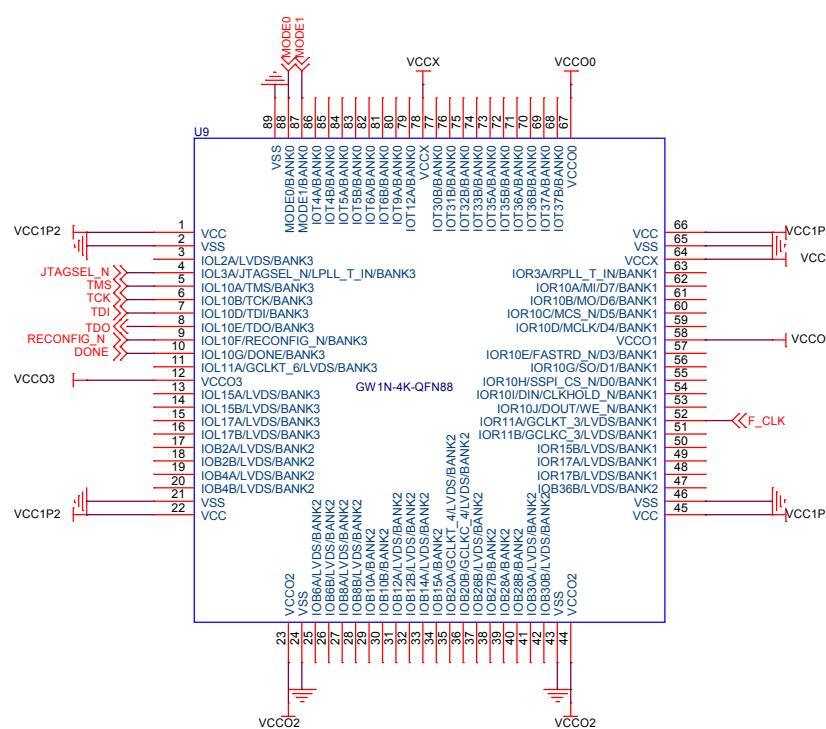
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\*The unconnected port of the chip is used as a single-ended signal port or multiplexing function  
\*Configuration: JTAG.AUTO BOOT.MSPI



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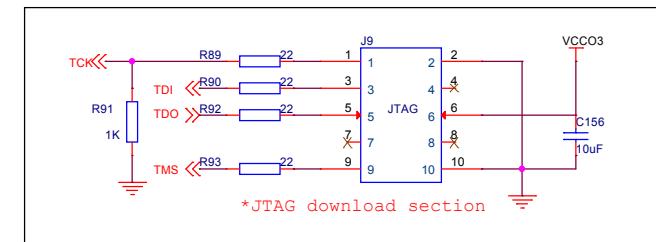
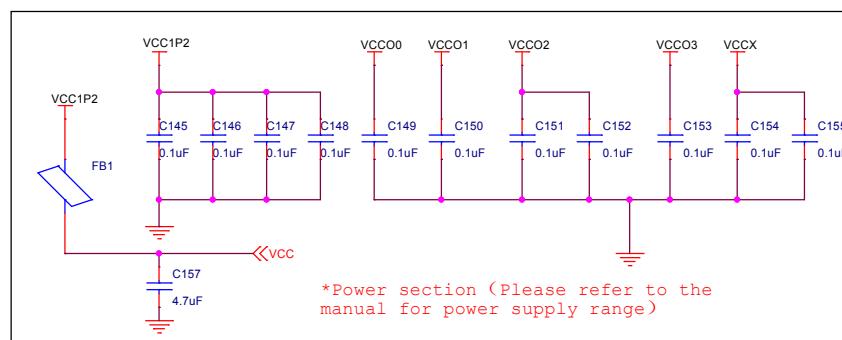
RECONFIG\_N ↳ R84 4.7K → VCCO3  
\*Configuration reset section (low level)

DONE ↳ D9 LED → R85 4.7K → VCCO3  
\*Configuration completed detection section

R86 33 ↳ MODE0  
R87 33 ↳ MODE1  
\*Configuration mode signal selection section

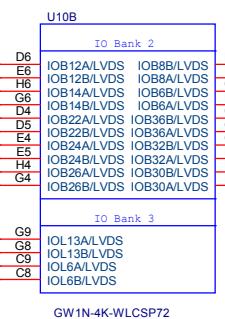
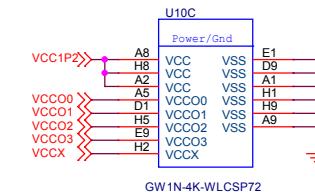
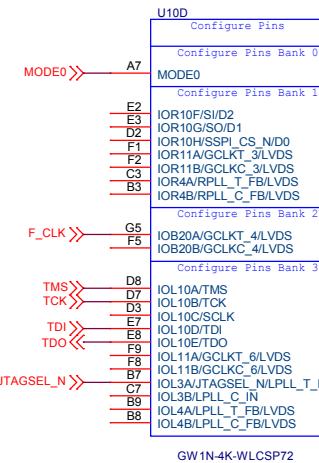
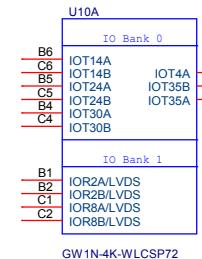
JTAGSEL\_N ↳ R88 33 →

JTAG mode selection signal section

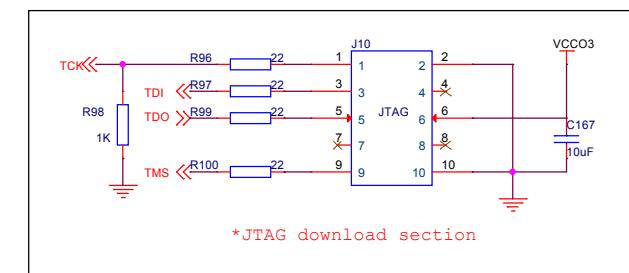
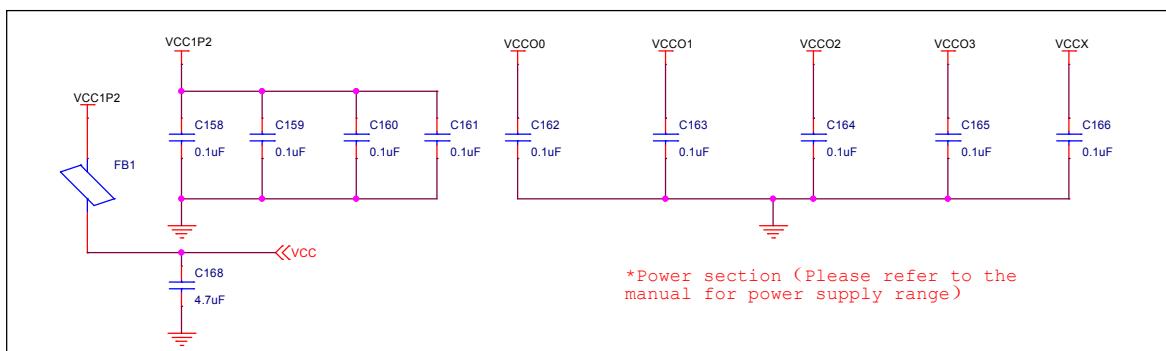
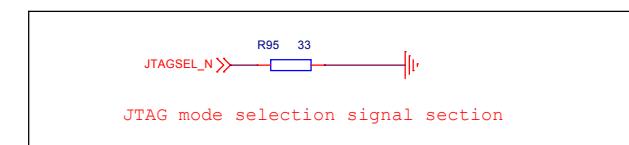
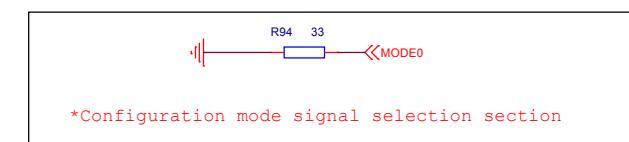


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