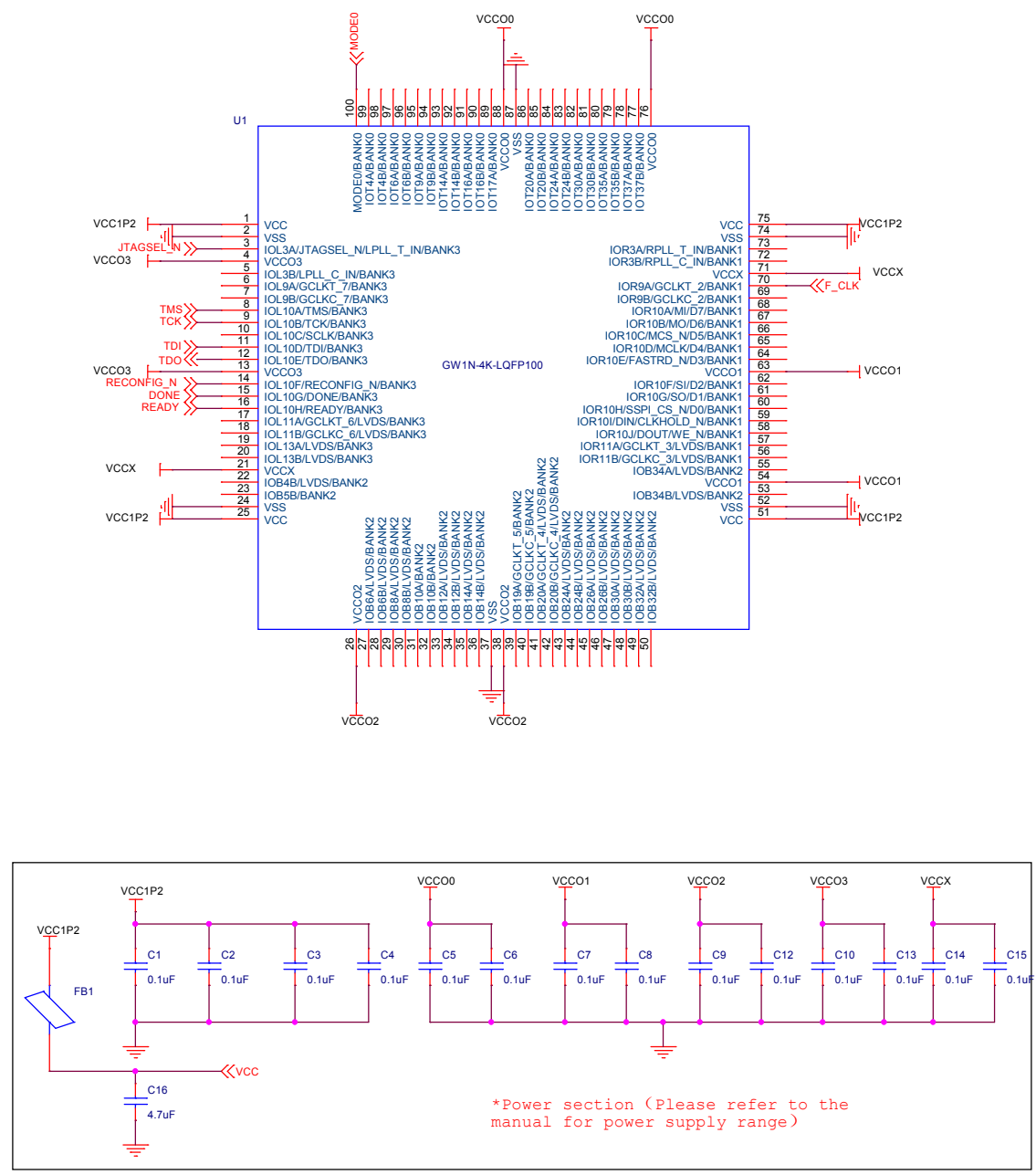


D

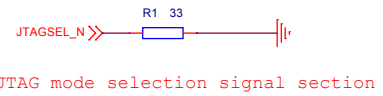
C

B

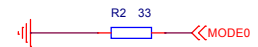
A



*Power section (Please refer to the manual for power supply range)



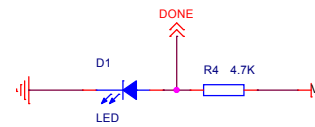
JTAG mode selection signal section



*Configuration mode signal selection section



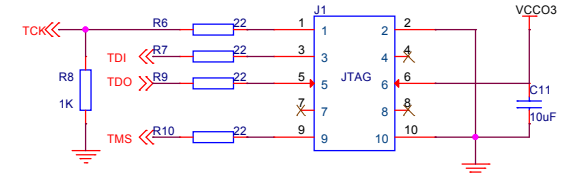
*Configuration reset section (low level)



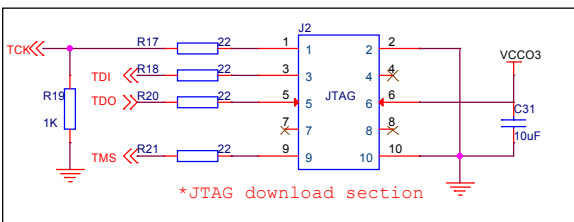
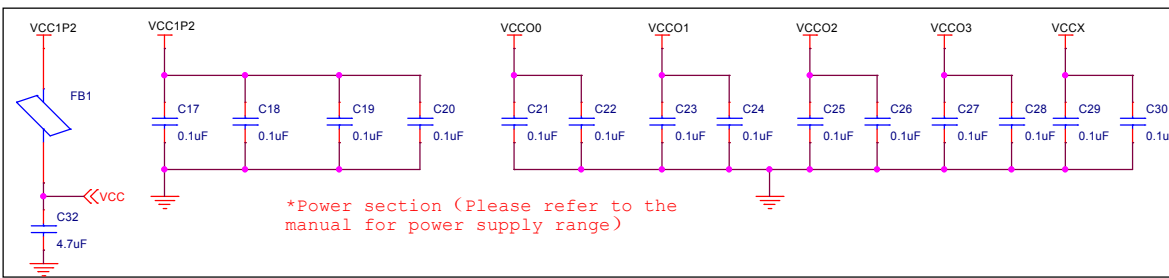
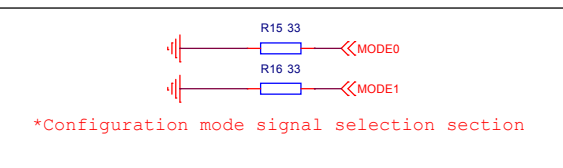
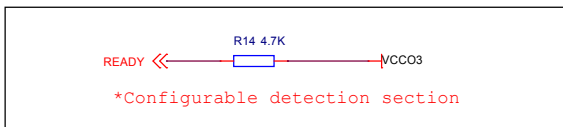
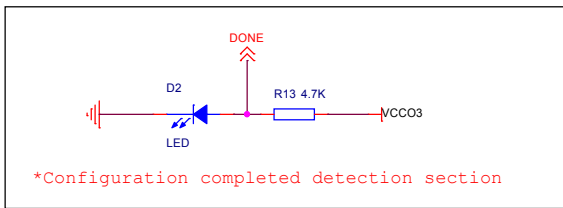
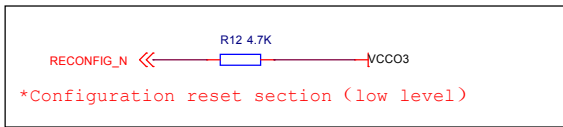
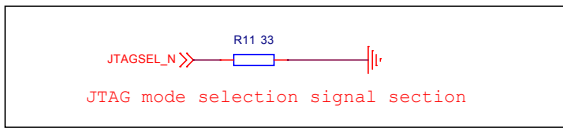
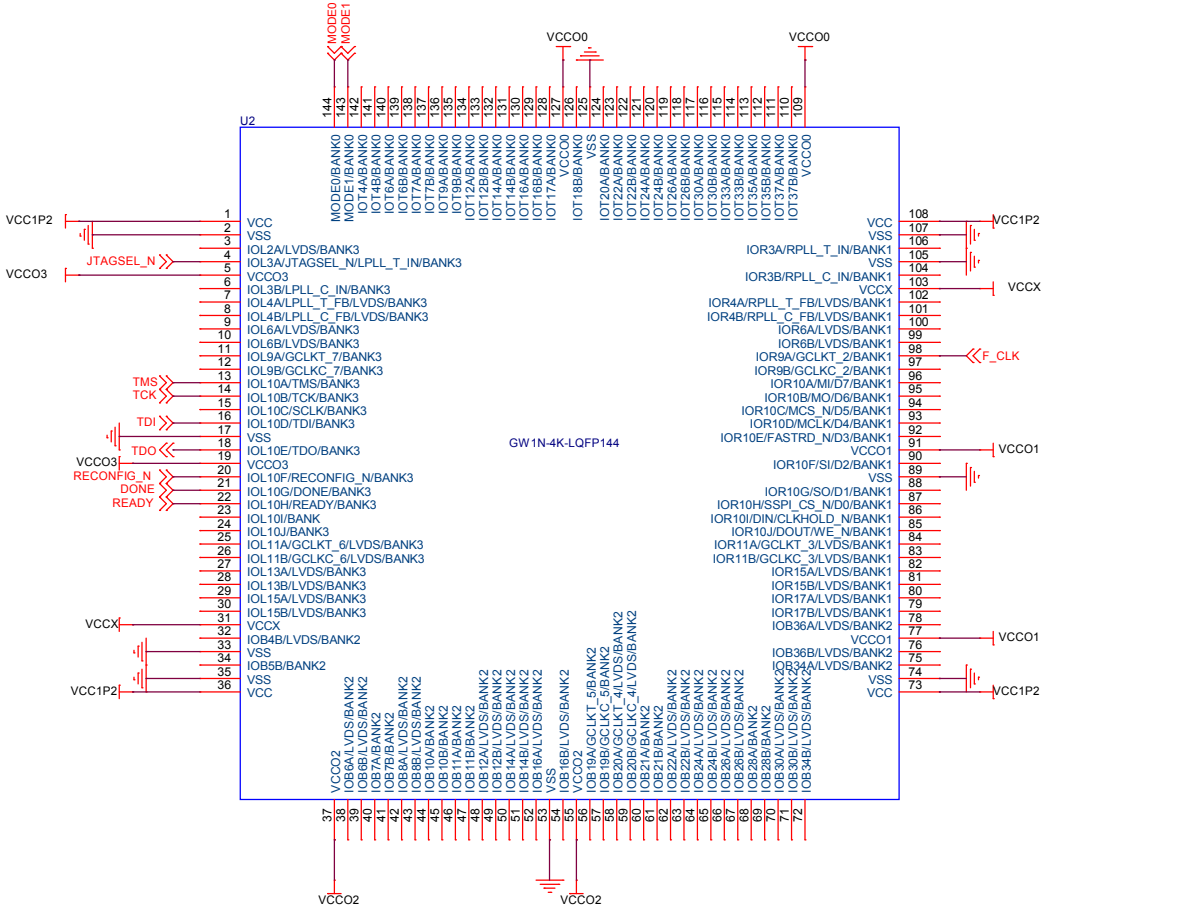
*Configuration completed detection section



*Configurable detection section



*JTAG download section

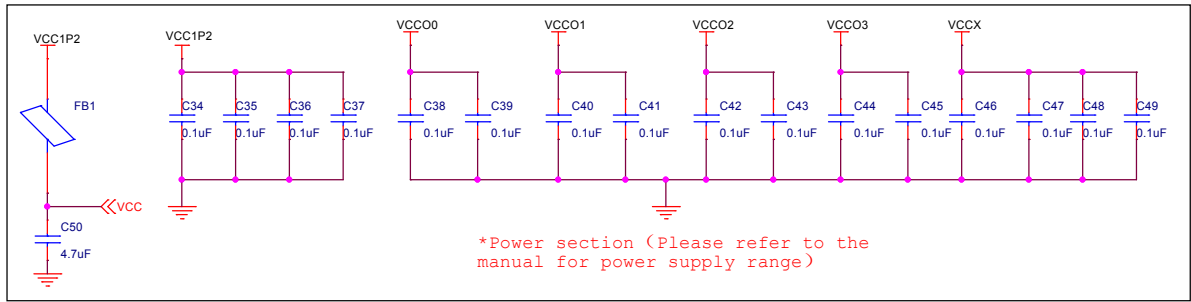
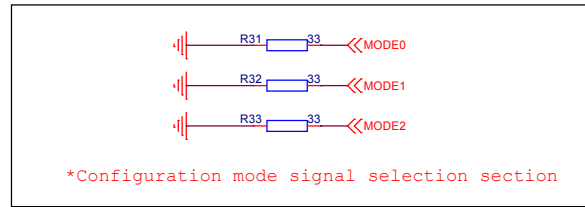
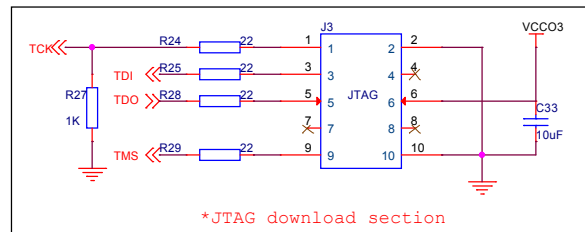
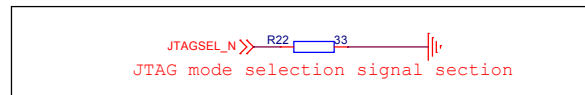
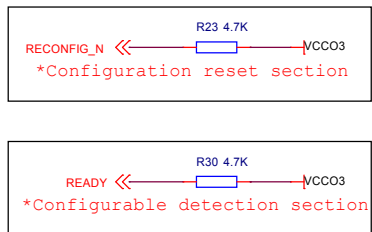
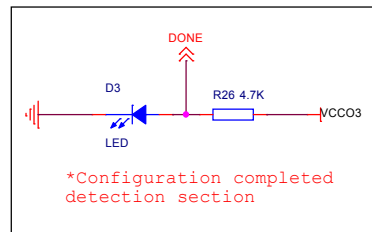
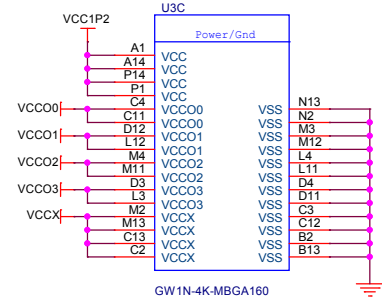
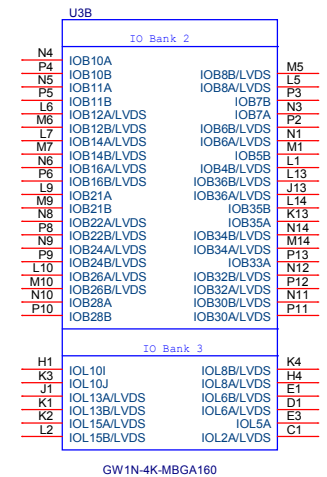
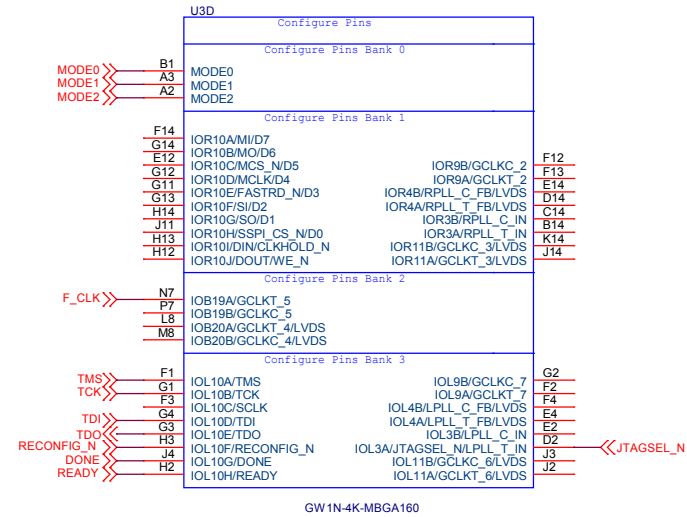
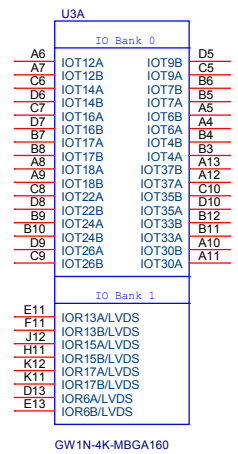


D

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B

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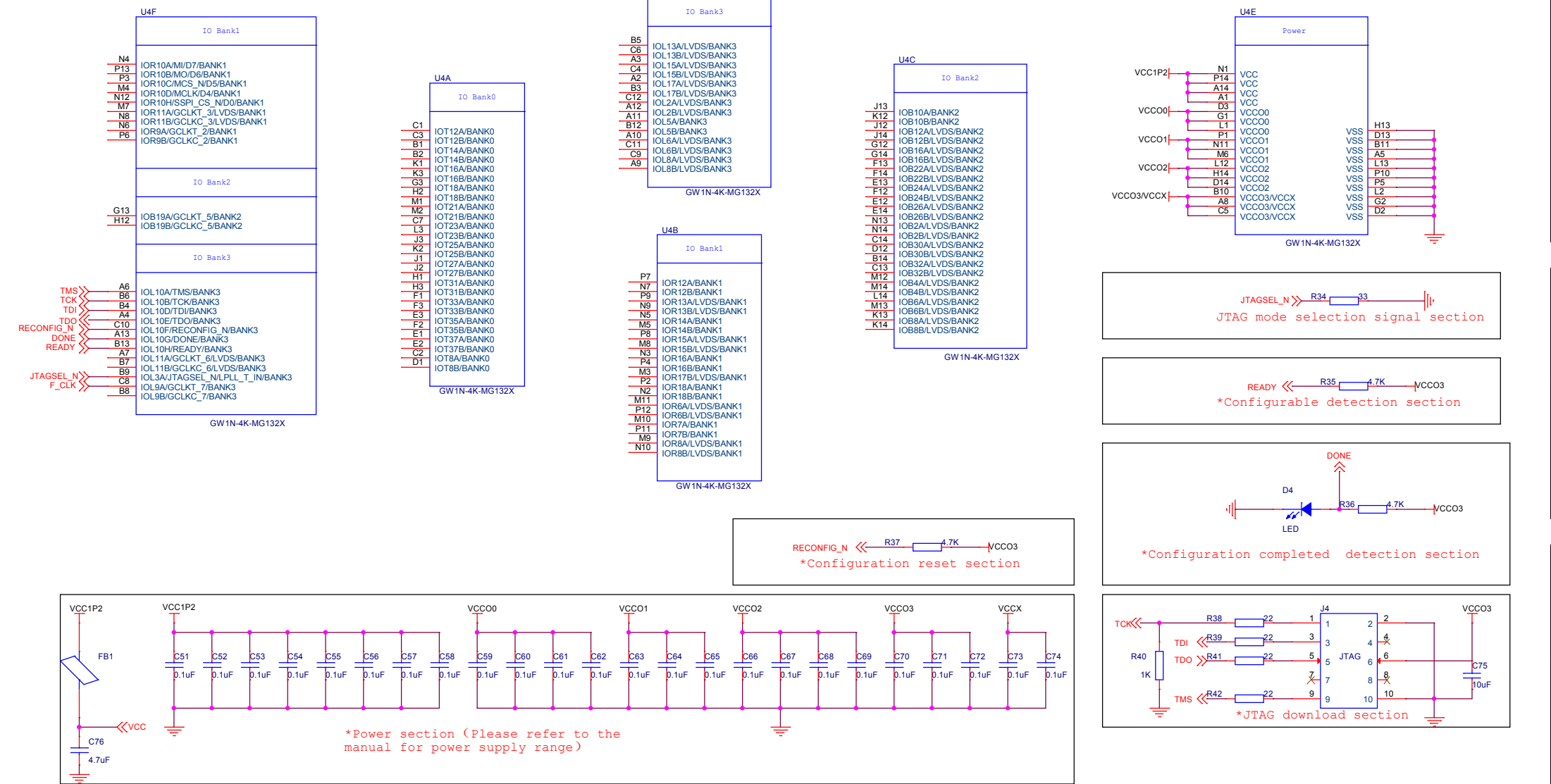


D

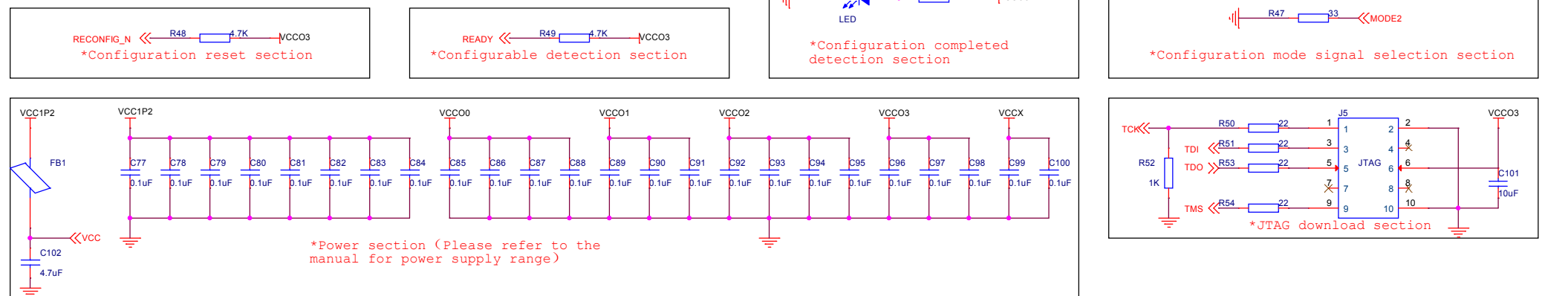
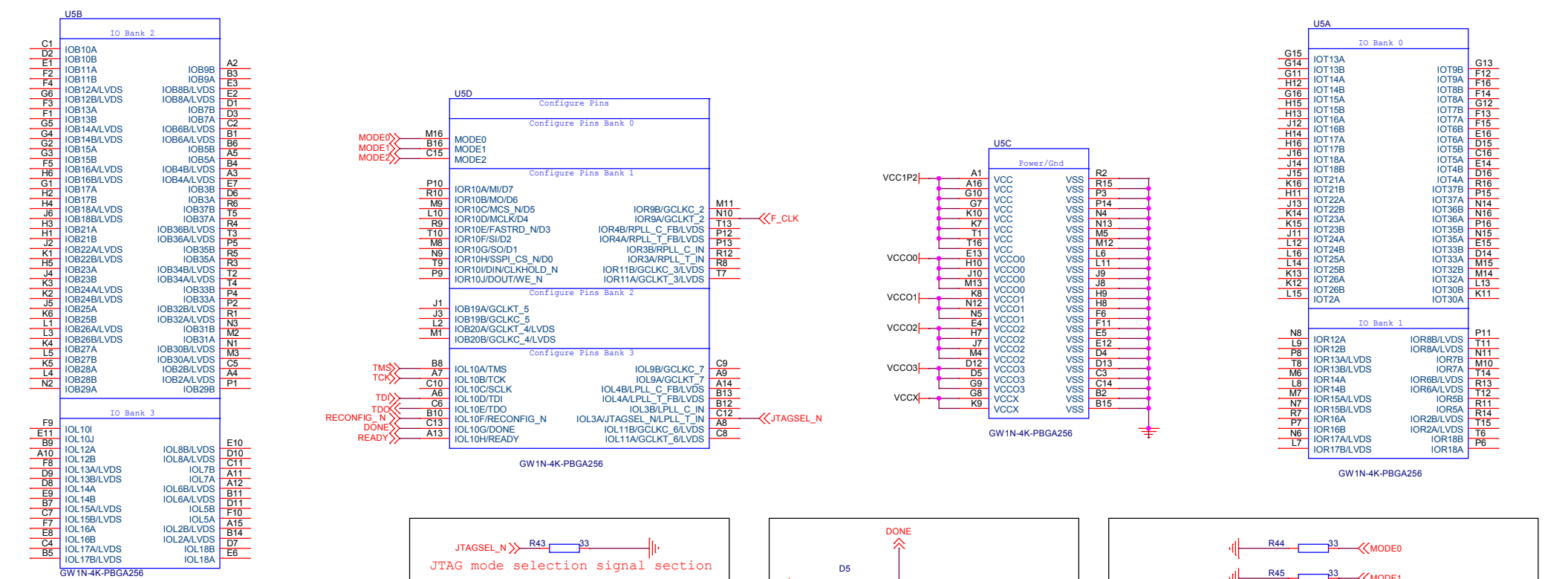
C

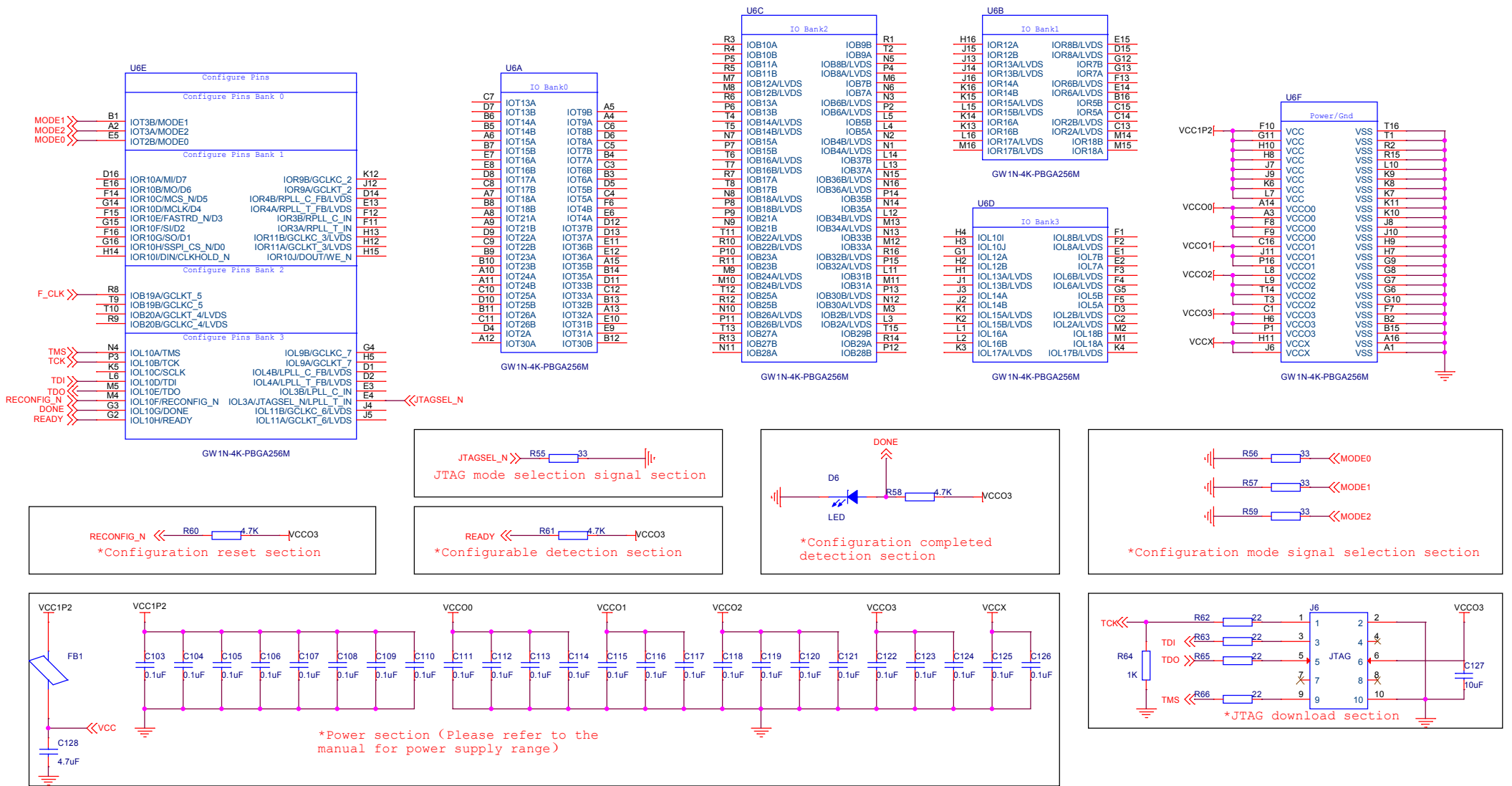
B

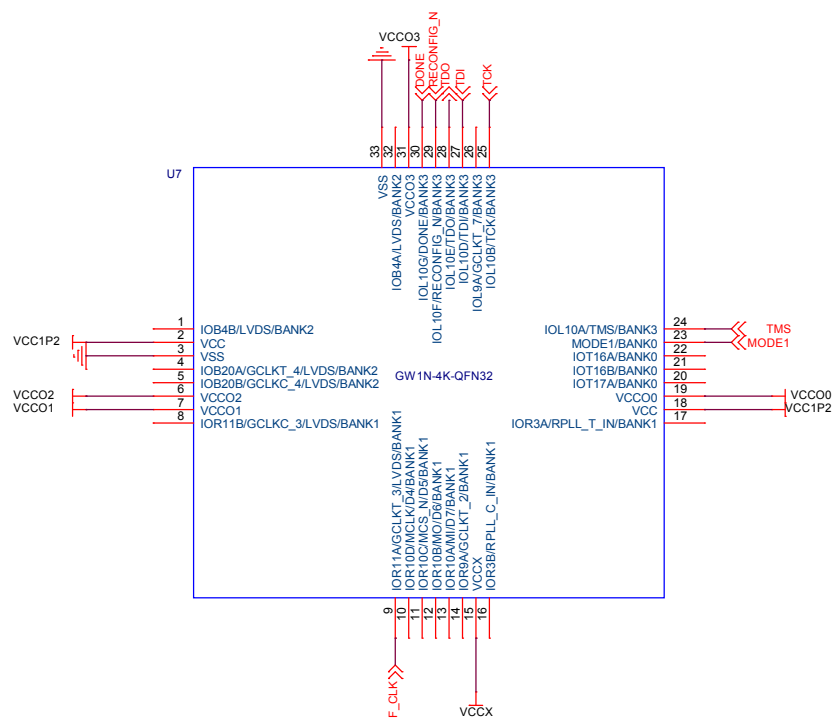
A



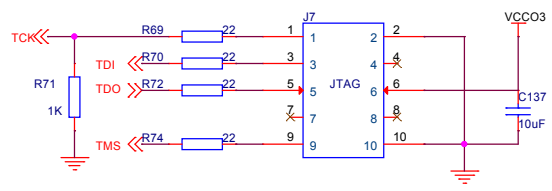
GW1N-LV4-PG256



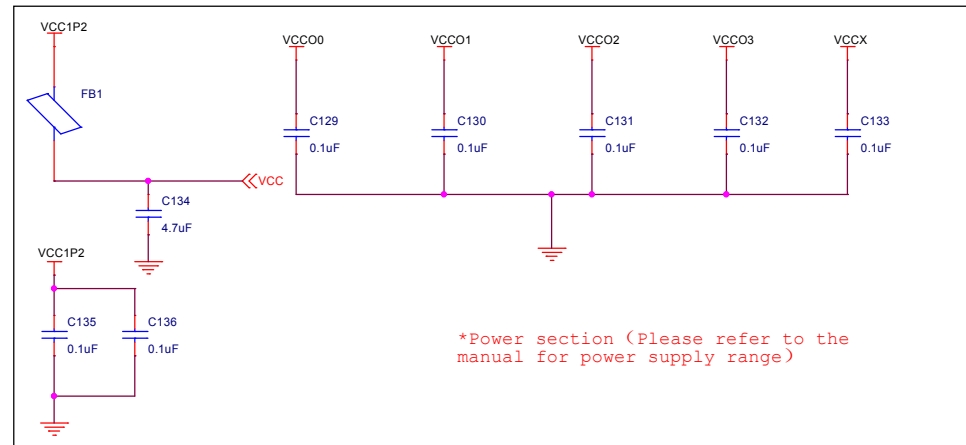




*The unconnected port of the chip is used as a single-ended signal port or multiplexing function
*Configuration: JTAG.AUTO BOOT.MSPI



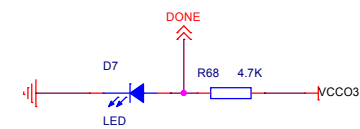
*JTAG download section



*Power section (Please refer to the manual for power supply range)



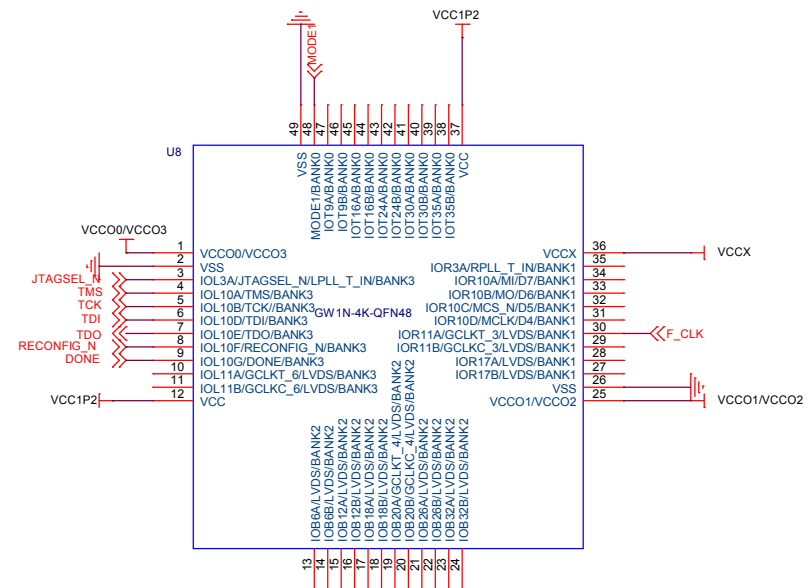
*Configuration reset section (low level)



*Configuration completed detection section



*Configuration mode signal selection section



*The unconnected port of the chip is used as a single-ended signal port or multiplexing function
*Configuration: JTAG.AUTO BOOT.MSPI

