

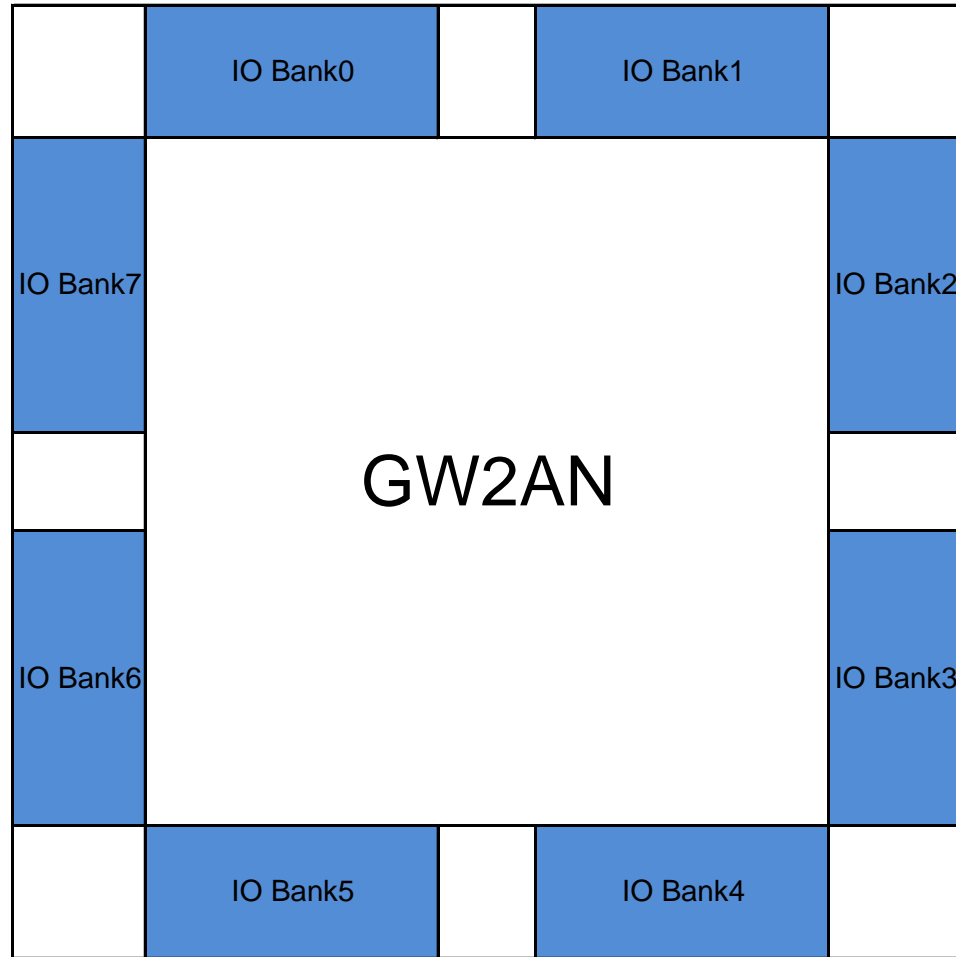
Date	Version	Description
01/22/2021	1.0E	Initial version published.
10/20/2022	1.0.1E	Four pins of IOR50[A/B] and IOR51[A/B] for UG676 package updated. The note in Pin Definitions sheet updated. The note in Pin List sheet updated. The note in Power sheet updated.
05/04/2023	1.0.2E	The description of CLKHOLD_N pin in Pin Definitions sheet updated.
06/30/2023	1.0.3E	The descriptions of pin definitions and info. for MODE0, MODE1, MODE2 pins optimized.
05/09/2024	1.0.4E	The IO Buffer sheet added. The description of EXTR pin in Pin Definitions sheet optimized. The I/O descriptions of Ready and Done pins in Pin Definitions sheet optimized.

Pins Name	Direction	Description
User I/O Pins		
IO [End][Row/Column Number][A/B]	I/O/LVDS	[End] indicates the pin location, including L(left), R(right), B(bottom), and T(top)
		[Row/Column Number] indicates the pin Row/Column number.
		If [End] is T(top) or B(bottom), the pin indicates the column number of the corresponding CFU. If [End] is L(left) or R(right), the pin indicates the row number of the corresponding CFU.
		[A/B] indicates differential signal pair information. LVDS indicates that this pin only supports true LVDS output.
Multi-Function Pins		
IO [End][Row/Column Number][A/B]/MMM		/MMM represents one or more of the other functions in addition to being general purpose user I/O. When not used for the special functions, these pins can be user I/O.
RECONFIG_N	I, Internal Weak Pull-up	Start new GowinCONFIG mode when low pulse
READY ^[2]	I/O, Internal Weak Pull-up	When high, device can be programmed and configured
		When low, device cannot be programmed and configured
DONE ^[2]	I/O, Internal Weak Pull-up	High indicates successful completion of programming and configuration
		When low, device cannot be programmed and configured
FASTRD_N/D3	I/O	In MSPI mode, FASTRD_N is used as Flash access speed port. Low indicates high-speed Flash access mode; high indicates regular Flash access mode.
		Data port D3 in CPU mode
MCLK/D4	I/O	Clock output MCLK in MSPI mode
		Data port D4 in CPU mode
MCS_N/D5	I/O	Enable signal MCS_N in MSPI mode, active-low
		Data port D5 in CPU mode
MI/D7	I/O	MISO in MSPI mode: Master data input/Slave data output
		Data port D7 in CPU mode
MO/D6	I/O	MOSI in MSPI mode: Master data output/Slave data input
		Data port D6 in CPU mode
SSPI_CS_N/D0	I/O	The enable signal of SSPI_CS_N in SSPI mode: low-active, internal weak pull-up
		Data port D0 in CPU mode
SO/D1	I/O	MISO in MSPI mode: Master data input/Slave data output
		Data port D1 in CPU mode
SI/D2		MOSI in MSPI mode: Master data output/Slave data input
		Data port D2 in CPU mode
SCLK	I	Clock input in SSPI, SERIAL, and CPU modes

Pins Name	Direction	Description
TMS	I, Internal Weak Pull Up	Serial mode input in JTAG mode
TCK	I	Serial clock input in JTAG mode; connecting 4.7K pull-down resistor on PCB is required.
TDI	I, Internal Weak Pull Up	Serial data input in JTAG mode
TDO	O	Serial data output in JTAG mode
JTAGSEL_N	I, Internal Weak Pull Up	JTAG mode selection, active-low
DIN	I, Internal Weak Pull Up	Data input in SERIAL mode
DOUT	O	Data output in SERIAL mode
CLKHOLD_N	I, Internal Weak Pull down	Active-high in SSPI mode; Active-low in CPU mode.
GCLKT_[x]	I	Pins for Global clock input, T(True), [x] is global clock number
GCLKC_[x]	I	Differential comparison input pin of GCLKT_[x], C(Comp), [x] is global clock number ^[1]
LPLL_T_fb/RPLL_T_fb	I	Left/Right PLL feedback input pins, T(True)
LPLL_C_fb/RPLL_C_fb	I	Left/Right PLL feedback input pins, C(Comp)
LPLL_T_in/RPLL_T_in	I	Left/Right PLL clock input pins, T(True)
LPLL_C_in/RPLL_C_in	I	Left/Right PLL clock input pins, C(Comp)
MODE2	I, Internal Weak Pull Up	GowinCONFIG modes selection pin; if this pin is marked as "VCCIO", it's internally powered; if this pin is marked as "GND", it's internally grounded.
MODE1	I, Internal Weak Pull Up	GowinCONFIG modes selection pin; if this pin is marked as "VCCIO", it's internally powered; if this pin is marked as "GND", it's internally grounded.
MODE0	I, Internal Weak Pull Up	GowinCONFIG modes selection pin; if this pin is marked as "VCCIO", it's internally powered; if this pin is marked as "GND", it's internally grounded.
Other Pins		
EXTR	NA	Dedicated pin to provide reference voltage for internal circuits, must be connected to an external 10K 1% resistor to ground.
NC	NA	Reserved.
VSS	NA	Ground.
VCC	NA	Power supply pins for internal core logic.
VCCIO#	NA	Power supply pins for the I/O voltage of I/O BANK#.

Pins Name	Direction	Description
VCCX	NA	Power supply pins for auxiliary voltage.
VCCPLL	NA	Power supply pins for left PLL.
VCCPLLR	NA	Power supply pins for right PLL.

Note!
 [1] When the input is single-ended, the GCLKC_[x] pin is not a global clock pin.
 [2] The default state of READY/DONE is open-drain output, internal weak pull-up. DONE outputs 0 during configuration.



Note!

- [1] Each Bank has independent reference voltage (VREF).
- [2] You can select to use internal VREF of IOB (equals to $0.5 * VCCIO$).
- [3] You can also select to use external VREF input (use any IO pins as external VREF input).

Note!							
The embedded pins of UG676 package can also be used as user IO.							
Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
EXTR	Ground		N/A			NONE	
IOB12A	LVDS	DQ6	5		True_of_IOB12B	TRUE	
IOB12B	LVDS	DQ6	5		Comp_of_IOB12A	TRUE	
IOB13A	I/O	DQ6	5		True_of_IOB13B	NONE	AE4
IOB13B	I/O	DQ6	5		Comp_of_IOB13A	NONE	AE6
IOB14A	LVDS	DQ6	5		True_of_IOB14B	TRUE	
IOB14B	LVDS	DQ6	5		Comp_of_IOB14A	TRUE	
IOB15A	I/O	DQ6	5		True_of_IOB15B	NONE	AC7
IOB15B	I/O	DQ6	5		Comp_of_IOB15A	NONE	AC8
IOB16A	I/O	DQ6	5		True_of_IOB16B	TRUE	W9
IOB16B	I/O	DQ6	5		Comp_of_IOB16A	TRUE	W10
IOB17A	I/O	DQ6	5		True_of_IOB17B	NONE	AA5
IOB17B	I/O	DQ6	5		Comp_of_IOB17A	NONE	AA7
IOB18A	I/O	DQ6	5		True_of_IOB18B	TRUE	AE7
IOB18B	I/O	DQ6	5		Comp_of_IOB18A	TRUE	AE8
IOB19A	I/O	DQ6	5		True_of_IOB19B	NONE	Y6
IOB19B	I/O	DQ6	5		Comp_of_IOB19A	NONE	Y7
IOB20A	I/O	DQ6	5		True_of_IOB20B	TRUE	AC9
IOB20B	I/O	DQ6	5		Comp_of_IOB20A	TRUE	AC10
IOB21A	I/O	DQ6	5		True_of_IOB21B	NONE	AF7
IOB21B	I/O	DQ6	5		Comp_of_IOB21A	NONE	AF8
IOB22A	LVDS	DQ6	5		True_of_IOB22B	TRUE	
IOB22B	LVDS	DQ6	5		Comp_of_IOB22A	TRUE	
IOB23A	I/O	DQ6	5		True_of_IOB23B	NONE	U9
IOB23B	I/O	DQ6	5		Comp_of_IOB23A	NONE	U11
IOB24A	I/O	DQ6	5		True_of_IOB24B	TRUE	AB7
IOB24B	I/O	DQ6	5		Comp_of_IOB24A	TRUE	AB9
IOB25A	I/O	DQ6	5		True_of_IOB25B	NONE	V10
IOB25B	I/O	DQ6	5		Comp_of_IOB25A	NONE	V11
IOB26A	I/O	DQ6	5		True_of_IOB26B	TRUE	AD7

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Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOB26B	I/O	DQ6	5		Comp_of_IOB26A	TRUE	AD9
IOB27A	I/O	DQS6	5		True_of_IOB27B	NONE	AA8
IOB27B	I/O	DQS6	5		Comp_of_IOB27A	NONE	AA9
IOB2A	I/O	DQ5	5		True_of_IOB2B	TRUE	AC4
IOB2B	I/O	DQ5	5		Comp_of_IOB2A	TRUE	AD4
IOB30A	I/O	DQS7	5		True_of_IOB30B	TRUE	Y8
IOB30B	I/O	DQS7	5		Comp_of_IOB30A	TRUE	Y9
IOB31A	I/O	DQ7	5		True_of_IOB31B	NONE	AE9
IOB31B	I/O	DQ7	5		Comp_of_IOB31A	NONE	AE10
IOB32A	LVDS	DQ7	5		True_of_IOB32B	TRUE	
IOB32B	LVDS	DQ7	5		Comp_of_IOB32A	TRUE	
IOB33A	I/O	DQ7	5		True_of_IOB33B	NONE	AF9
IOB33B	I/O	DQ7	5		Comp_of_IOB33A	NONE	AF10
IOB34A	LVDS	DQ7	5		True_of_IOB34B	TRUE	
IOB34B	LVDS	DQ7	5		Comp_of_IOB34A	TRUE	
IOB35A	I/O	DQ7	5		True_of_IOB35B	NONE	Y10
IOB35B	I/O	DQ7	5		Comp_of_IOB35A	NONE	Y11
IOB36A	I/O	DQ7	5		True_of_IOB36B	TRUE	AD10
IOB36B	I/O	DQ7	5		Comp_of_IOB36A	TRUE	AD11
IOB37A	I/O	DQ7	5		True_of_IOB37B	NONE	AC11
IOB37B	I/O	DQ7	5		Comp_of_IOB37A	NONE	AC12
IOB38A	LVDS	DQ7	5		True_of_IOB38B	TRUE	
IOB38B	LVDS	DQ7	5		Comp_of_IOB38A	TRUE	
IOB39A	I/O	DQ7	5		True_of_IOB39B	NONE	AD12
IOB39B	I/O	DQ7	5		Comp_of_IOB39A	NONE	AE12
IOB3A	I/O	DQ5	5		True_of_IOB3B	NONE	AE2
IOB3B	I/O	DQ5	5		Comp_of_IOB3A	NONE	AE3
IOB40A	I/O	DQ7	5		True_of_IOB40B	TRUE	AB10
IOB40B	I/O	DQ7	5		Comp_of_IOB40A	TRUE	AB12
IOB41A	I/O	DQ7	5		True_of_IOB41B	NONE	V12

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Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOB41B	I/O	DQ7	5		Comp_of_IOB41A	NONE	V13
IOB42A	I/O	DQ7	5		True_of_IOB42B	TRUE	AA10
IOB42B	I/O	DQ7	5		Comp_of_IOB42A	TRUE	AA12
IOB43A	I/O	DQ7	5		True_of_IOB43B	NONE	W12
IOB43B	I/O	DQ7	5		Comp_of_IOB43A	NONE	W13
IOB44A	I/O	DQ7	5		True_of_IOB44B	TRUE	AF12
IOB44B	I/O	DQ7	5		Comp_of_IOB44A	TRUE	AF13
IOB45A/GCLKT_5	I/O	DQ7	5	GCLKT_5	True_of_IOB45B	NONE	Y12
IOB45B/GCLKC_5	I/O	DQ7	5	GCLKC_5	Comp_of_IOB45A	NONE	Y13
IOB48A/GCLKT_4	I/O	DQ8	4	GCLKT_4	True_of_IOB48B	TRUE	AA13
IOB48B/GCLKC_4	I/O	DQ8	4	GCLKC_4	Comp_of_IOB48A	TRUE	AB13
IOB49A	I/O	DQ8	4		True_of_IOB49B	NONE	AE13
IOB49B	I/O	DQ8	4		Comp_of_IOB49A	NONE	AE14
IOB4A	I/O	DQ5	5		True_of_IOB4B	TRUE	AD5
IOB4B	I/O	DQ5	5		Comp_of_IOB4A	TRUE	AD6
IOB50A	I/O	DQ8	4		True_of_IOB50B	TRUE	V14
IOB50B	I/O	DQ8	4		Comp_of_IOB50A	TRUE	V15
IOB51A	I/O	DQ8	4		True_of_IOB51B	NONE	AC13
IOB51B	I/O	DQ8	4		Comp_of_IOB51A	NONE	AD14
IOB52A	I/O	DQ8	4		True_of_IOB52B	TRUE	Y14
IOB52B	I/O	DQ8	4		Comp_of_IOB52A	TRUE	Y15
IOB53A	I/O	DQ8	4		True_of_IOB53B	NONE	AC14
IOB53B	I/O	DQ8	4		Comp_of_IOB53A	NONE	AC15
IOB54A	LVDS	DQ8	4		True_of_IOB54B	TRUE	
IOB54B	LVDS	DQ8	4		Comp_of_IOB54A	TRUE	
IOB55A	I/O	DQ8	4		True_of_IOB55B	NONE	AA14
IOB55B	I/O	DQ8	4		Comp_of_IOB55A	NONE	AA15
IOB56A	I/O	DQ8	4		True_of_IOB56B	TRUE	AB15
IOB56B	I/O	DQ8	4		Comp_of_IOB56A	TRUE	AB16
IOB57A	I/O	DQ8	4		True_of_IOB57B	NONE	AF14

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Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOB57B	I/O	DQ8	4		Comp_of_IOB57A	NONE	AF15
IOB58A	LVDS	DQ8	4		True_of_IOB58B	TRUE	
IOB58B	LVDS	DQ8	4		Comp_of_IOB58A	TRUE	
IOB59A	I/O	DQ8	4		True_of_IOB59B	NONE	U15
IOB59B	I/O	DQ8	4		Comp_of_IOB59A	NONE	U16
IOB5A	I/O	DQ5	5		True_of_IOB5B	NONE	AF2
IOB5B	I/O	DQ5	5		Comp_of_IOB5A	NONE	AF3
IOB60A	I/O	DQ8	4		True_of_IOB60B	TRUE	AE15
IOB60B	I/O	DQ8	4		Comp_of_IOB60A	TRUE	AE17
IOB61A	I/O	DQ8	4		True_of_IOB61B	NONE	AC16
IOB61B	I/O	DQ8	4		Comp_of_IOB61A	NONE	AC17
IOB62A	I/O	DQ8	4		True_of_IOB62B	TRUE	AF17
IOB62B	I/O	DQ8	4		Comp_of_IOB62A	TRUE	AF18
IOB63A	I/O	DQS8	4		True_of_IOB63B	NONE	AD15
IOB63B	I/O	DQS8	4		Comp_of_IOB63A	NONE	AD16
IOB66A	I/O	DQS9	4		True_of_IOB66B	TRUE	AA17
IOB66B	I/O	DQS9	4		Comp_of_IOB66A	TRUE	AA18
IOB67A	I/O	DQ9	4		True_of_IOB67B	NONE	AE18
IOB67B	I/O	DQ9	4		Comp_of_IOB67A	NONE	AE19
IOB68A	I/O	DQ9	4		True_of_IOB68B	TRUE	V16
IOB68B	I/O	DQ9	4		Comp_of_IOB68A	TRUE	V17
IOB69A	I/O	DQ9	4		True_of_IOB69B	NONE	AB17
IOB69B	I/O	DQ9	4		Comp_of_IOB69A	NONE	AB18
IOB6A	I/O	DQ5	5		True_of_IOB6B	TRUE	AC5
IOB6B	I/O	DQ5	5		Comp_of_IOB6A	TRUE	AC6
IOB70A	I/O	DQ9	4		True_of_IOB70B	TRUE	W15
IOB70B	I/O	DQ9	4		Comp_of_IOB70A	TRUE	W17
IOB71A	I/O	DQ9	4		True_of_IOB71B	NONE	AC18
IOB71B	I/O	DQ9	4		Comp_of_IOB71A	NONE	AC19
IOB72A	I/O	DQ9	4		True_of_IOB72B	TRUE	AF19

Note!

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Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOB72B	I/O	DQ9	4		Comp_of_IOB72A	TRUE	AF20
IOB73A	I/O	DQ9	4		True_of_IOB73B	NONE	AD17
IOB73B	I/O	DQ9	4		Comp_of_IOB73A	NONE	AD19
IOB74A	I/O	DQ9	4		True_of_IOB74B	TRUE	Y20
IOB74B	I/O	DQ9	4		Comp_of_IOB74A	TRUE	Y21
IOB75A	I/O	DQ9	4		True_of_IOB75B	NONE	AE20
IOB75B	I/O	DQ9	4		Comp_of_IOB75A	NONE	AE21
IOB76A	I/O	DQ9	4		True_of_IOB76B	TRUE	AA19
IOB76B	I/O	DQ9	4		Comp_of_IOB76A	TRUE	AA20
IOB77A	I/O	DQ9	4		True_of_IOB77B	NONE	Y16
IOB77B	I/O	DQ9	4		Comp_of_IOB77A	NONE	Y17
IOB78A	LVDS	DQ9	4		True_of_IOB78B	TRUE	
IOB78B	LVDS	DQ9	4		Comp_of_IOB78A	TRUE	
IOB79A	I/O	DQ9	4		True_of_IOB79B	NONE	Y18
IOB79B	I/O	DQ9	4		Comp_of_IOB79A	NONE	Y19
IOB7A	I/O	DQ5	5		True_of_IOB7B	NONE	W7
IOB7B	I/O	DQ5	5		Comp_of_IOB7A	NONE	V8
IOB80A	LVDS	DQ9	4		True_of_IOB80B	TRUE	
IOB80B	LVDS	DQ9	4		Comp_of_IOB80A	TRUE	
IOB81A	I/O	DQ9	4		True_of_IOB81B	NONE	AB20
IOB81B	I/O	DQ9	4		Comp_of_IOB81A	NONE	AB21
IOB84A	I/O	DQS10	4		True_of_IOB84B	TRUE	AC20
IOB84B	I/O	DQS10	4		Comp_of_IOB84A	TRUE	AC21
IOB85A	I/O	DQ10	4		True_of_IOB85B	NONE	AF22
IOB85B	I/O	DQ10	4		Comp_of_IOB85A	NONE	AF23
IOB86A	I/O	DQ10	4		True_of_IOB86B	TRUE	W18
IOB86B	I/O	DQ10	4		Comp_of_IOB86A	TRUE	W20
IOB87A	I/O	DQ10	4		True_of_IOB87B	NONE	AD20
IOB87B	I/O	DQ10	4		Comp_of_IOB87A	NONE	AD21
IOB88A	I/O	DQ10	4		True_of_IOB88B	TRUE	AE23

Note!

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Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOB88B	I/O	DQ10	4		Comp_of_IOB88A	TRUE	AE24
IOB89A	I/O	DQ10	4		True_of_IOB89B	NONE	AC22
IOB89B	I/O	DQ10	4		Comp_of_IOB89A	NONE	AC23
IOB8A	I/O	DQ5	5		True_of_IOB8B	TRUE	AF4
IOB8B	I/O	DQ5	5		Comp_of_IOB8A	TRUE	AF5
IOB90A	I/O	DQ10	4		True_of_IOB90B	TRUE	AF24
IOB90B	I/O	DQ10	4		Comp_of_IOB90A	TRUE	AF25
IOB91A	I/O	DQ10	4		True_of_IOB91B	NONE	AD22
IOB91B	I/O	DQ10	4		Comp_of_IOB91A	NONE	AD23
IOB9A	I/O	DQS5	5		True_of_IOB9B	NONE	AB4
IOB9B	I/O	DQS5	5		Comp_of_IOB9A	NONE	AB6
IOL11A/LPLL1_T_fb	I/O	DQ1	7	LPLL1_T_fb	True_of_IOL11B	TRUE	G6
IOL11B/LPLL1_C_fb	I/O	DQ1	7	LPLL1_C_fb	Comp_of_IOL11A	TRUE	F5
IOL12A	I/O	DQ1	7		True_of_IOL12B	NONE	D1
IOL12B	I/O	DQ1	7		Comp_of_IOL12A	NONE	E1
IOL13A	LVDS	DQ1	7		True_of_IOL13B	TRUE	G3
IOL13B	LVDS	DQ1	7		Comp_of_IOL13A	TRUE	F2
IOL14A	I/O	DQ1	7		True_of_IOL14B	NONE	L10
IOL14B	I/O	DQ1	7		Comp_of_IOL14A	NONE	M10
IOL15A	I/O	DQ1	7		True_of_IOL15B	TRUE	F4
IOL15B	I/O	DQ1	7		Comp_of_IOL15A	TRUE	G5
IOL16A	I/O	DQ1	7		True_of_IOL16B	NONE	G4
IOL16B	I/O	DQ1	7		Comp_of_IOL16A	NONE	H4
IOL17A	LVDS	DQ1	7		True_of_IOL17B	TRUE	
IOL17B	LVDS	DQ1	7		Comp_of_IOL17A	TRUE	
IOL18A	I/O	DQS1	7		True_of_IOL18B	NONE	G2
IOL18B	I/O	DQS1	7		Comp_of_IOL18A	NONE	H2
IOL20A	I/O	DQ1	7		True_of_IOL20B	TRUE	H7
IOL20B	I/O	DQ1	7		Comp_of_IOL20A	TRUE	J7
IOL21A	I/O	DQ1	7		True_of_IOL21B	NONE	G1

Note!

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Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOL21B	I/O	DQ1	7		Comp_of_IOL21A	NONE	H1
IOL22A	LVDS	DQ1	7		True_of_IOL22B	TRUE	
IOL22B	LVDS	DQ1	7		Comp_of_IOL22A	TRUE	
IOL23A	I/O	DQ1	7		True_of_IOL23B	NONE	K8
IOL23B	I/O	DQ1	7		Comp_of_IOL23A	NONE	M8
IOL24A	LVDS	DQ1	7		True_of_IOL24B	TRUE	H6
IOL24B	LVDS	DQ1	7		Comp_of_IOL24A	TRUE	J6
IOL25A	I/O	DQ1	7		True_of_IOL25B	NONE	M9
IOL25B	I/O	DQ1	7		Comp_of_IOL25A	NONE	N9
IOL26A	LVDS	DQ1	7		True_of_IOL26B	TRUE	
IOL26B	LVDS	DQ1	7		Comp_of_IOL26A	TRUE	
IOL27A	I/O	DQ1	7		True_of_IOL27B	NONE	J5
IOL27B	I/O	DQ1	7		Comp_of_IOL27A	NONE	K5
IOL28A	I/O	DQ2	7		True_of_IOL28B	TRUE	J4
IOL28B	I/O	DQ2	7		Comp_of_IOL28A	TRUE	K4
IOL29A	I/O	DQ2	7		True_of_IOL29B	NONE	J2
IOL29B	I/O	DQ2	7		Comp_of_IOL29A	NONE	K2
IOL2A	I/O	DQ0	7		True_of_IOL2B	TRUE	C2
IOL2B	I/O	DQ0	7		Comp_of_IOL2A	TRUE	D2
IOL30A	LVDS	DQ2	7		True_of_IOL30B	TRUE	
IOL30B	LVDS	DQ2	7		Comp_of_IOL30A	TRUE	
IOL31A	I/O	DQ2	7		True_of_IOL31B	NONE	J3
IOL31B	I/O	DQ2	7		Comp_of_IOL31A	NONE	K3
IOL32A	I/O	DQ2	7		True_of_IOL32B	TRUE	M7
IOL32B	I/O	DQ2	7		Comp_of_IOL32A	TRUE	N7
IOL33A	I/O	DQ2	7		True_of_IOL33B	NONE	K6
IOL33B	I/O	DQ2	7		Comp_of_IOL33A	NONE	M6
IOL34A	I/O	DQS2	7		True_of_IOL34B	TRUE	J1
IOL34B	I/O	DQS2	7		Comp_of_IOL34A	TRUE	K1
IOL35A/LPLL2_T_in	I/O	DQ2	7	LPLL2_T_in	True_of_IOL35B	NONE	L4

Note!

The embedded pins of UG676 package can also be used as user IO.

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOL35B/LPLL2_C_in	I/O	DQ2	7	LPLL2_C_in	Comp_of_IOL35A	NONE	M4
IOL37A/LPLL2_T_fb	I/O	DQ2	7	LPLL2_T_fb	True_of_IOL37B	TRUE	L3
IOL37B/LPLL2_C_fb	I/O	DQ2	7	LPLL2_C_fb	Comp_of_IOL37A	TRUE	M3
IOL38A	I/O	DQ2	7		True_of_IOL38B	NONE	M2
IOL38B	I/O	DQ2	7		Comp_of_IOL38A	NONE	N2
IOL39A	LVDS	DQ2	7		True_of_IOL39B	TRUE	
IOL39B	LVDS	DQ2	7		Comp_of_IOL39A	TRUE	
IOL3A	I/O	DQ0	7		True_of_IOL3B	NONE	B1
IOL3B	I/O	DQ0	7		Comp_of_IOL3A	NONE	C1
IOL40A	I/O	DQ2	7		True_of_IOL40B	NONE	P7
IOL40B	I/O	DQ2	7		Comp_of_IOL40A	NONE	P8
IOL41A	LVDS	DQ2	7		True_of_IOL41B	TRUE	N6
IOL41B	LVDS	DQ2	7		Comp_of_IOL41A	TRUE	P6
IOL42A	I/O	DQ2	7		True_of_IOL42B	NONE	M5
IOL42B	I/O	DQ2	7		Comp_of_IOL42A	NONE	N5
IOL43A	I/O	DQ2	7		True_of_IOL43B	TRUE	N1
IOL43B	I/O	DQ2	7		Comp_of_IOL43A	TRUE	M1
IOL44A/GCLKT_7	I/O	DQ2	7	GCLKT_7	True_of_IOL44B	NONE	N4
IOL44B/GCLKC_7	I/O	DQ2	7	GCLKC_7	Comp_of_IOL44A	NONE	P4
IOL46A/GCLKT_6	I/O	DQ3	6	GCLKT_6	True_of_IOL46B	TRUE	P3
IOL46B/GCLKC_6	I/O	DQ3	6	GCLKC_6	Comp_of_IOL46A	TRUE	R3
IOL47A	I/O	DQ3	6		True_of_IOL47B	NONE	P2
IOL47B	I/O	DQ3	6		Comp_of_IOL47A	NONE	R2
IOL48A	I/O	DQ3	6		True_of_IOL48B	TRUE	R5
IOL48B	I/O	DQ3	6		Comp_of_IOL48A	TRUE	R6
IOL49A	I/O	DQ3	6		True_of_IOL49B	NONE	P9
IOL49B	I/O	DQ3	6		Comp_of_IOL49A	NONE	P10
IOL4A	LVDS	DQ0	7		True_of_IOL4B	TRUE	
IOL4B	LVDS	DQ0	7		Comp_of_IOL4A	TRUE	
IOL50A	I/O	DQ3	6		True_of_IOL50B	TRUE	R7

Note!							
The embedded pins of UG676 package can also be used as user IO.							
Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOL50B	I/O	DQ3	6		Comp_of_IOL50A	TRUE	T7
IOL51A	I/O	DQ3	6		True_of_IOL51B	NONE	R4
IOL51B	I/O	DQ3	6		Comp_of_IOL51A	NONE	T5
IOL52A	I/O	DQ3	6		True_of_IOL52B	TRUE	P1
IOL52B	I/O	DQ3	6		Comp_of_IOL52A	TRUE	R1
IOL53A	I/O	DQS3	6		True_of_IOL53B	NONE	T4
IOL53B	I/O	DQS3	6		Comp_of_IOL53A	NONE	U4
IOL55A	I/O	DQ3	6		True_of_IOL55B	TRUE	R8
IOL55B	I/O	DQ3	6		Comp_of_IOL55A	TRUE	U8
IOL56A	I/O	DQ3	6		True_of_IOL56B	NONE	U1
IOL56B	I/O	DQ3	6		Comp_of_IOL56A	NONE	V1
IOL57A	LVDS	DQ3	6		True_of_IOL57B	TRUE	T3
IOL57B	LVDS	DQ3	6		Comp_of_IOL57A	TRUE	U2
IOL58A	I/O	DQ3	6		True_of_IOL58B	NONE	R9
IOL58B	I/O	DQ3	6		Comp_of_IOL58A	NONE	R10
IOL59A	LVDS	DQ3	6		True_of_IOL59B	TRUE	U5
IOL59B	LVDS	DQ3	6		Comp_of_IOL59A	TRUE	V5
IOL5A	I/O	DQ0	7		True_of_IOL5B	NONE	K9
IOL5B	I/O	DQ0	7		Comp_of_IOL5A	NONE	L9
IOL60A	I/O	DQ3	6		True_of_IOL60B	NONE	U7
IOL60B	I/O	DQ3	6		Comp_of_IOL60A	NONE	V7
IOL61A	LVDS	DQ3	6		True_of_IOL61B	TRUE	
IOL61B	LVDS	DQ3	6		Comp_of_IOL61A	TRUE	
IOL62A	I/O	DQ3	6		True_of_IOL62B	NONE	U6
IOL62B	I/O	DQ3	6		Comp_of_IOL62A	NONE	V6
IOL67A	LVDS	DQ4	6		True_of_IOL67B	TRUE	V4
IOL67B	LVDS	DQ4	6		Comp_of_IOL67A	TRUE	W4
IOL68A	I/O	DQ4	6		True_of_IOL68B	NONE	W1
IOL68B	I/O	DQ4	6		Comp_of_IOL68A	NONE	Y1
IOL69A	LVDS	DQ4	6		True_of_IOL69B	TRUE	

Note!							
The embedded pins of UG676 package can also be used as user IO.							
Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOL69B	LVDS	DQ4	6		Comp_of_IOL69A	TRUE	
IOL6A	LVDS	DQ0	7		True_of_IOL6B	TRUE	
IOL6B	LVDS	DQ0	7		Comp_of_IOL6A	TRUE	
IOL70A	I/O	DQ4	6		True_of_IOL70B	NONE	T9
IOL70B	I/O	DQ4	6		Comp_of_IOL70A	NONE	T10
IOL71A	LVDS	DQ4	6		True_of_IOL71B	TRUE	U3
IOL71B	LVDS	DQ4	6		Comp_of_IOL71A	TRUE	W3
IOL72A	I/O	DQ4	6		True_of_IOL72B	NONE	V2
IOL72B	I/O	DQ4	6		Comp_of_IOL72A	NONE	W2
IOL73A	I/O	DQS4	6		True_of_IOL73B	TRUE	AB1
IOL73B	I/O	DQS4	6		Comp_of_IOL73A	TRUE	AC1
IOL74A/LPLL3_T_in	I/O	DQ4	6	LPLL3_T_in	True_of_IOL74B	NONE	W6
IOL74B/LPLL3_C_in	I/O	DQ4	6	LPLL3_C_in	Comp_of_IOL74A	NONE	Y5
IOL76A/LPLL3_T_fb	I/O	DQ4	6	LPLL3_T_fb	True_of_IOL76B	TRUE	Y4
IOL76B/LPLL3_C_fb	I/O	DQ4	6	LPLL3_C_fb	Comp_of_IOL76A	TRUE	AA4
IOL77A	I/O	DQ4	6		True_of_IOL77B	NONE	Y3
IOL77B	I/O	DQ4	6		Comp_of_IOL77A	NONE	AA3
IOL78A	LVDS	DQ4	6		True_of_IOL78B	TRUE	
IOL78B	LVDS	DQ4	6		Comp_of_IOL78A	TRUE	
IOL79A	I/O	DQ4	6		True_of_IOL79B	NONE	Y2
IOL79B	I/O	DQ4	6		Comp_of_IOL79A	NONE	AA2
IOL7A	I/O	DQ0	7		True_of_IOL7B	NONE	K7
IOL7B	I/O	DQ0	7		Comp_of_IOL7A	NONE	L7
IOL80A	LVDS	DQ4	6		True_of_IOL80B	TRUE	
IOL80B	LVDS	DQ4	6		Comp_of_IOL80A	TRUE	
IOL81A	I/O	DQ4	6		True_of_IOL81B	NONE	AB3
IOL81B	I/O	DQ4	6		Comp_of_IOL81A	NONE	AC3
IOL82A	I/O	DQ4	6		True_of_IOL82B	TRUE	AD1
IOL82B	I/O	DQ4	6		Comp_of_IOL82A	TRUE	AE1
IOL83A	I/O	DQ4	6		True_of_IOL83B	NONE	AC2

Note!							
The embedded pins of UG676 package can also be used as user IO.							
Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOL83B	I/O	DQ4	6		Comp_of_IOL83A	NONE	AD2
IOL8A	I/O	DQS0	7		True_of_IOL8B	TRUE	D3
IOL8B	I/O	DQS0	7		Comp_of_IOL8A	TRUE	E4
IOL9A/LPLL1_T_in	I/O	DQ0	7	LPLL1_T_in	True_of_IOL9B	NONE	E3
IOL9B/LPLL1_C_in	I/O	DQ0	7	LPLL1_C_in	Comp_of_IOL9A	NONE	F3
IOR11A/RPLL1_T_fb	I/O	DQ14	2	RPLL1_T_fb	True_of_IOR11B	TRUE	F22
IOR11B/RPLL1_C_fb	I/O	DQ14	2	RPLL1_C_fb	Comp_of_IOR11A	TRUE	G22
IOR12A	I/O	DQ14	2		True_of_IOR12B	NONE	D26
IOR12B	I/O	DQ14	2		Comp_of_IOR12A	NONE	E26
IOR13A	LVDS	DQ14	2		True_of_IOR13B	TRUE	G25
IOR13B	LVDS	DQ14	2		Comp_of_IOR13A	TRUE	H25
IOR14A	I/O	DQ14	2		True_of_IOR14B	NONE	K18
IOR14B	I/O	DQ14	2		Comp_of_IOR14A	NONE	L18
IOR15A	I/O	DQ14	2		True_of_IOR15B	TRUE	H24
IOR15B	I/O	DQ14	2		Comp_of_IOR15A	TRUE	J23
IOR16A	I/O	DQ14	2		True_of_IOR16B	NONE	H23
IOR16B	I/O	DQ14	2		Comp_of_IOR16A	NONE	J22
IOR17A	LVDS	DQ14	2		True_of_IOR17B	TRUE	
IOR17B	LVDS	DQ14	2		Comp_of_IOR17A	TRUE	
IOR18A	I/O	DQS14	2		True_of_IOR18B	NONE	H21
IOR18B	I/O	DQS14	2		Comp_of_IOR18A	NONE	J21
IOR20A	I/O	DQ14	2		True_of_IOR20B	TRUE	K24
IOR20B	I/O	DQ14	2		Comp_of_IOR20A	TRUE	L24
IOR21A	I/O	DQ14	2		True_of_IOR21B	NONE	G26
IOR21B	I/O	DQ14	2		Comp_of_IOR21A	NONE	H26
IOR22A	LVDS	DQ14	2		True_of_IOR22B	TRUE	K23
IOR22B	LVDS	DQ14	2		Comp_of_IOR22A	TRUE	L23
IOR23A	I/O	DQ14	2		True_of_IOR23B	NONE	L20
IOR23B	I/O	DQ14	2		Comp_of_IOR23A	NONE	M20
IOR24A	LVDS	DQ14	2		True_of_IOR24B	TRUE	

Note!

The embedded pins of UG676 package can also be used as user IO.

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOR24B	LVDS	DQ14	2		Comp_of_IOR24A	TRUE	
IOR25A	I/O	DQ14	2		True_of_IOR25B	NONE	L17
IOR25B	I/O	DQ14	2		Comp_of_IOR25A	NONE	N17
IOR26A	LVDS	DQ14	2		True_of_IOR26B	TRUE	
IOR26B	LVDS	DQ14	2		Comp_of_IOR26A	TRUE	
IOR27A	I/O	DQ14	2		True_of_IOR27B	NONE	K22
IOR27B	I/O	DQ14	2		Comp_of_IOR27A	NONE	L22
IOR28A	I/O	DQ13	2		True_of_IOR28B	TRUE	K21
IOR28B	I/O	DQ13	2		Comp_of_IOR28A	TRUE	M21
IOR29A	I/O	DQ13	2		True_of_IOR29B	NONE	J26
IOR29B	I/O	DQ13	2		Comp_of_IOR29A	NONE	K26
IOR2A	I/O	DQ15	2		True_of_IOR2B	TRUE	C25
IOR2B	I/O	DQ15	2		Comp_of_IOR2A	TRUE	D25
IOR30A	LVDS	DQ13	2		True_of_IOR30B	TRUE	
IOR30B	LVDS	DQ13	2		Comp_of_IOR30A	TRUE	
IOR31A	I/O	DQ13	2		True_of_IOR31B	NONE	M24
IOR31B	I/O	DQ13	2		Comp_of_IOR31A	NONE	N24
IOR32A	I/O	DQ13	2		True_of_IOR32B	TRUE	M19
IOR32B	I/O	DQ13	2		Comp_of_IOR32A	TRUE	N19
IOR33A	I/O	DQ13	2		True_of_IOR33B	NONE	M23
IOR33B	I/O	DQ13	2		Comp_of_IOR33A	NONE	N23
IOR34A	I/O	DQS13	2		True_of_IOR34B	TRUE	J25
IOR34B	I/O	DQS13	2		Comp_of_IOR34A	TRUE	K25
IOR35A/RPLL2_T_in	I/O	DQ13	2	RPLL2_T_in	True_of_IOR35B	NONE	M22
IOR35B/RPLL2_C_in	I/O	DQ13	2	RPLL2_C_in	Comp_of_IOR35A	NONE	N21
IOR37A/RPLL2_T_fb	I/O	DQ13	2	RPLL2_T_fb	True_of_IOR37B	TRUE	N20
IOR37B/RPLL2_C_fb	I/O	DQ13	2	RPLL2_C_fb	Comp_of_IOR37A	TRUE	P18
IOR38A	I/O	DQ13	2		True_of_IOR38B	NONE	M26
IOR38B	I/O	DQ13	2		Comp_of_IOR38A	NONE	N26
IOR39A	LVDS	DQ13	2		True_of_IOR39B	TRUE	

Note!

The embedded pins of UG676 package can also be used as user IO.

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOR39B	LVDS	DQ13	2		Comp_of_IOR39A	TRUE	
IOR3A	I/O	DQ15	2		True_of_IOR3B	NONE	B26
IOR3B	I/O	DQ15	2		Comp_of_IOR3A	NONE	C26
IOR40A	I/O	DQ13	2		True_of_IOR40B	NONE	M18
IOR40B	I/O	DQ13	2		Comp_of_IOR40A	NONE	N18
IOR41A	LVDS	DQ13	2		True_of_IOR41B	TRUE	
IOR41B	LVDS	DQ13	2		Comp_of_IOR41A	TRUE	
IOR42A/TDO	I/O	DQ13	2	TDO	True_of_IOR42B	NONE	P22
IOR42B/TMS	I/O	DQ13	2	TMS	Comp_of_IOR42A	NONE	P23
IOR43A/TCK	I/O	DQ13	2	TCK	True_of_IOR43B	TRUE	N25
IOR43B/TDI	I/O	DQ13	2	TDI	Comp_of_IOR43A	TRUE	M25
IOR44A/GCLKT_2	I/O	DQ13	2	GCLKT_2	True_of_IOR44B	NONE	P20
IOR44B/GCLKC_2	I/O	DQ13	2	GCLKC_2	Comp_of_IOR44A	NONE	P21
IOR46A/GCLKT_3	I/O	DQ12	3	GCLKT_3	True_of_IOR46B	TRUE	R24
IOR46B/GCLKC_3	I/O	DQ12	3	GCLKC_3	Comp_of_IOR46A	TRUE	T24
IOR47A/MODE0	I/O	DQ12	3	MODE0	True_of_IOR47B	NONE	P26
IOR47B/MODE1	I/O	DQ12	3	MODE1	Comp_of_IOR47A	NONE	R26
IOR48A/MODE2	I/O	DQ12	3	MODE2	True_of_IOR48B	TRUE	R23
IOR48B/RECONFIG_N	I/O	DQ12	3	RECONFIG_N	Comp_of_IOR48A	TRUE	T23
IOR49A/READY	I/O	DQ12	3	READY	True_of_IOR49B	NONE	R17
IOR49B/DONE	I/O	DQ12	3	DONE	Comp_of_IOR49A	NONE	T17
IOR4A	LVDS	DQ15	2		True_of_IOR4B	TRUE	E24
IOR4B	LVDS	DQ15	2		Comp_of_IOR4A	TRUE	F25
IOR50A/MI/D7	I/O	DQ12	3	MI/D7	True_of_IOR50B	TRUE	R22
IOR50B/MO/D6	I/O	DQ12	3	MO/D6	Comp_of_IOR50A	TRUE	R21
IOR51A/MCS_N/D5	I/O	DQ12	3	MCS_N/D5	True_of_IOR51B	NONE	R20
IOR51B/MCLK/D4	I/O	DQ12	3	MCLK/D4	Comp_of_IOR51A	NONE	T20
IOR52A/FASTRD_N/D3	I/O	DQ12	3	FASTRD_N/D3	True_of_IOR52B	TRUE	P25
IOR52B/SI/D2	I/O	DQ12	3	SI/D2	Comp_of_IOR52A	TRUE	R25
IOR53A/SO/D1	I/O	DQS12	3	SO/D1	True_of_IOR53B	NONE	R19

Note!							
The embedded pins of UG676 package can also be used as user IO.							
Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOR53B/SSPI_CS_N/D0	I/O	DQS12	3	SSPI_CS_N/D0	Comp_of_IOR53A	NONE	U19
IOR55A/DIN/CLKHOLD_N	I/O	DQ12	3	DIN/CLKHOLD_N	True_of_IOR55B	TRUE	U25
IOR55B/DOOUT/WE_N	I/O	DQ12	3	DOOUT/WE_N	Comp_of_IOR55A	TRUE	V25
IOR56A/SCLK	I/O	DQ12	3	SCLK	True_of_IOR56B	NONE	U26
IOR56B	I/O	DQ12	3		Comp_of_IOR56A	NONE	V26
IOR57A	LVDS	DQ12	3		True_of_IOR57B	TRUE	U24
IOR57B	LVDS	DQ12	3		Comp_of_IOR57A	TRUE	V24
IOR58A	I/O	DQ12	3		True_of_IOR58B	NONE	R18
IOR58B	I/O	DQ12	3		Comp_of_IOR58A	NONE	T18
IOR59A	LVDS	DQ12	3		True_of_IOR59B	TRUE	U23
IOR59B	LVDS	DQ12	3		Comp_of_IOR59A	TRUE	V23
IOR5A	I/O	DQ15	2		True_of_IOR5B	NONE	J20
IOR5B	I/O	DQ15	2		Comp_of_IOR5A	NONE	K20
IOR60A	I/O	DQ12	3		True_of_IOR60B	NONE	U22
IOR60B	I/O	DQ12	3		Comp_of_IOR60A	NONE	V22
IOR61A	LVDS	DQ12	3		True_of_IOR61B	TRUE	
IOR61B	LVDS	DQ12	3		Comp_of_IOR61A	TRUE	
IOR62A	I/O	DQ12	3		True_of_IOR62B	NONE	U21
IOR62B	I/O	DQ12	3		Comp_of_IOR62A	NONE	V21
IOR67A	LVDS	DQ11	3		True_of_IOR67B	TRUE	U20
IOR67B	LVDS	DQ11	3		Comp_of_IOR67A	TRUE	V20
IOR68A	I/O	DQ11	3		True_of_IOR68B	NONE	W26
IOR68B	I/O	DQ11	3		Comp_of_IOR68A	NONE	Y26
IOR69A	LVDS	DQ11	3		True_of_IOR69B	TRUE	V19
IOR69B	LVDS	DQ11	3		Comp_of_IOR69A	TRUE	W21
IOR6A	LVDS	DQ15	2		True_of_IOR6B	TRUE	
IOR6B	LVDS	DQ15	2		Comp_of_IOR6A	TRUE	
IOR70A	I/O	DQ11	3		True_of_IOR70B	NONE	U18
IOR70B	I/O	DQ11	3		Comp_of_IOR70A	NONE	V18
IOR71A	I/O	DQ11	3		True_of_IOR71B	TRUE	W25

Note!

The embedded pins of UG676 package can also be used as user IO.

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOR71B	I/O	DQ11	3		Comp_of_IOR71A	TRUE	Y25
IOR72A	I/O	DQ11	3		True_of_IOR72B	NONE	W23
IOR72B	I/O	DQ11	3		Comp_of_IOR72A	NONE	Y23
IOR73A	I/O	DQS11	3		True_of_IOR73B	TRUE	AB26
IOR73B	I/O	DQS11	3		Comp_of_IOR73A	TRUE	AC26
IOR74A/RPLL3_T_in	I/O	DQ11	3	RPLL3_T_in	True_of_IOR74B	NONE	Y24
IOR74B/RPLL3_C_in	I/O	DQ11	3	RPLL3_C_in	Comp_of_IOR74A	NONE	AA24
IOR76A/RPLL3_T_fb	I/O	DQ11	3	RPLL3_T_fb	True_of_IOR76B	TRUE	Y22
IOR76B/RPLL3_C_fb	I/O	DQ11	3	RPLL3_C_fb	Comp_of_IOR76A	TRUE	AA22
IOR77A	I/O	DQ11	3		True_of_IOR77B	NONE	AA25
IOR77B	I/O	DQ11	3		Comp_of_IOR77A	NONE	AC25
IOR78A	LVDS	DQ11	3		True_of_IOR78B	TRUE	
IOR78B	LVDS	DQ11	3		Comp_of_IOR78A	TRUE	
IOR79A	I/O	DQ11	3		True_of_IOR79B	NONE	AA23
IOR79B	I/O	DQ11	3		Comp_of_IOR79A	NONE	AB23
IOR7A	I/O	DQ15	2		True_of_IOR7B	NONE	J19
IOR7B	I/O	DQ15	2		Comp_of_IOR7A	NONE	K19
IOR80A	LVDS	DQ11	3		True_of_IOR80B	TRUE	
IOR80B	LVDS	DQ11	3		Comp_of_IOR80A	TRUE	
IOR81A	I/O	DQ11	3		True_of_IOR81B	NONE	AB24
IOR81B	I/O	DQ11	3		Comp_of_IOR81A	NONE	AC24
IOR82A	I/O	DQ11	3		True_of_IOR82B	TRUE	AD26
IOR82B	I/O	DQ11	3		Comp_of_IOR82A	TRUE	AE26
IOR83A	I/O	DQ11	3		True_of_IOR83B	NONE	AD25
IOR83B	I/O	DQ11	3		Comp_of_IOR83A	NONE	AE25
IOR8A	I/O	DQS15	2		True_of_IOR8B	TRUE	F24
IOR8B	I/O	DQS15	2		Comp_of_IOR8A	TRUE	G24
IOR9A/RPLL1_T_in	I/O	DQ15	2	RPLL1_T_in	True_of_IOR9B	NONE	F23
IOR9B/RPLL1_C_in	I/O	DQ15	2	RPLL1_C_in	Comp_of_IOR9A	NONE	G23
IOT12A	LVDS	DQ20	0		True_of_IOT12B	TRUE	

Note!							
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Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOT12B	LVDS	DQ20	0		Comp_of_IOT12A	TRUE	
IOT13A	I/O	DQ20	0		True_of_IOT13B	NONE	B4
IOT13B	I/O	DQ20	0		Comp_of_IOT13A	NONE	B6
IOT14A	LVDS	DQ20	0		True_of_IOT14B	TRUE	
IOT14B	LVDS	DQ20	0		Comp_of_IOT14A	TRUE	
IOT15A	I/O	DQ20	0		True_of_IOT15B	NONE	D7
IOT15B	I/O	DQ20	0		Comp_of_IOT15A	NONE	D8
IOT16A	I/O	DQ20	0		True_of_IOT16B	TRUE	H9
IOT16B	I/O	DQ20	0		Comp_of_IOT16A	TRUE	H10
IOT17A	I/O	DQ20	0		True_of_IOT17B	NONE	F7
IOT17B	I/O	DQ20	0		Comp_of_IOT17A	NONE	F8
IOT18A	I/O	DQ20	0		True_of_IOT18B	TRUE	A7
IOT18B	I/O	DQ20	0		Comp_of_IOT18A	TRUE	A8
IOT19A	I/O	DQ20	0		True_of_IOT19B	NONE	G7
IOT19B	I/O	DQ20	0		Comp_of_IOT19A	NONE	G8
IOT20A	I/O	DQ20	0		True_of_IOT20B	TRUE	D9
IOT20B	I/O	DQ20	0		Comp_of_IOT20A	TRUE	D10
IOT21A	I/O	DQ20	0		True_of_IOT21B	NONE	B7
IOT21B	I/O	DQ20	0		Comp_of_IOT21A	NONE	B8
IOT22A	LVDS	DQ20	0		True_of_IOT22B	TRUE	
IOT22B	LVDS	DQ20	0		Comp_of_IOT22A	TRUE	
IOT23A	I/O	DQ20	0		True_of_IOT23B	NONE	J10
IOT23B	I/O	DQ20	0		Comp_of_IOT23A	NONE	J11
IOT24A	I/O	DQ20	0		True_of_IOT24B	TRUE	E9
IOT24B	I/O	DQ20	0		Comp_of_IOT24A	TRUE	E10
IOT25A	I/O	DQ20	0		True_of_IOT25B	NONE	K11
IOT25B	I/O	DQ20	0		Comp_of_IOT25A	NONE	K12
IOT26A	I/O	DQ20	0		True_of_IOT26B	TRUE	C7
IOT26B	I/O	DQ20	0		Comp_of_IOT26A	TRUE	C8
IOT27A	I/O	DQS20	0		True_of_IOT27B	NONE	F9

Note!							
The embedded pins of UG676 package can also be used as user IO.							
Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOT27B	I/O	DQS20	0		Comp_of_IOT27A	NONE	F10
IOT2A	I/O	DQ21	0		True_of_IOT2B	TRUE	C4
IOT2B	I/O	DQ21	0		Comp_of_IOT2A	TRUE	C5
IOT30A	I/O	DQS19	0		True_of_IOT30B	TRUE	G9
IOT30B	I/O	DQS19	0		Comp_of_IOT30A	TRUE	G10
IOT31A	I/O	DQ19	0		True_of_IOT31B	NONE	A9
IOT31B	I/O	DQ19	0		Comp_of_IOT31A	NONE	A10
IOT32A	LVDS	DQ19	0		True_of_IOT32B	TRUE	
IOT32B	LVDS	DQ19	0		Comp_of_IOT32A	TRUE	
IOT33A	I/O	DQ19	0		True_of_IOT33B	NONE	B9
IOT33B	I/O	DQ19	0		Comp_of_IOT33A	NONE	B10
IOT34A	LVDS	DQ19	0		True_of_IOT34B	TRUE	
IOT34B	LVDS	DQ19	0		Comp_of_IOT34A	TRUE	
IOT35A	I/O	DQ19	0		True_of_IOT35B	NONE	D11
IOT35B	I/O	DQ19	0		Comp_of_IOT35A	NONE	D12
IOT36A	I/O	DQ19	0		True_of_IOT36B	TRUE	C10
IOT36B	I/O	DQ19	0		Comp_of_IOT36A	TRUE	C11
IOT37A	I/O	DQ19	0		True_of_IOT37B	NONE	E11
IOT37B	I/O	DQ19	0		Comp_of_IOT37A	NONE	E12
IOT38A	LVDS	DQ19	0		True_of_IOT38B	TRUE	
IOT38B	LVDS	DQ19	0		Comp_of_IOT38A	TRUE	
IOT39A	I/O	DQ19	0		True_of_IOT39B	NONE	A12
IOT39B	I/O	DQ19	0		Comp_of_IOT39A	NONE	A13
IOT3A	I/O	DQ21	0		True_of_IOT3B	NONE	A2
IOT3B	I/O	DQ21	0		Comp_of_IOT3A	NONE	A3
IOT40A	I/O	DQ19	0		True_of_IOT40B	TRUE	G11
IOT40B	I/O	DQ19	0		Comp_of_IOT40A	TRUE	G12
IOT41A	I/O	DQ19	0		True_of_IOT41B	NONE	J12
IOT41B	I/O	DQ19	0		Comp_of_IOT41A	NONE	J13
IOT42A	I/O	DQ19	0		True_of_IOT42B	TRUE	F12

Note!

The embedded pins of UG676 package can also be used as user IO.

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOT42B	I/O	DQ19	0		Comp_of_IOT42A	TRUE	G13
IOT43A	I/O	DQ19	0		True_of_IOT43B	NONE	H12
IOT43B	I/O	DQ19	0		Comp_of_IOT43A	NONE	H13
IOT44A	I/O	DQ19	0		True_of_IOT44B	TRUE	B12
IOT44B	I/O	DQ19	0		Comp_of_IOT44A	TRUE	B13
IOT45A/GCLKT_0	I/O	DQ19	0	GCLKT_0	True_of_IOT45B	NONE	F13
IOT45B/GCLKC_0	I/O	DQ19	0	GCLKC_0	Comp_of_IOT45A	NONE	D13
IOT48A/GCLKT_1	I/O	DQ18	1	GCLKT_1	True_of_IOT48B	TRUE	C12
IOT48B/GCLKC_1	I/O	DQ18	1	GCLKC_1	Comp_of_IOT48A	TRUE	C13
IOT49A	I/O	DQ18	1		True_of_IOT49B	NONE	A14
IOT49B	I/O	DQ18	1		Comp_of_IOT49A	NONE	A15
IOT4A	I/O	DQ21	0		True_of_IOT4B	TRUE	D4
IOT4B	I/O	DQ21	0		Comp_of_IOT4A	TRUE	D5
IOT50A	I/O	DQ18	1		True_of_IOT50B	TRUE	K14
IOT50B	I/O	DQ18	1		Comp_of_IOT50A	TRUE	K16
IOT51A	I/O	DQ18	1		True_of_IOT51B	NONE	G14
IOT51B	I/O	DQ18	1		Comp_of_IOT51A	NONE	G15
IOT52A	I/O	DQ18	1		True_of_IOT52B	TRUE	J14
IOT52B	I/O	DQ18	1		Comp_of_IOT52A	TRUE	J15
IOT53A	I/O	DQ18	1		True_of_IOT53B	NONE	B14
IOT53B	I/O	DQ18	1		Comp_of_IOT53A	NONE	B15
IOT54A	LVDS	DQ18	1		True_of_IOT54B	TRUE	
IOT54B	LVDS	DQ18	1		Comp_of_IOT54A	TRUE	
IOT55A	I/O	DQ18	1		True_of_IOT55B	NONE	C15
IOT55B	I/O	DQ18	1		Comp_of_IOT55A	NONE	C16
IOT56A	I/O	DQ18	1		True_of_IOT56B	TRUE	D14
IOT56B	I/O	DQ18	1		Comp_of_IOT56A	TRUE	D15
IOT57A	I/O	DQ18	1		True_of_IOT57B	NONE	A17
IOT57B	I/O	DQ18	1		Comp_of_IOT57A	NONE	A18
IOT58A	I/O	DQ18	1		True_of_IOT58B	TRUE	E14

Note!							
The embedded pins of UG676 package can also be used as user IO.							
Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOT58B	I/O	DQ18	1		Comp_of_IOT58A	TRUE	E15
IOT59A	I/O	DQ18	1		True_of_IOT59B	NONE	H15
IOT59B	I/O	DQ18	1		Comp_of_IOT59A	NONE	H17
IOT5A	I/O	DQ21	0		True_of_IOT5B	NONE	B2
IOT5B	I/O	DQ21	0		Comp_of_IOT5A	NONE	B3
IOT60A	LVDS	DQ18	1		True_of_IOT60B	TRUE	
IOT60B	LVDS	DQ18	1		Comp_of_IOT60A	TRUE	
IOT61A	I/O	DQ18	1		True_of_IOT61B	NONE	F14
IOT61B	I/O	DQ18	1		Comp_of_IOT61A	NONE	F15
IOT62A	I/O	DQ18	1		True_of_IOT62B	TRUE	B17
IOT62B	I/O	DQ18	1		Comp_of_IOT62A	TRUE	B18
IOT63A	I/O	DQS18	1		True_of_IOT63B	NONE	D16
IOT63B	I/O	DQS18	1		Comp_of_IOT63A	NONE	D17
IOT66A	I/O	DQS17	1		True_of_IOT66B	TRUE	C17
IOT66B	I/O	DQS17	1		Comp_of_IOT66A	TRUE	C18
IOT67A	I/O	DQ17	1		True_of_IOT67B	NONE	A19
IOT67B	I/O	DQ17	1		Comp_of_IOT67A	NONE	A20
IOT68A	I/O	DQ17	1		True_of_IOT68B	TRUE	G16
IOT68B	I/O	DQ17	1		Comp_of_IOT68A	TRUE	G17
IOT69A	I/O	DQ17	1		True_of_IOT69B	NONE	D18
IOT69B	I/O	DQ17	1		Comp_of_IOT69A	NONE	D19
IOT6A	I/O	DQ21	0		True_of_IOT6B	TRUE	C6
IOT6B	I/O	DQ21	0		Comp_of_IOT6A	TRUE	D6
IOT70A	I/O	DQ17	1		True_of_IOT70B	TRUE	J16
IOT70B	I/O	DQ17	1		Comp_of_IOT70A	TRUE	J17
IOT71A	I/O	DQ17	1		True_of_IOT71B	NONE	E17
IOT71B	I/O	DQ17	1		Comp_of_IOT71A	NONE	E18
IOT72A	I/O	DQ17	1		True_of_IOT72B	TRUE	B19
IOT72B	I/O	DQ17	1		Comp_of_IOT72A	TRUE	B20
IOT73A	I/O	DQ17	1		True_of_IOT73B	NONE	F17

Note!

The embedded pins of UG676 package can also be used as user IO.

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOT73B	I/O	DQ17	1		Comp_of_IOT73A	NONE	F18
IOT74A	I/O	DQ17	1		True_of_IOT74B	TRUE	F19
IOT74B	I/O	DQ17	1		Comp_of_IOT74A	TRUE	F20
IOT75A	I/O	DQ17	1		True_of_IOT75B	NONE	A22
IOT75B	I/O	DQ17	1		Comp_of_IOT75A	NONE	A23
IOT76A	I/O	DQ17	1		True_of_IOT76B	TRUE	C20
IOT76B	I/O	DQ17	1		Comp_of_IOT76A	TRUE	C21
IOT77A	I/O	DQ17	1		True_of_IOT77B	NONE	H18
IOT77B	I/O	DQ17	1		Comp_of_IOT77A	NONE	H20
IOT78A	LVDS	DQ17	1		True_of_IOT78B	TRUE	
IOT78B	LVDS	DQ17	1		Comp_of_IOT78A	TRUE	
IOT79A	I/O	DQ17	1		True_of_IOT79B	NONE	G18
IOT79B	I/O	DQ17	1		Comp_of_IOT79A	NONE	G19
IOT7A	I/O	DQ21	0		True_of_IOT7B	NONE	J8
IOT7B	I/O	DQ21	0		Comp_of_IOT7A	NONE	J9
IOT80A	LVDS	DQ17	1		True_of_IOT80B	TRUE	
IOT80B	LVDS	DQ17	1		Comp_of_IOT80A	TRUE	
IOT81A	I/O	DQ17	1		True_of_IOT81B	NONE	D20
IOT81B	I/O	DQ17	1		Comp_of_IOT81A	NONE	D21
IOT84A	I/O	DQS16	1		True_of_IOT84B	TRUE	E20
IOT84B	I/O	DQS16	1		Comp_of_IOT84A	TRUE	E21
IOT85A	I/O	DQ16	1		True_of_IOT85B	NONE	B21
IOT85B	I/O	DQ16	1		Comp_of_IOT85A	NONE	B23
IOT86A	I/O	DQ16	1		True_of_IOT86B	TRUE	G20
IOT86B	I/O	DQ16	1		Comp_of_IOT86A	TRUE	G21
IOT87A	I/O	DQ16	1		True_of_IOT87B	NONE	C22
IOT87B	I/O	DQ16	1		Comp_of_IOT87A	NONE	C23
IOT88A	I/O	DQ16	1		True_of_IOT88B	TRUE	A24
IOT88B	I/O	DQ16	1		Comp_of_IOT88A	TRUE	A25
IOT89A	I/O	DQ16	1		True_of_IOT89B	NONE	D22

Note!							
The embedded pins of UG676 package can also be used as user IO.							
Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOT89B	I/O	DQ16	1		Comp_of_IOT89A	NONE	D23
IOT8A	I/O	DQ21	0		True_of_IOT8B	TRUE	A4
IOT8B	I/O	DQ21	0		Comp_of_IOT8A	TRUE	A5
IOT90A	I/O	DQ16	1		True_of_IOT90B	TRUE	B24
IOT90B	I/O	DQ16	1		Comp_of_IOT90A	TRUE	B25
IOT91A	I/O	DQ16	1		True_of_IOT91B	NONE	E23
IOT91B/JTAGSEL_N	I/O	DQ16	1	JTAGSEL_N	Comp_of_IOT91A	NONE	D24
IOT9A	I/O	DQS21	0		True_of_IOT9B	NONE	E6
IOT9B	I/O	DQS21	0		Comp_of_IOT9A	NONE	E7
VCC	Power		N/A				K15
VCC	Power		N/A				L12
VCC	Power		N/A				L14
VCC	Power		N/A				L16
VCC	Power		N/A				T13
VCC	Power		N/A				M13
VCC	Power		N/A				U12
VCC	Power		N/A				M17
VCC	Power		N/A				N12
VCC	Power		N/A				N13
VCC	Power		N/A				N14
VCC	Power		N/A				N16
VCC	Power		N/A				P11
VCC	Power		N/A				P13
VCC	Power		N/A				P14
VCC	Power		N/A				P15
VCC	Power		N/A				R12
VCC	Power		N/A				R14
VCC	Power		N/A				R16
VCCIO0	Power		N/A				B5
VCCIO0	Power		N/A				B11

Note!

The embedded pins of UG676 package can also be used as user IO.

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
VCCIO0	Power		N/A				E8
VCCIO0	Power		N/A				E13
VCCIO0	Power		N/A				H11
VCCIO1	Power		N/A				B16
VCCIO1	Power		N/A				B22
VCCIO1	Power		N/A				E19
VCCIO1	Power		N/A				H16
VCCIO2	Power		N/A				E25
VCCIO2	Power		N/A				H22
VCCIO2	Power		N/A				L25
VCCIO2	Power		N/A				L19
VCCIO2	Power		N/A				N22
VCCIO3	Power		N/A				T25
VCCIO3	Power		N/A				T19
VCCIO3	Power		N/A				W22
VCCIO3	Power		N/A				AB25
VCCIO4	Power		N/A				AE22
VCCIO4	Power		N/A				AB19
VCCIO4	Power		N/A				W16
VCCIO4	Power		N/A				AE16
VCCIO4	Power		N/A				AB14
VCCIO5	Power		N/A				AE11
VCCIO5	Power		N/A				W11
VCCIO5	Power		N/A				AB8
VCCIO5	Power		N/A				AE5
VCCIO6	Power		N/A				AB2
VCCIO6	Power		N/A				T2
VCCIO6	Power		N/A				W5
VCCIO6	Power		N/A				P5
VCCIO6	Power		N/A				T8

Note!							
The embedded pins of UG676 package can also be used as user IO.							
Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
VCCIO7	Power		N/A				L2
VCCIO7	Power		N/A				L8
VCCIO7	Power		N/A				E2
VCCIO7	Power		N/A				H5
VCCPLLL	Power		N/A				M15
VCCPLLL	Power		N/A				M11
VCCPLLR	Power		N/A				T15
VCCPLLR	Power		N/A				T11
VCCX	Power		N/A				AB5
VCCX	Power		N/A				AB11
VCCX	Power		N/A				AB22
VCCX	Power		N/A				E5
VCCX	Power		N/A				E16
VCCX	Power		N/A				E22
VCCX	Power		N/A				J18
VCCX	Power		N/A				K13
VCCX	Power		N/A				L5
VCCX	Power		N/A				N10
VCCX	Power		N/A				P17
VCCX	Power		N/A				T22
VCCX	Power		N/A				U14
VCCX	Power		N/A				V9
VSS	Ground		N/A				A1
VSS	Ground		N/A				A6
VSS	Ground		N/A				A11
VSS	Ground		N/A				A16
VSS	Ground		N/A				A21
VSS	Ground		N/A				A26
VSS	Ground		N/A				AA1
VSS	Ground		N/A				AA6

Note!							
The embedded pins of UG676 package can also be used as user IO.							
Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
VSS	Ground		N/A				AA11
VSS	Ground		N/A				AA16
VSS	Ground		N/A				AA21
VSS	Ground		N/A				AA26
VSS	Ground		N/A				AD3
VSS	Ground		N/A				AD8
VSS	Ground		N/A				AD13
VSS	Ground		N/A				AD18
VSS	Ground		N/A				AD24
VSS	Ground		N/A				AF1
VSS	Ground		N/A				AF6
VSS	Ground		N/A				AF11
VSS	Ground		N/A				AF16
VSS	Ground		N/A				AF21
VSS	Ground		N/A				AF26
VSS	Ground		N/A				C3
VSS	Ground		N/A				C9
VSS	Ground		N/A				C14
VSS	Ground		N/A				C19
VSS	Ground		N/A				C24
VSS	Ground		N/A				F1
VSS	Ground		N/A				F6
VSS	Ground		N/A				F11
VSS	Ground		N/A				F16
VSS	Ground		N/A				F21
VSS	Ground		N/A				F26
VSS	Ground		N/A				H3
VSS	Ground		N/A				H8
VSS	Ground		N/A				H14
VSS	Ground		N/A				H19

Note!							
The embedded pins of UG676 package can also be used as user IO.							
Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
VSS	Ground		N/A				J24
VSS	Ground		N/A				K10
VSS	Ground		N/A				K17
VSS	Ground		N/A				L1
VSS	Ground		N/A				L6
VSS	Ground		N/A				L11
VSS	Ground		N/A				L13
VSS	Ground		N/A				L15
VSS	Ground		N/A				L21
VSS	Ground		N/A				L26
VSS	Ground		N/A				M12
VSS	Ground		N/A				M14
VSS	Ground		N/A				M16
VSS	Ground		N/A				N3
VSS	Ground		N/A				N8
VSS	Ground		N/A				N11
VSS	Ground		N/A				N15
VSS	Ground		N/A				P12
VSS	Ground		N/A				P16
VSS	Ground		N/A				P19
VSS	Ground		N/A				P24
VSS	Ground		N/A				R11
VSS	Ground		N/A				R13
VSS	Ground		N/A				R15
VSS	Ground		N/A				T1
VSS	Ground		N/A				T6
VSS	Ground		N/A				T12
VSS	Ground		N/A				T14
VSS	Ground		N/A				T16
VSS	Ground		N/A				T21

Note!

The embedded pins of UG676 package can also be used as user IO.

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
VSS	Ground		N/A				T26
VSS	Ground		N/A				U10
VSS	Ground		N/A				U13
VSS	Ground		N/A				U17
VSS	Ground		N/A				V3
VSS	Ground		N/A				W8
VSS	Ground		N/A				W14
VSS	Ground		N/A				W19
VSS	Ground		N/A				W24

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
BANK7 True LVDS Pair							
IOL11A/LPLL1_T_fb	I/O	DQ1	7	LPLL1_T_fb	True_of_IOL11B	TRUE	G6
IOL11B/LPLL1_C_fb	I/O	DQ1	7	LPLL1_C_fb	Comp_of_IOL11A	TRUE	F5
IOL13A	LVDS	DQ1	7		True_of_IOL13B	TRUE	G3
IOL13B	LVDS	DQ1	7		Comp_of_IOL13A	TRUE	F2
IOL15A	I/O	DQ1	7		True_of_IOL15B	TRUE	F4
IOL15B	I/O	DQ1	7		Comp_of_IOL15A	TRUE	G5
IOL17A	LVDS	DQ1	7		True_of_IOL17B	TRUE	
IOL17B	LVDS	DQ1	7		Comp_of_IOL17A	TRUE	
IOL20A	I/O	DQ1	7		True_of_IOL20B	TRUE	H7
IOL20B	I/O	DQ1	7		Comp_of_IOL20A	TRUE	J7
IOL22A	LVDS	DQ1	7		True_of_IOL22B	TRUE	
IOL22B	LVDS	DQ1	7		Comp_of_IOL22A	TRUE	
IOL24A	LVDS	DQ1	7		True_of_IOL24B	TRUE	H6
IOL24B	LVDS	DQ1	7		Comp_of_IOL24A	TRUE	J6
IOL26A	LVDS	DQ1	7		True_of_IOL26B	TRUE	
IOL26B	LVDS	DQ1	7		Comp_of_IOL26A	TRUE	
IOL28A	I/O	DQ2	7		True_of_IOL28B	TRUE	J4
IOL28B	I/O	DQ2	7		Comp_of_IOL28A	TRUE	K4
IOL2A	I/O	DQ0	7		True_of_IOL2B	TRUE	C2
IOL2B	I/O	DQ0	7		Comp_of_IOL2A	TRUE	D2
IOL30A	LVDS	DQ2	7		True_of_IOL30B	TRUE	
IOL30B	LVDS	DQ2	7		Comp_of_IOL30A	TRUE	
IOL32A	I/O	DQ2	7		True_of_IOL32B	TRUE	M7
IOL32B	I/O	DQ2	7		Comp_of_IOL32A	TRUE	N7
IOL34A	I/O	DQS2	7		True_of_IOL34B	TRUE	J1
IOL34B	I/O	DQS2	7		Comp_of_IOL34A	TRUE	K1
IOL37A/LPLL2_T_fb	I/O	DQ2	7	LPLL2_T_fb	True_of_IOL37B	TRUE	L3
IOL37B/LPLL2_C_fb	I/O	DQ2	7	LPLL2_C_fb	Comp_of_IOL37A	TRUE	M3
IOL39A	LVDS	DQ2	7		True_of_IOL39B	TRUE	
IOL39B	LVDS	DQ2	7		Comp_of_IOL39A	TRUE	
IOL41A	LVDS	DQ2	7		True_of_IOL41B	TRUE	N6
IOL41B	LVDS	DQ2	7		Comp_of_IOL41A	TRUE	P6

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOL43A	I/O	DQ2	7		True_of_IOL43B	TRUE	N1
IOL43B	I/O	DQ2	7		Comp_of_IOL43A	TRUE	M1
IOL4A	LVDS	DQ0	7		True_of_IOL4B	TRUE	
IOL4B	LVDS	DQ0	7		Comp_of_IOL4A	TRUE	
IOL6A	LVDS	DQ0	7		True_of_IOL6B	TRUE	
IOL6B	LVDS	DQ0	7		Comp_of_IOL6A	TRUE	
IOL8A	I/O	DQS0	7		True_of_IOL8B	TRUE	D3
IOL8B	I/O	DQS0	7		Comp_of_IOL8A	TRUE	E4
BANK6 True LVDS Pair							
IOL46A/GCLKT_6	I/O	DQ3	6	GCLKT_6	True_of_IOL46B	TRUE	P3
IOL46B/GCLKC_6	I/O	DQ3	6	GCLKC_6	Comp_of_IOL46A	TRUE	R3
IOL48A	I/O	DQ3	6		True_of_IOL48B	TRUE	R5
IOL48B	I/O	DQ3	6		Comp_of_IOL48A	TRUE	R6
IOL50A	I/O	DQ3	6		True_of_IOL50B	TRUE	R7
IOL50B	I/O	DQ3	6		Comp_of_IOL50A	TRUE	T7
IOL52A	I/O	DQ3	6		True_of_IOL52B	TRUE	P1
IOL52B	I/O	DQ3	6		Comp_of_IOL52A	TRUE	R1
IOL55A	I/O	DQ3	6		True_of_IOL55B	TRUE	R8
IOL55B	I/O	DQ3	6		Comp_of_IOL55A	TRUE	U8
IOL57A	LVDS	DQ3	6		True_of_IOL57B	TRUE	T3
IOL57B	LVDS	DQ3	6		Comp_of_IOL57A	TRUE	U2
IOL59A	LVDS	DQ3	6		True_of_IOL59B	TRUE	U5
IOL59B	LVDS	DQ3	6		Comp_of_IOL59A	TRUE	V5
IOL61A	LVDS	DQ3	6		True_of_IOL61B	TRUE	
IOL61B	LVDS	DQ3	6		Comp_of_IOL61A	TRUE	
IOL67A	LVDS	DQ4	6		True_of_IOL67B	TRUE	V4
IOL67B	LVDS	DQ4	6		Comp_of_IOL67A	TRUE	W4
IOL69A	LVDS	DQ4	6		True_of_IOL69B	TRUE	
IOL69B	LVDS	DQ4	6		Comp_of_IOL69A	TRUE	
IOL71A	LVDS	DQ4	6		True_of_IOL71B	TRUE	U3
IOL71B	LVDS	DQ4	6		Comp_of_IOL71A	TRUE	W3
IOL73A	I/O	DQS4	6		True_of_IOL73B	TRUE	AB1
IOL73B	I/O	DQS4	6		Comp_of_IOL73A	TRUE	AC1

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOL76A/LPLL3_T_fb	I/O	DQ4	6	LPLL3_T_fb	True_of_IOL76B	TRUE	Y4
IOL76B/LPLL3_C_fb	I/O	DQ4	6	LPLL3_C_fb	Comp_of_IOL76A	TRUE	AA4
IOL78A	LVDS	DQ4	6		True_of_IOL78B	TRUE	
IOL78B	LVDS	DQ4	6		Comp_of_IOL78A	TRUE	
IOL80A	LVDS	DQ4	6		True_of_IOL80B	TRUE	
IOL80B	LVDS	DQ4	6		Comp_of_IOL80A	TRUE	
IOL82A	I/O	DQ4	6		True_of_IOL82B	TRUE	AD1
IOL82B	I/O	DQ4	6		Comp_of_IOL82A	TRUE	AE1
BANK5 True LVDS Pair							
IOB12A	LVDS	DQ6	5		True_of_IOB12B	TRUE	
IOB12B	LVDS	DQ6	5		Comp_of_IOB12A	TRUE	
IOB14A	LVDS	DQ6	5		True_of_IOB14B	TRUE	
IOB14B	LVDS	DQ6	5		Comp_of_IOB14A	TRUE	
IOB16A	I/O	DQ6	5		True_of_IOB16B	TRUE	W9
IOB16B	I/O	DQ6	5		Comp_of_IOB16A	TRUE	W10
IOB18A	I/O	DQ6	5		True_of_IOB18B	TRUE	AE7
IOB18B	I/O	DQ6	5		Comp_of_IOB18A	TRUE	AE8
IOB20A	I/O	DQ6	5		True_of_IOB20B	TRUE	AC9
IOB20B	I/O	DQ6	5		Comp_of_IOB20A	TRUE	AC10
IOB22A	LVDS	DQ6	5		True_of_IOB22B	TRUE	
IOB22B	LVDS	DQ6	5		Comp_of_IOB22A	TRUE	
IOB24A	I/O	DQ6	5		True_of_IOB24B	TRUE	AB7
IOB24B	I/O	DQ6	5		Comp_of_IOB24A	TRUE	AB9
IOB26A	I/O	DQ6	5		True_of_IOB26B	TRUE	AD7
IOB26B	I/O	DQ6	5		Comp_of_IOB26A	TRUE	AD9
IOB2A	I/O	DQ5	5		True_of_IOB2B	TRUE	AC4
IOB2B	I/O	DQ5	5		Comp_of_IOB2A	TRUE	AD4
IOB30A	I/O	DQS7	5		True_of_IOB30B	TRUE	Y8
IOB30B	I/O	DQS7	5		Comp_of_IOB30A	TRUE	Y9
IOB32A	LVDS	DQ7	5		True_of_IOB32B	TRUE	
IOB32B	LVDS	DQ7	5		Comp_of_IOB32A	TRUE	
IOB34A	LVDS	DQ7	5		True_of_IOB34B	TRUE	
IOB34B	LVDS	DQ7	5		Comp_of_IOB34A	TRUE	

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOB36A	I/O	DQ7	5		True_of_IOB36B	TRUE	AD10
IOB36B	I/O	DQ7	5		Comp_of_IOB36A	TRUE	AD11
IOB38A	LVDS	DQ7	5		True_of_IOB38B	TRUE	
IOB38B	LVDS	DQ7	5		Comp_of_IOB38A	TRUE	
IOB40A	I/O	DQ7	5		True_of_IOB40B	TRUE	AB10
IOB40B	I/O	DQ7	5		Comp_of_IOB40A	TRUE	AB12
IOB42A	I/O	DQ7	5		True_of_IOB42B	TRUE	AA10
IOB42B	I/O	DQ7	5		Comp_of_IOB42A	TRUE	AA12
IOB44A	I/O	DQ7	5		True_of_IOB44B	TRUE	AF12
IOB44B	I/O	DQ7	5		Comp_of_IOB44A	TRUE	AF13
IOB4A	I/O	DQ5	5		True_of_IOB4B	TRUE	AD5
IOB4B	I/O	DQ5	5		Comp_of_IOB4A	TRUE	AD6
IOB6A	I/O	DQ5	5		True_of_IOB6B	TRUE	AC5
IOB6B	I/O	DQ5	5		Comp_of_IOB6A	TRUE	AC6
IOB8A	I/O	DQ5	5		True_of_IOB8B	TRUE	AF4
IOB8B	I/O	DQ5	5		Comp_of_IOB8A	TRUE	AF5
BANK4 True LVDS Pair							
IOB48A/GCLKT_4	I/O	DQ8	4	GCLKT_4	True_of_IOB48B	TRUE	AA13
IOB48B/GCLKC_4	I/O	DQ8	4	GCLKC_4	Comp_of_IOB48A	TRUE	AB13
IOB50A	I/O	DQ8	4		True_of_IOB50B	TRUE	V14
IOB50B	I/O	DQ8	4		Comp_of_IOB50A	TRUE	V15
IOB52A	I/O	DQ8	4		True_of_IOB52B	TRUE	Y14
IOB52B	I/O	DQ8	4		Comp_of_IOB52A	TRUE	Y15
IOB54A	LVDS	DQ8	4		True_of_IOB54B	TRUE	
IOB54B	LVDS	DQ8	4		Comp_of_IOB54A	TRUE	
IOB56A	I/O	DQ8	4		True_of_IOB56B	TRUE	AB15
IOB56B	I/O	DQ8	4		Comp_of_IOB56A	TRUE	AB16
IOB58A	LVDS	DQ8	4		True_of_IOB58B	TRUE	
IOB58B	LVDS	DQ8	4		Comp_of_IOB58A	TRUE	
IOB60A	I/O	DQ8	4		True_of_IOB60B	TRUE	AE15
IOB60B	I/O	DQ8	4		Comp_of_IOB60A	TRUE	AE17
IOB62A	I/O	DQ8	4		True_of_IOB62B	TRUE	AF17

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOB62B	I/O	DQ8	4		Comp_of_IOB62A	TRUE	AF18
IOB66A	I/O	DQS9	4		True_of_IOB66B	TRUE	AA17
IOB66B	I/O	DQS9	4		Comp_of_IOB66A	TRUE	AA18
IOB68A	I/O	DQ9	4		True_of_IOB68B	TRUE	V16
IOB68B	I/O	DQ9	4		Comp_of_IOB68A	TRUE	V17
IOB70A	I/O	DQ9	4		True_of_IOB70B	TRUE	W15
IOB70B	I/O	DQ9	4		Comp_of_IOB70A	TRUE	W17
IOB72A	I/O	DQ9	4		True_of_IOB72B	TRUE	AF19
IOB72B	I/O	DQ9	4		Comp_of_IOB72A	TRUE	AF20
IOB74A	I/O	DQ9	4		True_of_IOB74B	TRUE	Y20
IOB74B	I/O	DQ9	4		Comp_of_IOB74A	TRUE	Y21
IOB76A	I/O	DQ9	4		True_of_IOB76B	TRUE	AA19
IOB76B	I/O	DQ9	4		Comp_of_IOB76A	TRUE	AA20
IOB78A	LVDS	DQ9	4		True_of_IOB78B	TRUE	
IOB78B	LVDS	DQ9	4		Comp_of_IOB78A	TRUE	
IOB80A	LVDS	DQ9	4		True_of_IOB80B	TRUE	
IOB80B	LVDS	DQ9	4		Comp_of_IOB80A	TRUE	
IOB84A	I/O	DQS10	4		True_of_IOB84B	TRUE	AC20
IOB84B	I/O	DQS10	4		Comp_of_IOB84A	TRUE	AC21
IOB86A	I/O	DQ10	4		True_of_IOB86B	TRUE	W18
IOB86B	I/O	DQ10	4		Comp_of_IOB86A	TRUE	W20
IOB88A	I/O	DQ10	4		True_of_IOB88B	TRUE	AE23
IOB88B	I/O	DQ10	4		Comp_of_IOB88A	TRUE	AE24
IOB90A	I/O	DQ10	4		True_of_IOB90B	TRUE	AF24
IOB90B	I/O	DQ10	4		Comp_of_IOB90A	TRUE	AF25
BANK3 True LVDS Pair							
IOR46A/GCLKT_3	I/O	DQ12	3	GCLKT_3	True_of_IOR46B	TRUE	R24
IOR46B/GCLKC_3	I/O	DQ12	3	GCLKC_3	Comp_of_IOR46A	TRUE	T24
IOR48A/MODE2	I/O	DQ12	3	MODE2	True_of_IOR48B	TRUE	R23
IOR48B/RECONFIG_N	I/O	DQ12	3	RECONFIG_N	Comp_of_IOR48A	TRUE	T23
IOR50A/MI/D7	I/O	DQ12	3	MI/D7	True_of_IOR50B	TRUE	R22
IOR50B/MO/D6	I/O	DQ12	3	MO/D6	Comp_of_IOR50A	TRUE	R21

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOR52A/FASTRD_N/D3	I/O	DQ12	3	FASTRD_N/D3	True_of_IOR52B	TRUE	P25
IOR52B/SI/D2	I/O	DQ12	3	SI/D2	Comp_of_IOR52A	TRUE	R25
IOR55A/DIN/CLKHOLD_N	I/O	DQ12	3	DIN/CLKHOLD_N	True_of_IOR55B	TRUE	U25
IOR55B/DOOUT/WE_N	I/O	DQ12	3	DOOUT/WE_N	Comp_of_IOR55A	TRUE	V25
IOR57A	LVDS	DQ12	3		True_of_IOR57B	TRUE	U24
IOR57B	LVDS	DQ12	3		Comp_of_IOR57A	TRUE	V24
IOR59A	LVDS	DQ12	3		True_of_IOR59B	TRUE	U23
IOR59B	LVDS	DQ12	3		Comp_of_IOR59A	TRUE	V23
IOR61A	LVDS	DQ12	3		True_of_IOR61B	TRUE	
IOR61B	LVDS	DQ12	3		Comp_of_IOR61A	TRUE	
IOR67A	LVDS	DQ11	3		True_of_IOR67B	TRUE	U20
IOR67B	LVDS	DQ11	3		Comp_of_IOR67A	TRUE	V20
IOR69A	LVDS	DQ11	3		True_of_IOR69B	TRUE	V19
IOR69B	LVDS	DQ11	3		Comp_of_IOR69A	TRUE	W21
IOR71A	I/O	DQ11	3		True_of_IOR71B	TRUE	W25
IOR71B	I/O	DQ11	3		Comp_of_IOR71A	TRUE	Y25
IOR73A	I/O	DQS11	3		True_of_IOR73B	TRUE	AB26
IOR73B	I/O	DQS11	3		Comp_of_IOR73A	TRUE	AC26
IOR76A/RPLL3_T_fb	I/O	DQ11	3	RPLL3_T_fb	True_of_IOR76B	TRUE	Y22
IOR76B/RPLL3_C_fb	I/O	DQ11	3	RPLL3_C_fb	Comp_of_IOR76A	TRUE	AA22
IOR78A	LVDS	DQ11	3		True_of_IOR78B	TRUE	
IOR78B	LVDS	DQ11	3		Comp_of_IOR78A	TRUE	
IOR80A	LVDS	DQ11	3		True_of_IOR80B	TRUE	
IOR80B	LVDS	DQ11	3		Comp_of_IOR80A	TRUE	
IOR82A	I/O	DQ11	3		True_of_IOR82B	TRUE	AD26
IOR82B	I/O	DQ11	3		Comp_of_IOR82A	TRUE	AE26
BANK2 True LVDS Pair							
IOR11A/RPLL1_T_fb	I/O	DQ14	2	RPLL1_T_fb	True_of_IOR11B	TRUE	F22
IOR11B/RPLL1_C_fb	I/O	DQ14	2	RPLL1_C_fb	Comp_of_IOR11A	TRUE	G22
IOR13A	LVDS	DQ14	2		True_of_IOR13B	TRUE	G25
IOR13B	LVDS	DQ14	2		Comp_of_IOR13A	TRUE	H25
IOR15A	I/O	DQ14	2		True_of_IOR15B	TRUE	H24
IOR15B	I/O	DQ14	2		Comp_of_IOR15A	TRUE	J23

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOR17A	LVDS	DQ14	2		True_of_IOR17B	TRUE	
IOR17B	LVDS	DQ14	2		Comp_of_IOR17A	TRUE	
IOR20A	I/O	DQ14	2		True_of_IOR20B	TRUE	K24
IOR20B	I/O	DQ14	2		Comp_of_IOR20A	TRUE	L24
IOR22A	LVDS	DQ14	2		True_of_IOR22B	TRUE	K23
IOR22B	LVDS	DQ14	2		Comp_of_IOR22A	TRUE	L23
IOR24A	LVDS	DQ14	2		True_of_IOR24B	TRUE	
IOR24B	LVDS	DQ14	2		Comp_of_IOR24A	TRUE	
IOR26A	LVDS	DQ14	2		True_of_IOR26B	TRUE	
IOR26B	LVDS	DQ14	2		Comp_of_IOR26A	TRUE	
IOR28A	I/O	DQ13	2		True_of_IOR28B	TRUE	K21
IOR28B	I/O	DQ13	2		Comp_of_IOR28A	TRUE	M21
IOR2A	I/O	DQ15	2		True_of_IOR2B	TRUE	C25
IOR2B	I/O	DQ15	2		Comp_of_IOR2A	TRUE	D25
IOR30A	LVDS	DQ13	2		True_of_IOR30B	TRUE	
IOR30B	LVDS	DQ13	2		Comp_of_IOR30A	TRUE	
IOR32A	I/O	DQ13	2		True_of_IOR32B	TRUE	M19
IOR32B	I/O	DQ13	2		Comp_of_IOR32A	TRUE	N19
IOR34A	I/O	DQS13	2		True_of_IOR34B	TRUE	J25
IOR34B	I/O	DQS13	2		Comp_of_IOR34A	TRUE	K25
IOR37A/RPLL2_T_fb	I/O	DQ13	2	RPLL2_T_fb	True_of_IOR37B	TRUE	N20
IOR37B/RPLL2_C_fb	I/O	DQ13	2	RPLL2_C_fb	Comp_of_IOR37A	TRUE	P18
IOR39A	LVDS	DQ13	2		True_of_IOR39B	TRUE	
IOR39B	LVDS	DQ13	2		Comp_of_IOR39A	TRUE	
IOR41A	LVDS	DQ13	2		True_of_IOR41B	TRUE	
IOR41B	LVDS	DQ13	2		Comp_of_IOR41A	TRUE	
IOR43A/TCK	I/O	DQ13	2	TCK	True_of_IOR43B	TRUE	N25
IOR43B/TDI	I/O	DQ13	2	TDI	Comp_of_IOR43A	TRUE	M25
IOR4A	LVDS	DQ15	2		True_of_IOR4B	TRUE	E24
IOR4B	LVDS	DQ15	2		Comp_of_IOR4A	TRUE	F25
IOR6A	LVDS	DQ15	2		True_of_IOR6B	TRUE	
IOR6B	LVDS	DQ15	2		Comp_of_IOR6A	TRUE	
IOR8A	I/O	DQS15	2		True_of_IOR8B	TRUE	F24

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOR8B	I/O	DQS15	2		Comp_of_IOR8A	TRUE	G24

BANK1 True LVDS Pair

IOT48A/GCLKT_1	I/O	DQ18	1	GCLKT_1	True_of_IOT48B	TRUE	C12
IOT48B/GCLKC_1	I/O	DQ18	1	GCLKC_1	Comp_of_IOT48A	TRUE	C13
IOT50A	I/O	DQ18	1		True_of_IOT50B	TRUE	K14
IOT50B	I/O	DQ18	1		Comp_of_IOT50A	TRUE	K16
IOT52A	I/O	DQ18	1		True_of_IOT52B	TRUE	J14
IOT52B	I/O	DQ18	1		Comp_of_IOT52A	TRUE	J15
IOT54A	LVDS	DQ18	1		True_of_IOT54B	TRUE	
IOT54B	LVDS	DQ18	1		Comp_of_IOT54A	TRUE	
IOT56A	I/O	DQ18	1		True_of_IOT56B	TRUE	D14
IOT56B	I/O	DQ18	1		Comp_of_IOT56A	TRUE	D15
IOT58A	I/O	DQ18	1		True_of_IOT58B	TRUE	E14
IOT58B	I/O	DQ18	1		Comp_of_IOT58A	TRUE	E15
IOT60A	LVDS	DQ18	1		True_of_IOT60B	TRUE	
IOT60B	LVDS	DQ18	1		Comp_of_IOT60A	TRUE	
IOT62A	I/O	DQ18	1		True_of_IOT62B	TRUE	B17
IOT62B	I/O	DQ18	1		Comp_of_IOT62A	TRUE	B18
IOT66A	I/O	DQS17	1		True_of_IOT66B	TRUE	C17
IOT66B	I/O	DQS17	1		Comp_of_IOT66A	TRUE	C18
IOT68A	I/O	DQ17	1		True_of_IOT68B	TRUE	G16
IOT68B	I/O	DQ17	1		Comp_of_IOT68A	TRUE	G17
IOT70A	I/O	DQ17	1		True_of_IOT70B	TRUE	J16
IOT70B	I/O	DQ17	1		Comp_of_IOT70A	TRUE	J17
IOT72A	I/O	DQ17	1		True_of_IOT72B	TRUE	B19
IOT72B	I/O	DQ17	1		Comp_of_IOT72A	TRUE	B20
IOT74A	I/O	DQ17	1		True_of_IOT74B	TRUE	F19
IOT74B	I/O	DQ17	1		Comp_of_IOT74A	TRUE	F20
IOT76A	I/O	DQ17	1		True_of_IOT76B	TRUE	C20
IOT76B	I/O	DQ17	1		Comp_of_IOT76A	TRUE	C21
IOT78A	LVDS	DQ17	1		True_of_IOT78B	TRUE	
IOT78B	LVDS	DQ17	1		Comp_of_IOT78A	TRUE	

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOT80A	LVDS	DQ17	1		True_of_IOT80B	TRUE	
IOT80B	LVDS	DQ17	1		Comp_of_IOT80A	TRUE	
IOT84A	I/O	DQS16	1		True_of_IOT84B	TRUE	E20
IOT84B	I/O	DQS16	1		Comp_of_IOT84A	TRUE	E21
IOT86A	I/O	DQ16	1		True_of_IOT86B	TRUE	G20
IOT86B	I/O	DQ16	1		Comp_of_IOT86A	TRUE	G21
IOT88A	I/O	DQ16	1		True_of_IOT88B	TRUE	A24
IOT88B	I/O	DQ16	1		Comp_of_IOT88A	TRUE	A25
IOT90A	I/O	DQ16	1		True_of_IOT90B	TRUE	B24
IOT90B	I/O	DQ16	1		Comp_of_IOT90A	TRUE	B25
BANK0 True LVDS Pair							
IOT12A	LVDS	DQ20	0		True_of_IOT12B	TRUE	
IOT12B	LVDS	DQ20	0		Comp_of_IOT12A	TRUE	
IOT14A	LVDS	DQ20	0		True_of_IOT14B	TRUE	
IOT14B	LVDS	DQ20	0		Comp_of_IOT14A	TRUE	
IOT16A	I/O	DQ20	0		True_of_IOT16B	TRUE	H9
IOT16B	I/O	DQ20	0		Comp_of_IOT16A	TRUE	H10
IOT18A	I/O	DQ20	0		True_of_IOT18B	TRUE	A7
IOT18B	I/O	DQ20	0		Comp_of_IOT18A	TRUE	A8
IOT20A	I/O	DQ20	0		True_of_IOT20B	TRUE	D9
IOT20B	I/O	DQ20	0		Comp_of_IOT20A	TRUE	D10
IOT22A	LVDS	DQ20	0		True_of_IOT22B	TRUE	
IOT22B	LVDS	DQ20	0		Comp_of_IOT22A	TRUE	
IOT24A	I/O	DQ20	0		True_of_IOT24B	TRUE	E9
IOT24B	I/O	DQ20	0		Comp_of_IOT24A	TRUE	E10
IOT26A	I/O	DQ20	0		True_of_IOT26B	TRUE	C7
IOT26B	I/O	DQ20	0		Comp_of_IOT26A	TRUE	C8
IOT2A	I/O	DQ21	0		True_of_IOT2B	TRUE	C4
IOT2B	I/O	DQ21	0		Comp_of_IOT2A	TRUE	C5
IOT30A	I/O	DQS19	0		True_of_IOT30B	TRUE	G9
IOT30B	I/O	DQS19	0		Comp_of_IOT30A	TRUE	G10
IOT32A	LVDS	DQ19	0		True_of_IOT32B	TRUE	
IOT32B	LVDS	DQ19	0		Comp_of_IOT32A	TRUE	

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOT34A	LVDS	DQ19	0		True_of_IOT34B	TRUE	
IOT34B	LVDS	DQ19	0		Comp_of_IOT34A	TRUE	
IOT36A	I/O	DQ19	0		True_of_IOT36B	TRUE	C10
IOT36B	I/O	DQ19	0		Comp_of_IOT36A	TRUE	C11
IOT38A	LVDS	DQ19	0		True_of_IOT38B	TRUE	
IOT38B	LVDS	DQ19	0		Comp_of_IOT38A	TRUE	
IOT40A	I/O	DQ19	0		True_of_IOT40B	TRUE	G11
IOT40B	I/O	DQ19	0		Comp_of_IOT40A	TRUE	G12
IOT42A	I/O	DQ19	0		True_of_IOT42B	TRUE	F12
IOT42B	I/O	DQ19	0		Comp_of_IOT42A	TRUE	G13
IOT44A	I/O	DQ19	0		True_of_IOT44B	TRUE	B12
IOT44B	I/O	DQ19	0		Comp_of_IOT44A	TRUE	B13
IOT4A	I/O	DQ21	0		True_of_IOT4B	TRUE	D4
IOT4B	I/O	DQ21	0		Comp_of_IOT4A	TRUE	D5
IOT6A	I/O	DQ21	0		True_of_IOT6B	TRUE	C6
IOT6B	I/O	DQ21	0		Comp_of_IOT6A	TRUE	D6
IOT8A	I/O	DQ21	0		True_of_IOT8B	TRUE	A4
IOT8B	I/O	DQ21	0		Comp_of_IOT8A	TRUE	A5

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
BANK7 True LVDS Pair							
IOL13A	LVDS	DQ1	7		True_of_IOL13B	TRUE	G3
IOL13B	LVDS	DQ1	7		Comp_of_IOL13A	TRUE	F2
IOL17A	LVDS	DQ1	7		True_of_IOL17B	TRUE	
IOL17B	LVDS	DQ1	7		Comp_of_IOL17A	TRUE	
IOL22A	LVDS	DQ1	7		True_of_IOL22B	TRUE	
IOL22B	LVDS	DQ1	7		Comp_of_IOL22A	TRUE	
IOL24A	LVDS	DQ1	7		True_of_IOL24B	TRUE	H6
IOL24B	LVDS	DQ1	7		Comp_of_IOL24A	TRUE	J6
IOL26A	LVDS	DQ1	7		True_of_IOL26B	TRUE	
IOL26B	LVDS	DQ1	7		Comp_of_IOL26A	TRUE	
IOL30A	LVDS	DQ2	7		True_of_IOL30B	TRUE	
IOL30B	LVDS	DQ2	7		Comp_of_IOL30A	TRUE	
IOL39A	LVDS	DQ2	7		True_of_IOL39B	TRUE	
IOL39B	LVDS	DQ2	7		Comp_of_IOL39A	TRUE	
IOL41A	LVDS	DQ2	7		True_of_IOL41B	TRUE	N6
IOL41B	LVDS	DQ2	7		Comp_of_IOL41A	TRUE	P6
IOL4A	LVDS	DQ0	7		True_of_IOL4B	TRUE	
IOL4B	LVDS	DQ0	7		Comp_of_IOL4A	TRUE	
IOL6A	LVDS	DQ0	7		True_of_IOL6B	TRUE	
IOL6B	LVDS	DQ0	7		Comp_of_IOL6A	TRUE	
BANK6 True LVDS Pair							
IOL57A	LVDS	DQ3	6		True_of_IOL57B	TRUE	T3
IOL57B	LVDS	DQ3	6		Comp_of_IOL57A	TRUE	U2
IOL59A	LVDS	DQ3	6		True_of_IOL59B	TRUE	U5
IOL59B	LVDS	DQ3	6		Comp_of_IOL59A	TRUE	V5
IOL61A	LVDS	DQ3	6		True_of_IOL61B	TRUE	
IOL61B	LVDS	DQ3	6		Comp_of_IOL61A	TRUE	
IOL67A	LVDS	DQ4	6		True_of_IOL67B	TRUE	V4
IOL67B	LVDS	DQ4	6		Comp_of_IOL67A	TRUE	W4
IOL69A	LVDS	DQ4	6		True_of_IOL69B	TRUE	

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOL69B	LVDS	DQ4	6		Comp_of_IOL69A	TRUE	
IOL71A	LVDS	DQ4	6		True_of_IOL71B	TRUE	U3
IOL71B	LVDS	DQ4	6		Comp_of_IOL71A	TRUE	W3
IOL78A	LVDS	DQ4	6		True_of_IOL78B	TRUE	
IOL78B	LVDS	DQ4	6		Comp_of_IOL78A	TRUE	
IOL80A	LVDS	DQ4	6		True_of_IOL80B	TRUE	
IOL80B	LVDS	DQ4	6		Comp_of_IOL80A	TRUE	
BANK5 True LVDS Pair							
IOB12A	LVDS	DQ6	5		True_of_IOB12B	TRUE	
IOB12B	LVDS	DQ6	5		Comp_of_IOB12A	TRUE	
IOB14A	LVDS	DQ6	5		True_of_IOB14B	TRUE	
IOB14B	LVDS	DQ6	5		Comp_of_IOB14A	TRUE	
IOB22A	LVDS	DQ6	5		True_of_IOB22B	TRUE	
IOB22B	LVDS	DQ6	5		Comp_of_IOB22A	TRUE	
IOB32A	LVDS	DQ7	5		True_of_IOB32B	TRUE	
IOB32B	LVDS	DQ7	5		Comp_of_IOB32A	TRUE	
IOB34A	LVDS	DQ7	5		True_of_IOB34B	TRUE	
IOB34B	LVDS	DQ7	5		Comp_of_IOB34A	TRUE	
IOB38A	LVDS	DQ7	5		True_of_IOB38B	TRUE	
IOB38B	LVDS	DQ7	5		Comp_of_IOB38A	TRUE	
BANK4 True LVDS Pair							
IOB54A	LVDS	DQ8	4		True_of_IOB54B	TRUE	
IOB54B	LVDS	DQ8	4		Comp_of_IOB54A	TRUE	
IOB58A	LVDS	DQ8	4		True_of_IOB58B	TRUE	
IOB58B	LVDS	DQ8	4		Comp_of_IOB58A	TRUE	
IOB78A	LVDS	DQ9	4		True_of_IOB78B	TRUE	
IOB78B	LVDS	DQ9	4		Comp_of_IOB78A	TRUE	
IOB80A	LVDS	DQ9	4		True_of_IOB80B	TRUE	
IOB80B	LVDS	DQ9	4		Comp_of_IOB80A	TRUE	
BANK3 True LVDS Pair							
IOR57A	LVDS	DQ12	3		True_of_IOR57B	TRUE	U24

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOR57B	LVDS	DQ12	3		Comp_of_IOR57A	TRUE	V24
IOR59A	LVDS	DQ12	3		True_of_IOR59B	TRUE	U23
IOR59B	LVDS	DQ12	3		Comp_of_IOR59A	TRUE	V23
IOR61A	LVDS	DQ12	3		True_of_IOR61B	TRUE	
IOR61B	LVDS	DQ12	3		Comp_of_IOR61A	TRUE	
IOR67A	LVDS	DQ11	3		True_of_IOR67B	TRUE	U20
IOR67B	LVDS	DQ11	3		Comp_of_IOR67A	TRUE	V20
IOR69A	LVDS	DQ11	3		True_of_IOR69B	TRUE	V19
IOR69B	LVDS	DQ11	3		Comp_of_IOR69A	TRUE	W21
IOR78A	LVDS	DQ11	3		True_of_IOR78B	TRUE	
IOR78B	LVDS	DQ11	3		Comp_of_IOR78A	TRUE	
IOR80A	LVDS	DQ11	3		True_of_IOR80B	TRUE	
IOR80B	LVDS	DQ11	3		Comp_of_IOR80A	TRUE	
BANK2 True LVDS Pair							
IOR13A	LVDS	DQ14	2		True_of_IOR13B	TRUE	G25
IOR13B	LVDS	DQ14	2		Comp_of_IOR13A	TRUE	H25
IOR17A	LVDS	DQ14	2		True_of_IOR17B	TRUE	
IOR17B	LVDS	DQ14	2		Comp_of_IOR17A	TRUE	
IOR22A	LVDS	DQ14	2		True_of_IOR22B	TRUE	K23
IOR22B	LVDS	DQ14	2		Comp_of_IOR22A	TRUE	L23
IOR24A	LVDS	DQ14	2		True_of_IOR24B	TRUE	
IOR24B	LVDS	DQ14	2		Comp_of_IOR24A	TRUE	
IOR26A	LVDS	DQ14	2		True_of_IOR26B	TRUE	
IOR26B	LVDS	DQ14	2		Comp_of_IOR26A	TRUE	
IOR30A	LVDS	DQ13	2		True_of_IOR30B	TRUE	
IOR30B	LVDS	DQ13	2		Comp_of_IOR30A	TRUE	
IOR39A	LVDS	DQ13	2		True_of_IOR39B	TRUE	
IOR39B	LVDS	DQ13	2		Comp_of_IOR39A	TRUE	
IOR41A	LVDS	DQ13	2		True_of_IOR41B	TRUE	
IOR41B	LVDS	DQ13	2		Comp_of_IOR41A	TRUE	
IOR4A	LVDS	DQ15	2		True_of_IOR4B	TRUE	E24

Pin Name	Function	DQS	BANK	Configuration Function	Differential Pair	LVDS	UG676
IOR4B	LVDS	DQ15	2		Comp_of_IOR4A	TRUE	F25
IOR6A	LVDS	DQ15	2		True_of_IOR6B	TRUE	
IOR6B	LVDS	DQ15	2		Comp_of_IOR6A	TRUE	
BANK1 True LVDS Pair							
IOT54A	LVDS	DQ18	1		True_of_IOT54B	TRUE	
IOT54B	LVDS	DQ18	1		Comp_of_IOT54A	TRUE	
IOT60A	LVDS	DQ18	1		True_of_IOT60B	TRUE	
IOT60B	LVDS	DQ18	1		Comp_of_IOT60A	TRUE	
IOT78A	LVDS	DQ17	1		True_of_IOT78B	TRUE	
IOT78B	LVDS	DQ17	1		Comp_of_IOT78A	TRUE	
IOT80A	LVDS	DQ17	1		True_of_IOT80B	TRUE	
IOT80B	LVDS	DQ17	1		Comp_of_IOT80A	TRUE	
BANK0 True LVDS Pair							
IOT12A	LVDS	DQ20	0		True_of_IOT12B	TRUE	
IOT12B	LVDS	DQ20	0		Comp_of_IOT12A	TRUE	
IOT14A	LVDS	DQ20	0		True_of_IOT14B	TRUE	
IOT14B	LVDS	DQ20	0		Comp_of_IOT14A	TRUE	
IOT22A	LVDS	DQ20	0		True_of_IOT22B	TRUE	
IOT22B	LVDS	DQ20	0		Comp_of_IOT22A	TRUE	
IOT32A	LVDS	DQ19	0		True_of_IOT32B	TRUE	
IOT32B	LVDS	DQ19	0		Comp_of_IOT32A	TRUE	
IOT34A	LVDS	DQ19	0		True_of_IOT34B	TRUE	
IOT34B	LVDS	DQ19	0		Comp_of_IOT34A	TRUE	
IOT38A	LVDS	DQ19	0		True_of_IOT38B	TRUE	
IOT38B	LVDS	DQ19	0		Comp_of_IOT38A	TRUE	

Function	IO		LVDS
	None	TRUE	TRUE
Single-ended IO Buffer			
IBUF	Yes	Yes	Yes
IBUF_R	Yes	Yes	Yes
IOBUF	Yes	Yes	No
IOBUF_R	Yes	Yes	No
I3C_IOBUF	Yes	Yes	No
OBUF	Yes	Yes	No
TBUF	Yes	Yes	No
Function	IO		LVDS
	None	TRUE	TRUE
Differential IO Buffer			
TLVDS_IBUF	Yes	Yes	Yes
TLVDS_IBUF_ADC	Yes	Yes	Yes
TLVDS_IOBUF	Yes	Yes	No
TLVDS_OBUF	No	Yes	Yes
TLVDS_TBUF	No	Yes	Yes
ELVDS_IBUF	Yes	Yes	Yes
ELVDS_IBUF_MIPI	Yes	Yes	Yes
ELVDS_IBUF_R	Yes	Yes	Yes
ELVDS_IOBUF	Yes	Yes	No
ELVDS_IOBUF_R	Yes	Yes	No
ELVDS_OBUF	Yes	Yes	No
ELVDS_TBUF	Yes	Yes	No
MIPI_IBUF	No	Yes	No
MIPI_OBUF_A	No	Yes	No

Note!

[1] It is recommended to set the VCCIO of the Bank using TrueLVDS to 2.5V.

[2] VCCX should be greater than or equal to VCCIO.

Recommended Operating Conditions for GW2AN-55 UG676

Name	Description	Min.	Max.
VCC	Core Power	0.95V	1.05V
VCCIO0, VCCIO1, VCCIO2, VCCIO3, VCCIO4, VCCIO5, VCCIO6, VCCIO7	I/O Bank Voltage VCCIO3 is connected to Flash and provides voltage to Flash.	1.14V 2.7V	3.6V 3.6V
VCCX	Auxiliary voltage	2.7V	3.6V