

Date	Version	Description
04/20/2023	1.0E	Initial version published. FPG676A and PG484A packages supported.
05/25/2023	1.1E	PG484 package added. The descriptions of MODE0/MODE1/MODE2 in Pin Definitions sheet updated.
06/30/2023	1.2E	PG676A package added.
09/08/2023	1.3E	UG324 package added.
10/24/2023	1.3.1E	The Max. voltage value and the note of VCC_REG in Power sheet updated.
11/30/2023	1.4E	UG324A package added. The configuration info. of MCKTEST pin removed.
02/01/2024	1.4.1E	The info. of X16 removed. The note of SGCLKC_[x]/MGCLKC_[x]/SGCLKT_[x]/MGCLKT_[x] pins in Pin Definitions sheet added.
04/18/2024	1.4.2E	UG324 package removed. The Max.value of VCC voltage in Power sheet updated. The description of CFGBVS pin in Pin Definitions sheet optimized.
05/22/2024	1.4.3E	The Max. value of MIPI voltage for PG484 and UG324A packages in Power sheet updated.
07/05/2024	1.4.4E	Power sheet optimized. The names and descriptions of voltage pins updated.
08/09/2024	1.5E	PG484F package added.
10/25/2024	1.5.1E	The VCCIO5/VCCIO6/VCCIO7 pins of UG324A package updated to be short-circuited.
03/07/2025	1.5.2E	The names of power supply pins updated. The information of VEFUSE voltage removed. The definition of ADCINCK pin in Pin Definitions sheet added. The configuration function of ADCTN/ADCTP/ADCVN/ADCVP pins removed.
03/14/2025	1.5.3E	The MIPI pins of PG676A package updated. The MIPI pins and ADC pins of FPG676A package updated. The definitions of DQS/DQ pins in Pin Definitions sheet added.
06/20/2025	1.5.4E	The description of VCCLDO voltage in Power sheet updated.

Pin Name	I/O	Description
User I/O		
IO[End][Row/Column Number][A/B]	I/O/LVDS	[End] indicates the pin location, including L(left), R(right), B(bottom), and T(top).
		[Row/Column Number] indicates the pin row/column number. If [End] is T(top) or B(bottom), the pin indicates the column number of the corresponding CFU. If [End] is L(left) or R(right), the pin indicates the row number of the corresponding CFU.
		[A/B] indicates differential signal pair information.
		LVDS indicates that the pin only supports LVDS output.
Multi-Function Pins		
IO[End][Row/Column Number][A/B]/MMM		/MMM represents one or more of the other functions in addition to being general purpose user I/O. When these functions are not in use, these pins can be used as user I/O.
D00	I/O	CPU Mode: Data input/output (bidirectional) pin D00
D01	I/O	CPU Mode: Data input/output (bidirectional) pin D01 MSPI Mode: Serial data input in X1 mode; In X2 and X4 modes, the input pin of parallel data bit 1 that connects to pin DQ1/Q/SO/IO1 of external Flash device
D02	I/O	CPU Mode: Data input/output (bidirectional) pin D02 MSPI Mode: In X4 mode, the input pin of parallel data bit 2 that connects to pin DQ2/W#/WP#/IO2 of external Flash device respectively
D03	I/O	CPU Mode: Data input/output (bidirectional) pin D03 MSPI Mode: In X4 mode, the input pin of parallel data bit 3 that connects to pin DQ3/HOLD#/IO3 of external Flash device respectively
D04~D07	I/O	CPU Mode: Data input/output port D04~D07
D08~D31	I	CPU Mode: Data input port D08~D31
ADCINCK0	I/O	ADC0 dedicated clock input pin
ADCINCK1	I/O	ADC1 dedicated clock input pin
CCLK	I/O	Configuration Clock Slave Mode: CCLK is input and need connect to external clock source Master Mode: CCLK is output
CFGBVS	I/O	Configuration Bank Voltage Selection Signal The bank where the configuration IO (JTAG, MSPI, etc.) is located refers to bank3, bank4, and bank10 When the VCCIO of the bank where the configuration IO is located is 2.5V and 3.3V, CFGBVS is connected to 1 When the VCCIO of the bank where the configuration IO is located is less than 1.8V, CFGBVS is connected to 0
CFGPU	I/O	Weak pull-up selection signal pin during configuration

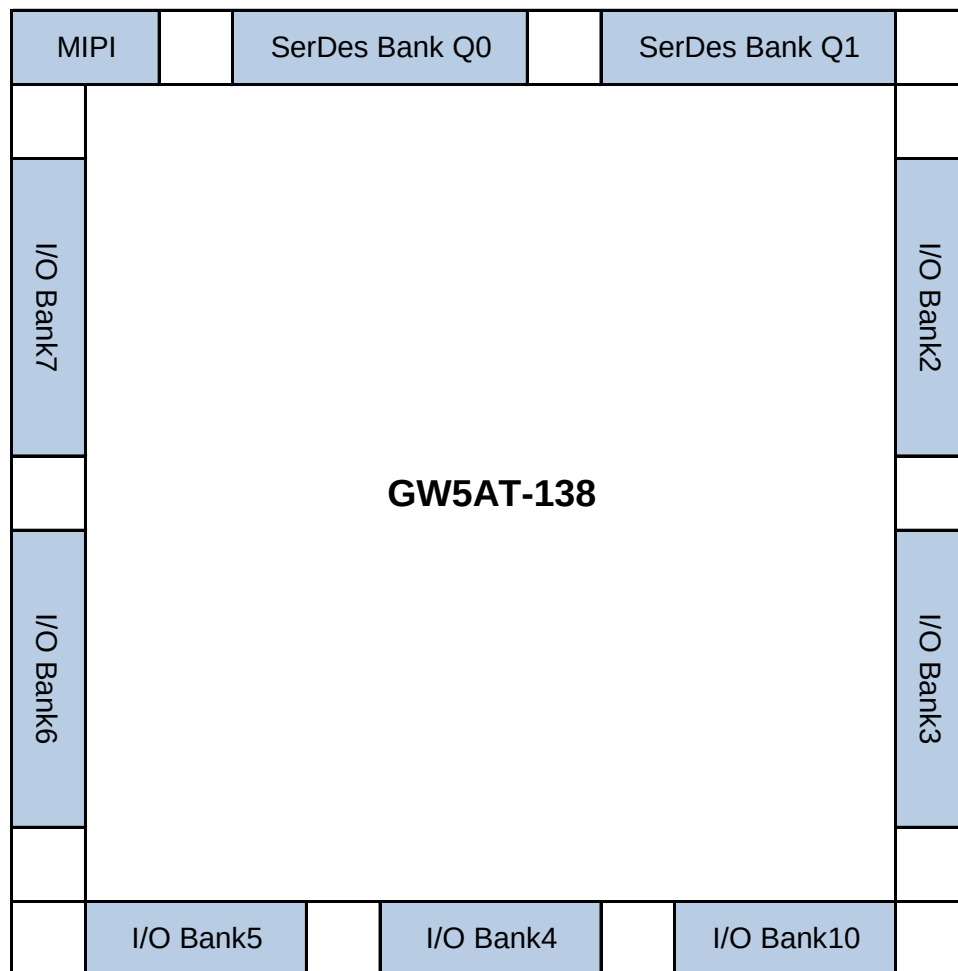
Pin Name	I/O	Description
EMCCLK	I	External Input Clock Signal Master Mode: EMCCLK is used as the clock source of FPGA configuration logic and output CCLK Slave Mode: EMCCLK is not associated with slave mode
MOSI	I/O	MSPI Mode: Serial instruction and address output, in X2 and X4 modes, the input pin of parallel data bit 0 that connects to pin DQ0/D/SI/IO0 of external Flash device
CSI_B	I	CPU Mode: Chip select signal, active-low Master CPU Mode: The chip select signal can connect to external configuration controller and can be grounded directly or grounded in series with a 1KΩ resistor Slave CPU Mode: External configuration controller can select FPGA through controlling CSI_B signal In Master and Slave modes, CSI_B signal is sent from external controller; In other modes, CSI_B signal is not associated with external controller
DOUT_CSO_B	O	Connect to next-level device in FPGA cascade configuration mode (Daisy Chain) Serial Mode: Output the configuration data of next-level device Master SPI Mode: Output the configuration data of next-level device CPU Mode: Output the chip select signal of next-level device
PUDC_B	I	Active-low, weak pull-up selection signal pin during configuration: Enable internal weak pull-up resistor during configuration after FPGA power up PUDC_B low: all GPIOs except PUDC_B weak pull-up PUDC_B high: all GPIOs are in high impedance PUDC_B are not allowed to be left floating during configuration
RDWR	I	CPU Mode: Data write/read controlling signal When RDWR is high, FPGA outputs data, while RDWR is low, external controller writes data into FPGA Master CPU Mode: Connect external controller RDWR signal; Connect directly or in series with resistor $\leq 1K\Omega$ to GND Slave CPU Mode: External controller RDWR signal In CPU mode, low 8-bit dedicated IO will be affected by RDWR status after wakeup; low 8-bit dedicated IO will not be affected by RDWR status if it is set as multiplexed IO
SI	I/O	SI in SSPI mode
SO	O	SO in SSPI mode
SSPI_CLK	I/O	SSPI/QSSPI Configuration Mode: Clock input pin
SSPI_WPN	I/O	QSSPI Configuration Mode: Data input pin
SGCLKC_[x] ^[2]	I	Differential input pin of SGCLKT_[x], C(Comp), [x] is clock number
SGCLKT_[x] ^[2]	I	Dedicated clock input pin driving the same clock domain ,T(True), [x] is clock number

Pin Name	I/O	Description
MGCLKC [x] ^[2]	I	Differential input pin of MGCLKT_[x], C(Comp), [x] is clock number
MGCLKT [x] ^[2]	I	Dedicated clock input pin driving multiple clock domains, T(True), [x] is clock number
VREF		Reference Voltage
DIN	I, internal weak pull-down	SERIAL Mode: Data input
TMS	I, internal weak pull-up	JTAG Mode: Serial mode input
TCK	I	JTAG Mode: Serial clock input
TDO	O	JTAG Mode: Serial data output
TDI	I, internal weak pull-up	JTAG mode: Serial data input
RECONFIG_N	I, internal weak pull-up	Global reset GowinCONFIG logic signal, active low
DONE ^[1]	O, internal weak pull-up	High, the programming configuration has been completed successfully; Low, the programming configuration has not been completed or failed.
	I, internal weak pull-up	When the DONE signal is low, delay the chip to activate. Activate the chip until the DONE signal is high.
READY ^[1]	O, internal weak pull-up	High, the device can be programmed and configured currently; Low, the device cannot be programmed and configured currently.
MCS_N	O	MSPI Mode: Enable signal MCS_N, active-low
SSPI_CS_N	I/O	SSPI Mode: Enable signal SSPI_CS_N, active-low, and internal weak pull-up
CLKHOLD_N	I, internal weak pull-down	In SSPI mode, active high In CPU mode, active low
LPLL_C_FB/RPLL_C_FB	I	Left/Right PLL feedback input pin, C(Comp)
LPLL_T_FB/RPLL_T_FB	I	Left/Right PLL feedback input pin, T(True)
LPLL_C_IN/RPLL_C_IN	I	Left/Right PLL clock input pin, C(Comp)
LPLL_T_IN/RPLL_T_IN	I	Left/Right PLL clock input pin, T(True)
MODE2	I, internal weak pull-down	GowinCONFIG mode selection pin: if this pin is marked as "VCCIO", it's internally powered; if this pin is marked as "GND", it's internally grounded.
MODE1	I, internal weak pull-down	GowinCONFIG mode selection pin: if this pin is marked as "VCCIO", it's internally powered; if this pin is marked as "GND", it's internally grounded.
MODE0	I, internal weak pull-down	GowinCONFIG mode selection pin: if this pin is marked as "VCCIO", it's internally powered; if this pin is marked as "GND", it's internally grounded.

Pin Name	I/O	Description
Other Pins		
ADCINCK	I	ADC clock input pin
DQ*	I	DQ signal pin in DQS* group by default; it can also be configured as a DQS data signal pin in DQS* group via Gowin Software.
DQS*	I	DQS signal pin in DQS* group by default; it can also be configured as a DQ data signal pin in DQS* group via Gowin Software.
DQ1/DQS_01	I	DQ data signal pin in the DQS1 group by default; it can also be configured as a DQS signal pin in DQS0 group or as a DQS signal pin in the DQS1 group via Gowin Software.
DQ2/DQS_23	I	DQ data signal pin in the DQS2 group by default; it can also be configured as a DQS signal pin in DQS2 group or as a DQS signal pin in the DQS3 group via Gowin Software.
DQ5/DQS_45	I	DQ data signal pin in the DQS5 group by default; it can also be configured as a DQS signal pin in DQS4 group or as a DQS signal pin in the DQS5 group via Gowin Software.
DQ6/DQS_67	I	DQ data signal pin in the DQS6 group by default; it can also be configured as a DQS signal pin in DQS6 group or as a DQS signal pin in the DQS7 group via Gowin Software.
DQ10/DQS_910	I	DQ data signal pin in the DQS10 group by default; it can also be configured as a DQS signal pin in DQS9 group or as a DQS signal pin in the DQS10 group via Gowin Software.
ADCTN	DIO	The differential analog signal input pin of SENSOR1/SENSOR2
ADCTP	DIO	The differential analog signal input pin of SENSOR1/SENSOR2
ADCVN	DIO	The differential analog signal input pin of SENSOR1/SENSOR2
ADCVP	DIO	The differential analog signal input pin of SENSOR1/SENSOR2
ATEST_O	DIO	The analog signal test pin of SerDes
M0_CKN	DIO	The clock channel differential input pin of MIPI_DPHY_RX M0
M0_CKP	DIO	The clock channel differential input pin of MIPI_DPHY_RX M0
M0_D0N	DIO	The data channel 0 differential input pin of MIPI_DPHY_RX M0
M0_D0P	DIO	The data channel 0 differential input pin of MIPI_DPHY_RX M0
M0_D1N	DIO	The data channel 1 differential input pin of MIPI_DPHY_RX M0
M0_D1P	DIO	The data channel 1 differential input pin of MIPI_DPHY_RX M0
M0_D2N	DIO	The data channel 2 differential input pin of MIPI_DPHY_RX M0
M0_D2P	DIO	The data channel 2 differential input pin of MIPI_DPHY_RX M0
M0_D3N	DIO	The data channel 3 differential input pin of MIPI_DPHY_RX M0
M0_D3P	DIO	The data channel 3 differential input pin of MIPI_DPHY_RX M0
M1_CKN	DIO	The data channel differential input pin of MIPI_DPHY_RX M1
M1_CKP	DIO	The data channel differential input pin of MIPI_DPHY_RX M1

Pin Name	I/O	Description
M1_D0N	DIO	The data channel 0 differential input pin of MIPI_DPHY_RX M1
M1_D0P	DIO	The data channel 0 differential input pin of MIPI_DPHY_RX M1
M1_D1N	DIO	The data channel 1 differential input pin of MIPI_DPHY_RX M1
M1_D1P	DIO	The data channel 1 differential input pin of MIPI_DPHY_RX M1
M1_D2N	DIO	The data channel 2 differential input pin of MIPI_DPHY_RX M1
M1_D2P	DIO	The data channel 2 differential input pin of MIPI_DPHY_RX M1
M1_D3N	DIO	The data channel 3 differential input pin of MIPI_DPHY_RX M1
M1_D3P	DIO	The data channel 3 differential input pin of MIPI_DPHY_RX M1
Q0_LN0_RXM_I	DIO	The data channel 0 RX differential input pin of SerDes Q0
Q0_LN0_RXP_I	DIO	The data channel 0 RX differential input pin of SerDes Q0
Q0_LN0_TXM_O	DIO	The data channel 0 TX differential output pin of SerDes Q0
Q0_LN0_TXP_O	DIO	The data channel 0 TX differential output pin of SerDes Q0
Q0_LN1_RXM_I	DIO	The data channel 1 RX differential input pin of SerDes Q0
Q0_LN1_RXP_I	DIO	The data channel 1 RX differential input pin of SerDes Q0
Q0_LN1_TXM_O	DIO	The data channel 1 TX differential output pin of SerDes Q0
Q0_LN1_TXP_O	DIO	The data channel 1 TX differential output pin of SerDes Q0
Q0_LN2_RXM_I	DIO	The data channel 2 RX differential input pin of SerDes Q0
Q0_LN2_RXP_I	DIO	The data channel 2 RX differential input pin of SerDes Q0
Q0_LN2_TXM_O	DIO	The data channel 2 TX differential output pin of SerDes Q0
Q0_LN2_TXP_O	DIO	The data channel 2 TX differential output pin of SerDes Q0
Q0_LN3_RXM_I	DIO	The data channel 3 RX differential input pin of SerDes Q0
Q0_LN3_RXP_I	DIO	The data channel 3 RX differential input pin of SerDes Q0
Q0_LN3_TXM_O	DIO	The data channel 3 TX differential output pin of SerDes Q0
Q0_LN3_TXP_O	DIO	The data channel 3 TX differential output pin of SerDes Q0
Q1_LN0_RXM_I	DIO	The data channel 0 RX differential input pin of SerDes Q1
Q1_LN0_RXP_I	DIO	The data channel 0 RX differential input pin of SerDes Q1
Q1_LN0_TXM_O	DIO	The data channel 0 TX differential output pin of SerDes Q1
Q1_LN0_TXP_O	DIO	The data channel 0 TX differential output pin of SerDes Q1
Q1_LN1_RXM_I	DIO	The data channel 1 RX differential input pin of SerDes Q1
Q1_LN1_RXP_I	DIO	The data channel 1 RX differential input pin of SerDes Q1
Q1_LN1_TXM_O	DIO	The data channel 1 TX differential output pin of SerDes Q1
Q1_LN1_TXP_O	DIO	The data channel 1 TX differential output pin of SerDes Q1

Pin Name	I/O	Description
Q1_LN2_RXM_I	DIO	The data channel 2 RX differential input pin of SerDes Q1
Q1_LN2_RXP_I	DIO	The data channel 2 RX differential input pin of SerDes Q1
Q1_LN2_TXM_O	DIO	The data channel 2 TX differential output pin of SerDes Q1
Q1_LN2_TXP_O	DIO	The data channel 2 TX differential output pin of SerDes Q1
Q1_LN3_RXM_I	DIO	The data channel 3 RX differential input pin of SerDes Q1
Q1_LN3_RXP_I	DIO	The data channel 3 RX differential input pin of SerDes Q1
Q1_LN3_TXM_O	DIO	The data channel 3 TX differential output pin of SerDes Q1
Q1_LN3_TXP_O	DIO	The data channel 3 TX differential output pin of SerDes Q1
Q0_REFCLKM_0	DIO	The reference clock channel 0 differential input pin of SerDes Q0
Q0_REFCLKP_0	DIO	The reference clock channel 0 differential input pin of SerDes Q0
Q0_REFCLKM_1	DIO	The reference clock channel 1 differential input pin of SerDes Q0
Q0_REFCLKP_1	DIO	The reference clock channel 1 differential input pin of SerDes Q0
Q1_REFCLKM_0	DIO	The reference clock channel 0 differential input pin of SerDes Q1
Q1_REFCLKM_1	DIO	The reference clock channel 0 differential input pin of SerDes Q1
Q1_REFCLKP_0	DIO	The reference clock channel 1 differential input pin of SerDes Q1
Q1_REFCLKP_1	DIO	The reference clock channel 1 differential input pin of SerDes Q1
VSS	NA	Ground pin
VCC	NA	Core power supply pin
VCCIO#	NA	IO power supply pin of I/O BANK#
VCCX	NA	Auxiliary power supply pin
VCCLDO	NA	Power supply pin of internal LDO module that provides voltage for PLL and SRAM
VDDAQ*	NA	Power supply pin of internal analog circuit of QUAD* for SerDes module
VDDDQ*	NA	Power supply pin of internal digital circuit of QUAD* for SerDes module
VDDTQ*	NA	Power supply pin of QUAD* TX transmitter for SerDes module
VDDHAQ*	NA	Internal high-voltage power supply pin of QUAD* for SerDes module
VDDAM	NA	Power supply pin of internal analog circuit for MIPI module
VDDDM	NA	Power supply pin of internal digital circuit for MIPI module
VDDXM	NA	Auxiliary power supply pin of MIPI module
VREF*	NA	Reference power supply pin
Note! ^[1] The default state of READY/DONE is open-drain output, internal weak pull-up. DONE outputs 0 during configuration. ^[2] The SGCLKC_[x] pin is equivalent to the MGCLKC_[x] pin; the SGCLKT_[x] pin is equivalent to the MGCLKT_[x] pin.		



Note!

- [1] Each Bank has independent reference voltage (VREF).
- [2] You can choose to use the embedded VREF source of IOB (0.6V, 0.675V, 0.75V, 0.9V, and VCCIO-based scaled voltages (33%,42%,50%,58%)).
- [3] You can also select to use external VREF input (use any IO pins as external VREF input).

Name	Description	Min.	Max.
FPGA Logic			
V_{CC}	Core voltage	0.87V	1.03V
V_{CCIO}	I/O Bank voltage	1V	3.465V
V_{CCX}	Auxiliary voltage	1.71V	1.89V
$V_{CCLDO}^{[1]}$	Power supply voltage for internal LDO module that powers SRAM	1.14V	1.89V
Gigabit Transceiver			
V_{DDHAQ}^*	Analog high power supply voltage	1.71V	1.89V
V_{DDAQ}^*	Analog core power supply voltage	0.87V	1.03V
V_{DDTQ}^*	TX power supply voltage	0.87V	1.03V
V_{DDDQ}^*	Digital core power supply voltage	0.87V	1.03V
MIPI			
V_{DDAM}	Analog core power supply voltage	0.87V	1.03V
V_{DDXM}	Analog auxiliary voltage power supply voltage	1.71V	1.89V
V_{DDDM}	Digital core power supply voltage	0.87V	1.03V
Note! ^[1] The greater the V_{CCLDO} voltage, the higher the power consumption. If multiple power supplies are shorted on some packages or PCBs, it is necessary to take the intersection of the ranges of all shorted power supplies to satisfy the needs of multiple power supplies at the same time.			

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOB102A/MCS_N	I/O	4	DQ12	MCS_N	True_of_IOB102B	True	T19
IOB102B/D08/SO	I/O	4	DQ12	D08/SO	Comp_of_IOB102A	True	T20
IOB104A/PUDC_B	I/O	4	DQS12	PUDC_B	True_of_IOB104B	True	U22
IOB104B/EMCCLK	I/O	4	DQS12	EMCCLK	Comp_of_IOB104A	True	V22
IOB106A/CSI_B	I/O	4	DQ13	CSI_B	True_of_IOB106B	True	V17
IOB106B/D31	I/O	4	DQ13	D31	Comp_of_IOB106A	True	W17
IOB108A/D30	I/O	4	DQ13	D30	True_of_IOB108B	True	AA18
IOB108B/D29	I/O	4	DQ13	D29	Comp_of_IOB108A	True	AB18
IOB110A/RDWR	I/O	4	DQS13	RDWR	True_of_IOB110B	True	AA19
IOB110B/DOUT_CSO_B	I/O	4	DQS13	DOUT_CSO_B	Comp_of_IOB110A	True	AB20
IOB112A/D28	I/O	4	DQ13	D28	True_of_IOB112B	True	U17
IOB112B/D27	I/O	4	DQ13	D27	Comp_of_IOB112A	True	U18
IOB114A/SGCLKT_4/BPLL2_T_FB1/ BPLL3_T_FB1	I/O	4	DQ13	SGCLKT_4/BPLL2_T_FB1/BPL L3_T_FB1	True_of_IOB114B	True	V18
IOB114B/SGCLKC_4/BPLL2_C_FB1/ BPLL3_C_FB1	I/O	4	DQ13	SGCLKC_4/BPLL2_C_FB1/BPL L3_C_FB1	Comp_of_IOB114A	True	V19
IOB116A/MGCLKT_4/BPLL2_T_FB0/ BPLL3_T_FB0	I/O	4	DQ13	MGCLKT_4/BPLL2_T_FB0/BPL L3_T_FB0	True_of_IOB116B	True	Y18
IOB116B/MGCLKC_4/BPLL2_C_FB 0/BPLL3_C_FB0	I/O	4	DQ13	MGCLKC_4/BPLL2_C_FB0/BPL L3_C_FB0	Comp_of_IOB116A	True	Y19
IOB120A/SGCLKT_5/BPLL2_T_IN0/ BPLL3_T_IN0	I/O	4	DQ14	SGCLKT_5/BPLL2_T_IN0/BPLL 3_T_IN0	True_of_IOB120B	True	U20
IOB120B/SGCLKC_5/BPLL2_C_IN0/ BPLL3_C_IN0	I/O	4	DQ14	SGCLKC_5/BPLL2_C_IN0/BPLL 3_C_IN0	Comp_of_IOB120A	True	V20
IOB122A/MGCLKT_5/BPLL2_T_IN1/ BPLL3_T_IN1	I/O	4	DQ14	MGCLKT_5/BPLL2_T_IN1/BPLL 3_T_IN1	True_of_IOB122B	True	W19
IOB122B/MGCLKC_5/BPLL2_C_IN1/ BPLL3_C_IN1	I/O	4	DQ14	MGCLKC_5/BPLL2_C_IN1/BPL L3_C_IN1	Comp_of_IOB122A	True	W20
IOB124A/D09	I/O	4	DQ14	D09	True_of_IOB124B	True	W21
IOB124B/D10	I/O	4	DQ14	D10	Comp_of_IOB124A	True	W22

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOB126A/D11	I/O	4	DQ14	D11	True_of_IOB126B	True	AA20
IOB126B/D12	I/O	4	DQ14	D12	Comp_of_IOB126A	True	AA21
IOB129A/D14	I/O	4	DQ14	D14	True_of_IOB129B	True	AB21
IOB129B/D15	I/O	4	DQ14	D15	Comp_of_IOB129A	True	AB22
IOB131A/SSPI_CS_N	I/O	4	DQS14	SSPI_CS_N	True_of_IOB131B	True	Y21
IOB131B/D13	I/O	4	DQS14	D13	Comp_of_IOB131A	True	Y22
IOB133A/D26	I/O	4	DQ15	D26	True_of_IOB133B	True	P14
IOB133B/D25	I/O	4	DQ15	D25	Comp_of_IOB133A	True	R14
IOB135A/CLKHOLD_N	I/O	4	DQS15	CLKHOLD_N	True_of_IOB135B	True	N17
IOB135B/D22	I/O	4	DQS15	D22	Comp_of_IOB135A	True	P17
IOB138A/D24	I/O	4	DQ15	D24	True_of_IOB138B	True	R18
IOB138B/D23	I/O	4	DQ15	D23	Comp_of_IOB138A	True	T18
IOB140A/D21	I/O	4	DQ15	D21	True_of_IOB140B	True	P15
IOB140B/D20	I/O	4	DQ15	D20	Comp_of_IOB140A	True	R16
IOB142A/D19	I/O	4	DQ15	D19	True_of_IOB142B	True	N13
IOB142B/D18	I/O	4	DQ15	D18	Comp_of_IOB142A	True	N14
IOB144A/D17	I/O	4	DQ15	D17	True_of_IOB144B	True	P16
IOB144B/D16	I/O	4	DQ15	D16	Comp_of_IOB144A	True	R17
IOB146A	I/O	4	none			none	N15
IOB169A/TDO	I/O	10	none	TDO	True_of_IOB169B	True	U13
IOB169B/TMS	I/O	10	none	TMS	Comp_of_IOB169A	True	T13
IOB171A/READY	I/O	10	none	READY	True_of_IOB171B	True	U12
IOB171B/DONE	I/O	10	none	DONE	Comp_of_IOB171A	True	G11
IOB173A/TCK	I/O	10	none	TCK	True_of_IOB173B	True	V12
IOB173B/TDI	I/O	10	none	TDI	Comp_of_IOB173A	True	R13
IOB175A/MODE0	I/O	10	none	MODE0	True_of_IOB175B	True	U11
IOB175B/CCLK	I/O	10	none	CCLK	Comp_of_IOB175A	True	L12
IOB177A/MODE1	I/O	10	none	MODE1	True_of_IOB177B	True	U10
IOB177B/MODE2	I/O	10	none	MODE2	Comp_of_IOB177A	True	U9
IOB179A/CFGBVS	I/O	10	none	CFGBVS	True_of_IOB179B	True	U8
IOB179B/RECONFIG_N	I/O	10	none	RECONFIG_N	Comp_of_IOB179A	True	N12

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOB51A	I/O	5	DQ9		True_of_IOB51B	True	AB11
IOB51B	I/O	5	DQ9		Comp_of_IOB51A	True	AB12
IOB53A	I/O	5	DQ9		True_of_IOB53B	True	AA9
IOB53B	I/O	5	DQ9		Comp_of_IOB53A	True	AB10
IOB56A	I/O	5	DQS9		True_of_IOB56B	True	AA10
IOB56B	I/O	5	DQS9		Comp_of_IOB56A	True	AA11
IOB58A	I/O	5	DQ9		True_of_IOB58B	True	V10
IOB58B	I/O	5	DQ9		Comp_of_IOB58A	True	W10
IOB60A/SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	I/O	5	DQ9	SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	True_of_IOB60B	True	Y11
IOB60B/SGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	I/O	5	DQ9	SGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	Comp_of_IOB60A	True	Y12
IOB62A/MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	I/O	5	DQ9	MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	True_of_IOB62B	True	W11
IOB62B/MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	I/O	5	DQ9	MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	Comp_of_IOB62A	True	W12
IOB66A/MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	I/O	5	DQ10	MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	True_of_IOB66B	True	V13
IOB66B/MGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	I/O	5	DQ10	MGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	Comp_of_IOB66A	True	V14
IOB68A/SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	I/O	5	DQ10	SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	True_of_IOB68B	True	U15
IOB68B/SGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	I/O	5	DQ10	SGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	Comp_of_IOB68A	True	V15
IOB70A	I/O	5	DQS10		True_of_IOB70B	True	T14
IOB70B	I/O	5	DQS10		Comp_of_IOB70A	True	T15
IOB72A	I/O	5	DQ10		True_of_IOB72B	True	W15
IOB72B	I/O	5	DQ10		Comp_of_IOB72A	True	W16
IOB76A	I/O	5	DQ10		True_of_IOB76B	True	T16
IOB76B	I/O	5	DQ10		Comp_of_IOB76A	True	U16
IOB78A	I/O	5	DQ11		True_of_IOB78B	True	Y16

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOB78B	I/O	5	DQ11		Comp_of_IOB78A	True	AA16
IOB80A	I/O	5	DQ11		True_of_IOB80B	True	AB16
IOB80B	I/O	5	DQ11		Comp_of_IOB80A	True	AB17
IOB83A	I/O	5	DQ11		True_of_IOB83B	True	AA15
IOB83B	I/O	5	DQ11		Comp_of_IOB83A	True	AB15
IOB85A	I/O	5	DQ11		True_of_IOB85B	True	W14
IOB85B	I/O	5	DQ11		Comp_of_IOB85A	True	Y14
IOB87A	I/O	5	DQ11		True_of_IOB87B	True	Y13
IOB87B	I/O	5	DQ11		Comp_of_IOB87A	True	AA14
IOB89A	I/O	5	DQS11		True_of_IOB89B	True	AA13
IOB89B	I/O	5	DQS11		Comp_of_IOB89A	True	AB13
IOB91A	I/O	5	none			none	Y17
IOB92A	I/O	4	none			none	P20
IOB93A/D00/MOSI	I/O	4	DQ12	D00/MOSI	True_of_IOB93B	True	P22
IOB93B/D01/DIN	I/O	4	DQ12	D01/DIN	Comp_of_IOB93A	True	R22
IOB95A/D02	I/O	4	DQ12	D02	True_of_IOB95B	True	P21
IOB95B/D03	I/O	4	DQ12	D03	Comp_of_IOB95A	True	R21
IOB97A/D04	I/O	4	DQ12	D04	True_of_IOB97B	True	T21
IOB97B/D05/SI	I/O	4	DQ12	D05/SI	Comp_of_IOB97A	True	U21
IOB99A/D06/SSPI_CLK	I/O	4	DQ12	D06/SSPI_CLK	True_of_IOB99B	True	P19
IOB99B/D07/SSPI_WPN	I/O	4	DQ12	D07/SSPI_WPN	Comp_of_IOB99A	True	R19
IOL101A	I/O	6	DQ7		True_of_IOL101B	True	AA8
IOL101B	I/O	6	DQ7		Comp_of_IOL101A	True	AB8
IOL103A	I/O	6	DQ7		True_of_IOL103B	True	Y8
IOL103B	I/O	6	DQ7		Comp_of_IOL103A	True	Y7
IOL105A	I/O	6	DQ7		True_of_IOL105B	True	W9
IOL105B	I/O	6	DQ7		Comp_of_IOL105A	True	Y9
IOL107A	I/O	6	DQ7		True_of_IOL107B	True	V7
IOL107B	I/O	6	DQ7		Comp_of_IOL107A	True	W7
IOL109A	I/O	6	none			none	U7
IOL1A	I/O	7	none			none	F4

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOL20A	I/O	7	DQ1		True_of_IOL20B	True	J5
IOL20B	I/O	7	DQ1		Comp_of_IOL20A	True	H5
IOL24A/MGCLKT_11/LPLL0_T_IN1/ LPLL1_T_IN1	I/O	7	DQ1	MGCLKT_11/LPLL0_T_IN1/LPL L1_T_IN1	True_of_IOL24B	True	H4
IOL24B/MGCLKC_11/LPLL0_C_IN1/ LPLL1_C_IN1	I/O	7	DQ1	MGCLKC_11/LPLL0_C_IN1/LPL L1_C_IN1	Comp_of_IOL24A	True	G4
IOL29A/MGCLKT_10/LPLL0_T_FB1/ LPLL1_T_FB1	I/O	7	DQ2	MGCLKT_10/LPLL0_T_FB1/LPL L1_T_FB1	True_of_IOL29B	True	K4
IOL29B/MGCLKC_10/LPLL0_C_FB1/ LPLL1_C_FB1	I/O	7	DQ2	MGCLKC_10/LPLL0_C_FB1/LP LL1_C_FB1	Comp_of_IOL29A	True	J4
IOL31A/SGCLKT_10/LPLL0_T_FB0/ LPLL1_T_FB0	I/O	7	DQ2	SGCLKT_10/LPLL0_T_FB0/LPL L1_T_FB0	True_of_IOL31B	True	L3
IOL31B/SGCLKC_10/LPLL0_C_FB0/ LPLL1_C_FB0	I/O	7	DQ2	SGCLKC_10/LPLL0_C_FB0/LP LL1_C_FB0	Comp_of_IOL31A	True	K3
IOL33A	I/O	7	DQ2		True_of_IOL33B	True	M3
IOL33B	I/O	7	DQ2		Comp_of_IOL33A	True	M2
IOL35A	I/O	7	DQS2		True_of_IOL35B	True	M1
IOL35B	I/O	7	DQS2		Comp_of_IOL35A	True	L1
IOL38A	I/O	7	DQ2		True_of_IOL38B	True	K6
IOL38B	I/O	7	DQ2		Comp_of_IOL38A	True	J6
IOL40A	I/O	7	DQ2		True_of_IOL40B	True	L5
IOL40B	I/O	7	DQ2		Comp_of_IOL40A	True	L4
IOL42A	I/O	7	DQ3		True_of_IOL42B	True	N4
IOL42B	I/O	7	DQ3		Comp_of_IOL42A	True	N3
IOL44A	I/O	7	DQ3		True_of_IOL44B	True	R1
IOL44B	I/O	7	DQ3		Comp_of_IOL44A	True	P1
IOL47A	I/O	7	DQ3		True_of_IOL47B	True	P2
IOL47B	I/O	7	DQ3		Comp_of_IOL47A	True	N2
IOL49A	I/O	7	DQS3		True_of_IOL49B	True	P5
IOL49B	I/O	7	DQS3		Comp_of_IOL49A	True	P4
IOL51A	I/O	7	DQ3		True_of_IOL51B	True	M6

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOL51B	I/O	7	DQ3		Comp_of_IOL51A	True	M5
IOL53A	I/O	7	DQ3		True_of_IOL53B	True	P6
IOL53B	I/O	7	DQ3		Comp_of_IOL53A	True	N5
IOL55A	I/O	7	none			none	L6
IOL56A	I/O	6	DQ4		True_of_IOL56B	True	T1
IOL56B	I/O	6	DQ4		Comp_of_IOL56A	True	U1
IOL58A	I/O	6	DQ4		True_of_IOL58B	True	U2
IOL58B	I/O	6	DQ4		Comp_of_IOL58A	True	V2
IOL60A	I/O	6	DQ4		True_of_IOL60B	True	U3
IOL60B	I/O	6	DQ4		Comp_of_IOL60A	True	V3
IOL62A	I/O	6	DQ4		True_of_IOL62B	True	W2
IOL62B	I/O	6	DQ4		Comp_of_IOL62A	True	Y2
IOL65A	I/O	6	DQS4		True_of_IOL65B	True	R3
IOL65B	I/O	6	DQS4		Comp_of_IOL65A	True	R2
IOL67A	I/O	6	DQ4		True_of_IOL67B	True	W1
IOL67B	I/O	6	DQ4		Comp_of_IOL67A	True	Y1
IOL69A	I/O	6	DQS5		True_of_IOL69B	True	Y3
IOL69B	I/O	6	DQS5		Comp_of_IOL69A	True	AA3
IOL71A	I/O	6	DQ5		True_of_IOL71B	True	AA1
IOL71B	I/O	6	DQ5		Comp_of_IOL71A	True	AB1
IOL73A	I/O	6	none			none	T3
IOL74A	I/O	6	DQ5		True_of_IOL74B	True	AB3
IOL74B	I/O	6	DQ5		Comp_of_IOL74A	True	AB2
IOL76A	I/O	6	DQ5		True_of_IOL76B	True	AA5
IOL76B	I/O	6	DQ5		Comp_of_IOL76A	True	AB5
IOL78A/SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	I/O	6	DQ5	SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	True_of_IOL78B	True	Y4
IOL78B/SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	I/O	6	DQ5	SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	Comp_of_IOL78A	True	AA4
IOL80A/MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	I/O	6	DQ5	MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	True_of_IOL80B	True	V4

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOL80B/MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	I/O	6	DQ5	MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	Comp_of_IOL80A	True	W4
IOL83A/MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	I/O	6	DQ6	MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	True_of_IOL83B	True	R4
IOL83B/MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	I/O	6	DQ6	MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	Comp_of_IOL83A	True	T4
IOL85A/SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	I/O	6	DQ6	SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	True_of_IOL85B	True	T5
IOL85B/SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	I/O	6	DQ6	SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	Comp_of_IOL85A	True	U5
IOL87A	I/O	6	DQ6		True_of_IOL87B	True	U6
IOL87B	I/O	6	DQ6		Comp_of_IOL87A	True	V5
IOL89A	I/O	6	DQ6		True_of_IOL89B	True	R6
IOL89B	I/O	6	DQ6		Comp_of_IOL89A	True	T6
IOL92A	I/O	6	DQ6		True_of_IOL92B	True	Y6
IOL92B	I/O	6	DQ6		Comp_of_IOL92A	True	AA6
IOL94A	I/O	6	DQS6		True_of_IOL94B	True	W6
IOL94B	I/O	6	DQS6		Comp_of_IOL94A	True	W5
IOL96A	I/O	6	DQS7		True_of_IOL96B	True	V9
IOL96B	I/O	6	DQS7		Comp_of_IOL96A	True	V8
IOL98A	I/O	6	DQ7		True_of_IOL98B	True	AB7
IOL98B	I/O	6	DQ7		Comp_of_IOL98A	True	AB6
IOR101A	I/O	3	DQ16		True_of_IOR101B	True	J15
IOR101B	I/O	3	DQ16		Comp_of_IOR101A	True	H15
IOR103A	I/O	3	DQS16		True_of_IOR103B	True	J14
IOR103B	I/O	3	DQS16		Comp_of_IOR103A	True	H14
IOR105A	I/O	3	DQ16		True_of_IOR105B	True	G15
IOR105B	I/O	3	DQ16		Comp_of_IOR105A	True	G16
IOR107A/ADCINCK1	I/O	3	DQ16	ADCINCK1	True_of_IOR107B	True	G17
IOR107B	I/O	3	DQ16		Comp_of_IOR107A	True	G18
IOR109A	I/O	3	none			none	J16

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOR11A	I/O	2	DQ23		True_of_IOR11B	True	F13
IOR11B	I/O	2	DQ23		Comp_of_IOR11A	True	F14
IOR13A	I/O	2	DQ23		True_of_IOR13B	True	F16
IOR13B	I/O	2	DQ23		Comp_of_IOR13A	True	E17
IOR15A	I/O	2	DQ22		True_of_IOR15B	True	A13
IOR15B	I/O	2	DQ22		Comp_of_IOR15A	True	A14
IOR17A	I/O	2	DQ22		True_of_IOR17B	True	C13
IOR17B	I/O	2	DQ22		Comp_of_IOR17A	True	B13
IOR1A	I/O	2	none			none	F15
IOR20A	I/O	2	DQS22		True_of_IOR20B	True	A15
IOR20B	I/O	2	DQS22		Comp_of_IOR20A	True	A16
IOR22A	I/O	2	DQ22		True_of_IOR22B	True	B15
IOR22B	I/O	2	DQ22		Comp_of_IOR22A	True	B16
IOR24A/SGCLKT_1/RPLL0_T_FB0/ RPLL1_T_FB0	I/O	2	DQ22	SGCLKT_1/RPLL0_T_FB0/RPL L1_T_FB0	True_of_IOR24B	True	B17
IOR24B/SGCLKC_1/RPLL0_C_FB0/ RPLL1_C_FB0	I/O	2	DQ22	SGCLKC_1/RPLL0_C_FB0/RPL L1_C_FB0	Comp_of_IOR24A	True	B18
IOR26A/MGCLKT_1/RPLL0_T_FB1/ RPLL1_T_FB1	I/O	2	DQ22	MGCLKT_1/RPLL0_T_FB1/RPL L1_T_FB1	True_of_IOR26B	True	D17
IOR26B/MGCLKC_1/RPLL0_C_FB1/ RPLL1_C_FB1	I/O	2	DQ22	MGCLKC_1/RPLL0_C_FB1/RPL L1_C_FB1	Comp_of_IOR26A	True	C17
IOR29A/SGCLKT_0/RPLL0_T_IN0/R PLL1_T_IN0	I/O	2	DQ21	SGCLKT_0/RPLL0_T_IN0/RPLL 1_T_IN0	True_of_IOR29B	True	E19
IOR29B/SGCLKC_0/RPLL0_C_IN0/ RPLL1_C_IN0	I/O	2	DQ21	SGCLKC_0/RPLL0_C_IN0/RPL L1_C_IN0	Comp_of_IOR29A	True	D19
IOR2A	I/O	2	DQ23		True_of_IOR2B	True	E13
IOR2B	I/O	2	DQ23		Comp_of_IOR2A	True	E14
IOR31A/MGCLKT_0/RPLL0_T_IN1/ RPLL1_T_IN1	I/O	2	DQ21	MGCLKT_0/RPLL0_T_IN1/RPL L1_T_IN1	True_of_IOR31B	True	C18
IOR31B/MGCLKC_0/RPLL0_C_IN1/ RPLL1_C_IN1	I/O	2	DQ21	MGCLKC_0/RPLL0_C_IN1/RPL L1_C_IN1	Comp_of_IOR31A	True	C19

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOR33A	I/O	2	DQ21		True_of_IOR33B	True	F19
IOR33B	I/O	2	DQ21		Comp_of_IOR33A	True	F20
IOR35A	I/O	2	DQS21		True_of_IOR35B	True	F18
IOR35B	I/O	2	DQS21		Comp_of_IOR35A	True	E18
IOR38A	I/O	2	DQ21		True_of_IOR38B	True	A18
IOR38B	I/O	2	DQ21		Comp_of_IOR38A	True	A19
IOR40A	I/O	2	DQ21		True_of_IOR40B	True	B20
IOR40B	I/O	2	DQ21		Comp_of_IOR40A	True	A20
IOR42A	I/O	2	DQ20		True_of_IOR42B	True	C22
IOR42B	I/O	2	DQ20		Comp_of_IOR42A	True	B22
IOR44A	I/O	2	DQS20		True_of_IOR44B	True	B21
IOR44B	I/O	2	DQS20		Comp_of_IOR44A	True	A21
IOR47A	I/O	2	DQ20		True_of_IOR47B	True	D20
IOR47B	I/O	2	DQ20		Comp_of_IOR47A	True	C20
IOR49A	I/O	2	DQ20		True_of_IOR49B	True	E22
IOR49B	I/O	2	DQ20		Comp_of_IOR49A	True	D22
IOR4A	I/O	2	DQ23		True_of_IOR4B	True	D14
IOR4B	I/O	2	DQ23		Comp_of_IOR4A	True	D15
IOR51A	I/O	2	DQ20		True_of_IOR51B	True	E21
IOR51B	I/O	2	DQ20		Comp_of_IOR51A	True	D21
IOR53A	I/O	2	DQ20		True_of_IOR53B	True	G21
IOR53B	I/O	2	DQ20		Comp_of_IOR53A	True	G22
IOR55A	I/O	2	none			none	F21
IOR56A	I/O	3	DQ19		True_of_IOR56B	True	M13
IOR56B	I/O	3	DQ19		Comp_of_IOR56A	True	L13
IOR58A	I/O	3	DQS19		True_of_IOR58B	True	K17
IOR58B	I/O	3	DQS19		Comp_of_IOR58A	True	J17
IOR60A	I/O	3	DQ19		True_of_IOR60B	True	K13
IOR60B	I/O	3	DQ19		Comp_of_IOR60A	True	K14
IOR62A	I/O	3	DQ19		True_of_IOR62B	True	L14
IOR62B	I/O	3	DQ19		Comp_of_IOR62A	True	L15

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOR65A	I/O	3	DQ19		True_of_IOR65B	True	L16
IOR65B	I/O	3	DQ19		Comp_of_IOR65A	True	K16
IOR67A	I/O	3	DQ19		True_of_IOR67B	True	M15
IOR67B	I/O	3	DQ19		Comp_of_IOR67A	True	M16
IOR69A	I/O	3	DQ18		True_of_IOR69B	True	H20
IOR69B	I/O	3	DQ18		Comp_of_IOR69A	True	G20
IOR6A	I/O	2	DQ23		True_of_IOR6B	True	E16
IOR6B	I/O	2	DQ23		Comp_of_IOR6A	True	D16
IOR71A	I/O	3	DQ18		True_of_IOR71B	True	M21
IOR71B	I/O	3	DQ18		Comp_of_IOR71A	True	L21
IOR73A	I/O	3	none			none	M17
IOR74A	I/O	3	DQ18		True_of_IOR74B	True	J22
IOR74B	I/O	3	DQ18		Comp_of_IOR74A	True	H22
IOR76A	I/O	3	DQS18		True_of_IOR76B	True	K21
IOR76B	I/O	3	DQS18		Comp_of_IOR76A	True	K22
IOR78A/SGCLKT_3/RPLL2_T_FB0/ RPLL3_T_FB0	I/O	3	DQ18	SGCLKT_3/RPLL2_T_FB0/RPL L3_T_FB0	True_of_IOR78B	True	J20
IOR78B/SGCLKC_3/RPLL2_C_FB0/ RPLL3_C_FB0	I/O	3	DQ18	SGCLKC_3/RPLL2_C_FB0/RPL L3_C_FB0	Comp_of_IOR78A	True	J21
IOR80A/MGCLKT_3/RPLL2_T_FB1/ RPLL3_T_FB1	I/O	3	DQ18	MGCLKT_3/RPLL2_T_FB1/RPL L3_T_FB1	True_of_IOR80B	True	J19
IOR80B/MGCLKC_3/RPLL2_C_FB1/ RPLL3_C_FB1	I/O	3	DQ18	MGCLKC_3/RPLL2_C_FB1/RPL L3_C_FB1	Comp_of_IOR80A	True	H19
IOR83A/MGCLKT_2/RPLL2_T_IN1/ RPLL3_T_IN1	I/O	3	DQ17	MGCLKT_2/RPLL2_T_IN1/RPL L3_T_IN1	True_of_IOR83B	True	K18
IOR83B/MGCLKC_2/RPLL2_C_IN1/ RPLL3_C_IN1	I/O	3	DQ17	MGCLKC_2/RPLL2_C_IN1/RPL L3_C_IN1	Comp_of_IOR83A	True	K19
IOR85A/SGCLKT_2/RPLL2_T_IN0/R PLL3_T_IN0	I/O	3	DQ17	SGCLKT_2/RPLL2_T_IN0/RPLL 3_T_IN0	True_of_IOR85B	True	L19
IOR85B/SGCLKC_2/RPLL2_C_IN0/ RPLL3_C_IN0	I/O	3	DQ17	SGCLKC_2/RPLL2_C_IN0/RPL L3_C_IN0	Comp_of_IOR85A	True	L20

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOR87A	I/O	3	DQ17		True_of_IOR87B	True	M18
IOR87B	I/O	3	DQ17		Comp_of_IOR87A	True	L18
IOR89A	I/O	3	DQS17		True_of_IOR89B	True	N22
IOR89B	I/O	3	DQS17		Comp_of_IOR89A	True	M22
IOR8A	I/O	2	DQS23		True_of_IOR8B	True	C14
IOR8B	I/O	2	DQS23		Comp_of_IOR8A	True	C15
IOR92A	I/O	3	DQ17		True_of_IOR92B	True	N18
IOR92B	I/O	3	DQ17		Comp_of_IOR92A	True	N19
IOR94A	I/O	3	DQ17		True_of_IOR94B	True	N20
IOR94B	I/O	3	DQ17		Comp_of_IOR94A	True	M20
IOR96A	I/O	3	DQ16		True_of_IOR96B	True	H17
IOR96B	I/O	3	DQ16		Comp_of_IOR96A	True	H18
IOR98A	I/O	3	DQ16		True_of_IOR98B	True	H13
IOR98B	I/O	3	DQ16		Comp_of_IOR98A	True	G13
M0_CKN	DIO	MIPI	none			none	D1
M0_CKP	DIO	MIPI	none			none	E1
M0_D0N	DIO	MIPI	none			none	A1
M0_D0P	DIO	MIPI	none			none	B1
M0_D1N	DIO	MIPI	none			none	B2
M0_D1P	DIO	MIPI	none			none	C2
M0_D2N	DIO	MIPI	none			none	D2
M0_D2P	DIO	MIPI	none			none	E2
M0_D3N	DIO	MIPI	none			none	F1
M0_D3P	DIO	MIPI	none			none	G1
M1_CKN	DIO	MIPI	none			none	E3
M1_CKP	DIO	MIPI	none			none	F3
M1_D0N	DIO	MIPI	none			none	G2
M1_D0P	DIO	MIPI	none			none	H2
M1_D1N	DIO	MIPI	none			none	G3
M1_D1P	DIO	MIPI	none			none	H3
M1_D2N	DIO	MIPI	none			none	J1

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
M1_D2P	DIO	MIPI	none			none	K1
M1_D3N	DIO	MIPI	none			none	J2
M1_D3P	DIO	MIPI	none			none	K2
Q0_LN0_RXM_I	DIO	Q0	none			none	A8
Q0_LN0_RXP_I	DIO	Q0	none			none	B8
Q0_LN0_TXM_O	DIO	Q0	none			none	A4
Q0_LN0_TXP_O	DIO	Q0	none			none	B4
Q0_LN1_RXM_I	DIO	Q0	none			none	C9
Q0_LN1_RXP_I	DIO	Q0	none			none	D9
Q0_LN1_TXM_O	DIO	Q0	none			none	C7
Q0_LN1_TXP_O	DIO	Q0	none			none	D7
Q0_LN2_RXM_I	DIO	Q0	none			none	A10
Q0_LN2_RXP_I	DIO	Q0	none			none	B10
Q0_LN2_TXM_O	DIO	Q0	none			none	A6
Q0_LN2_TXP_O	DIO	Q0	none			none	B6
Q0_LN3_RXM_I	DIO	Q0	none			none	C11
Q0_LN3_RXP_I	DIO	Q0	none			none	D11
Q0_LN3_TXM_O	DIO	Q0	none			none	C5
Q0_LN3_TXP_O	DIO	Q0	none			none	D5
Q0_REFCLKM_0	DIO	Q0	none			none	E6
Q0_REFCLKM_1	DIO	Q0	none			none	E10
Q0_REFCLKP_0	DIO	Q0	none			none	F6
Q0_REFCLKP_1	DIO	Q0	none			none	F10
ADCTN	DIO	ADC	none			none	N9
ADCTP	DIO	ADC	none			none	N10
ADCVN	DIO	ADC	none			none	M9
ADCVP	DIO	ADC	none			none	L10
VDDAM_VDDDM	Power	N/A					L11
VDDXM	Power	N/A					K10
VDDAM_VDDDM	Power	N/A					N11
VDDAQ0	Power	N/A					F7

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
VDDAQ0	Power	N/A					D6
VDDAQ0	Power	N/A					D10
VDDHAQ0	Power	N/A					H12
VDDHAQ0	Power	N/A					K12
VDDTQ0	Power	N/A					E8
VDDTQ0	Power	N/A					F9
VCC	Power	N/A					L7
VCC	Power	N/A					T8
VCC	Power	N/A					P10
VCC	Power	N/A					N7
VCC	Power	N/A					H10
VCC	Power	N/A					T10
VCC	Power	N/A					R9
VCC	Power	N/A					J9
VCC	Power	N/A					M8
VCC	Power	N/A					J7
VCC	Power	N/A					H8
VCC	Power	N/A					K8
VCC	Power	N/A					P8
VCC	Power	N/A					R7
VCCLDO	Power	N/A					C4
VCCLDO	Power	N/A					C8
VCCLDO	Power	N/A					B11
VCCLDO	Power	N/A					B5
VCCLDO	Power	N/A					B9
VCCLDO	Power	N/A					B7
VCCIO10	Power	N/A					F12
VCCIO10	Power	N/A					T12
VCCX	Power	N/A					R11
VCCX	Power	N/A					P12
VCCX	Power	N/A					M12

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
VCCIO2	Power	N/A					A17
VCCIO2	Power	N/A					E15
VCCIO2	Power	N/A					C21
VCCIO2	Power	N/A					D18
VCCIO2	Power	N/A					B14
VCCIO2	Power	N/A					F22
VCCIO3	Power	N/A					N21
VCCIO3	Power	N/A					G19
VCCIO3	Power	N/A					L17
VCCIO3	Power	N/A					J13
VCCIO3	Power	N/A					K20
VCCIO3	Power	N/A					H16
VCCIO4	Power	N/A					Y20
VCCIO4	Power	N/A					P18
VCCIO4	Power	N/A					U19
VCCIO4	Power	N/A					R15
VCCIO4	Power	N/A					T22
VCCIO4	Power	N/A					M14
VCCIO5	Power	N/A					W13
VCCIO5	Power	N/A					Y10
VCCIO5	Power	N/A					V16
VCCIO5	Power	N/A					AB14
VCCIO5	Power	N/A					AA17
VCCIO6	Power	N/A					W3
VCCIO6	Power	N/A					AA7
VCCIO6	Power	N/A					V6
VCCIO6	Power	N/A					T2
VCCIO6	Power	N/A					R5
VCCIO6	Power	N/A					AB4
VCCIO7	Power	N/A					F2
VCCIO7	Power	N/A					C1

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
VCCIO7	Power	N/A					N1
VCCIO7	Power	N/A					H6
VCCIO7	Power	N/A					J3
VCCIO7	Power	N/A					M4
VSS	Ground	N/A					K9
VSS	Ground	N/A					D8
VSS	Ground	N/A					A2
VSS	Ground	N/A					A3
VSS	Ground	N/A					A5
VSS	Ground	N/A					A7
VSS	Ground	N/A					A9
VSS	Ground	N/A					A11
VSS	Ground	N/A					A12
VSS	Ground	N/A					A22
VSS	Ground	N/A					AA2
VSS	Ground	N/A					AA12
VSS	Ground	N/A					AA22
VSS	Ground	N/A					AB9
VSS	Ground	N/A					AB19
VSS	Ground	N/A					B3
VSS	Ground	N/A					B12
VSS	Ground	N/A					B19
VSS	Ground	N/A					C3
VSS	Ground	N/A					C6
VSS	Ground	N/A					C10
VSS	Ground	N/A					C12
VSS	Ground	N/A					C16
VSS	Ground	N/A					D3
VSS	Ground	N/A					D4
VSS	Ground	N/A					D12
VSS	Ground	N/A					D13

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
VSS	Ground	N/A					E4
VSS	Ground	N/A					E5
VSS	Ground	N/A					E7
VSS	Ground	N/A					E9
VSS	Ground	N/A					E11
VSS	Ground	N/A					E20
VSS	Ground	N/A					F5
VSS	Ground	N/A					F11
VSS	Ground	N/A					F17
VSS	Ground	N/A					G5
VSS	Ground	N/A					G6
VSS	Ground	N/A					G7
VSS	Ground	N/A					G8
VSS	Ground	N/A					G9
VSS	Ground	N/A					G10
VSS	Ground	N/A					G12
VSS	Ground	N/A					G14
VSS	Ground	N/A					H1
VSS	Ground	N/A					H7
VSS	Ground	N/A					H9
VSS	Ground	N/A					H11
VSS	Ground	N/A					H21
VSS	Ground	N/A					J8
VSS	Ground	N/A					J10
VSS	Ground	N/A					J12
VSS	Ground	N/A					J18
VSS	Ground	N/A					K5
VSS	Ground	N/A					K7
VSS	Ground	N/A					K11
VSS	Ground	N/A					K15
VSS	Ground	N/A					L2

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
VSS	Ground	N/A					L8
VSS	Ground	N/A					L22
VSS	Ground	N/A					M7
VSS	Ground	N/A					M11
VSS	Ground	N/A					M19
VSS	Ground	N/A					N6
VSS	Ground	N/A					N8
VSS	Ground	N/A					N16
VSS	Ground	N/A					P3
VSS	Ground	N/A					P7
VSS	Ground	N/A					P9
VSS	Ground	N/A					P11
VSS	Ground	N/A					P13
VSS	Ground	N/A					R8
VSS	Ground	N/A					R10
VSS	Ground	N/A					R12
VSS	Ground	N/A					R20
VSS	Ground	N/A					T7
VSS	Ground	N/A					T9
VSS	Ground	N/A					T11
VSS	Ground	N/A					T17
VSS	Ground	N/A					U4
VSS	Ground	N/A					U14
VSS	Ground	N/A					V1
VSS	Ground	N/A					V11
VSS	Ground	N/A					V21
VSS	Ground	N/A					W8
VSS	Ground	N/A					W18
VSS	Ground	N/A					Y5
VSS	Ground	N/A					Y15
NC	N/A	N/A					M10

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
NC	N/A	N/A					L9
NC	N/A	N/A					E12
NC	N/A	N/A					F8
NC	N/A	N/A					J11

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOB102A/MCS_N	I/O	4	DQ12	MCS_N	True_of_IOB102B	True	T19
IOB102B/D08/SO	I/O	4	DQ12	D08/SO	Comp_of_IOB102A	True	T20
IOB104A/PUDC_B	I/O	4	DQS12	PUDC_B	True_of_IOB104B	True	U22
IOB104B/EMCCLK	I/O	4	DQS12	EMCCLK	Comp_of_IOB104A	True	V22
IOB106A/CSI_B	I/O	4	DQ13	CSI_B	True_of_IOB106B	True	V17
IOB106B/D31	I/O	4	DQ13	D31	Comp_of_IOB106A	True	W17
IOB108A/D30	I/O	4	DQ13	D30	True_of_IOB108B	True	AA18
IOB108B/D29	I/O	4	DQ13	D29	Comp_of_IOB108A	True	AB18
IOB110A/RDWR	I/O	4	DQS13	RDWR	True_of_IOB110B	True	AA19
IOB110B/DOUT_CSO_B	I/O	4	DQS13	DOUT_CSO_B	Comp_of_IOB110A	True	AB20
IOB112A/D28	I/O	4	DQ13	D28	True_of_IOB112B	True	U17
IOB112B/D27	I/O	4	DQ13	D27	Comp_of_IOB112A	True	U18
IOB114A/SGCLKT_4/BPLL2_T_FB1/ BPLL3_T_FB1	I/O	4	DQ13	SGCLKT_4/BPLL2_T_FB1/BPL L3_T_FB1	True_of_IOB114B	True	V18
IOB114B/SGCLKC_4/BPLL2_C_FB1/ BPLL3_C_FB1	I/O	4	DQ13	SGCLKC_4/BPLL2_C_FB1/BP LL3_C_FB1	Comp_of_IOB114A	True	V19
IOB116A/MGCLKT_4/BPLL2_T_FB0/ BPLL3_T_FB0	I/O	4	DQ13	MGCLKT_4/BPLL2_T_FB0/BPL L3_T_FB0	True_of_IOB116B	True	Y18
IOB116B/MGCLKC_4/BPLL2_C_FB0/ BPLL3_C_FB0	I/O	4	DQ13	MGCLKC_4/BPLL2_C_FB0/BP LL3_C_FB0	Comp_of_IOB116A	True	Y19
IOB120A/SGCLKT_5/BPLL2_T_IN0/ BPLL3_T_IN0	I/O	4	DQ14	SGCLKT_5/BPLL2_T_IN0/BPL L3_T_IN0	True_of_IOB120B	True	U20
IOB120B/SGCLKC_5/BPLL2_C_IN0/ BPLL3_C_IN0	I/O	4	DQ14	SGCLKC_5/BPLL2_C_IN0/BPL L3_C_IN0	Comp_of_IOB120A	True	V20
IOB122A/MGCLKT_5/BPLL2_T_IN1/ BPLL3_T_IN1	I/O	4	DQ14	MGCLKT_5/BPLL2_T_IN1/BPL L3_T_IN1	True_of_IOB122B	True	W19
IOB122B/MGCLKC_5/BPLL2_C_IN1/ BPLL3_C_IN1	I/O	4	DQ14	MGCLKC_5/BPLL2_C_IN1/BPL L3_C_IN1	Comp_of_IOB122A	True	W20
IOB124A/D09	I/O	4	DQ14	D09	True_of_IOB124B	True	W21
IOB124B/D10	I/O	4	DQ14	D10	Comp_of_IOB124A	True	W22

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOB126A/D11	I/O	4	DQ14	D11	True_of_IOB126B	True	AA20
IOB126B/D12	I/O	4	DQ14	D12	Comp_of_IOB126A	True	AA21
IOB129A/D14	I/O	4	DQ14	D14	True_of_IOB129B	True	AB21
IOB129B/D15	I/O	4	DQ14	D15	Comp_of_IOB129A	True	AB22
IOB131A/SSPI_CS_N	I/O	4	DQS14	SSPI_CS_N	True_of_IOB131B	True	Y21
IOB131B/D13	I/O	4	DQS14	D13	Comp_of_IOB131A	True	Y22
IOB133A/D26	I/O	4	DQ15	D26	True_of_IOB133B	True	P14
IOB133B/D25	I/O	4	DQ15	D25	Comp_of_IOB133A	True	R14
IOB135A/CLKHOLD_N	I/O	4	DQS15	CLKHOLD_N	True_of_IOB135B	True	N17
IOB135B/D22	I/O	4	DQS15	D22	Comp_of_IOB135A	True	P17
IOB138A/D24	I/O	4	DQ15	D24	True_of_IOB138B	True	R18
IOB138B/D23	I/O	4	DQ15	D23	Comp_of_IOB138A	True	T18
IOB140A/D21	I/O	4	DQ15	D21	True_of_IOB140B	True	P15
IOB140B/D20	I/O	4	DQ15	D20	Comp_of_IOB140A	True	R16
IOB142A/D19	I/O	4	DQ15	D19	True_of_IOB142B	True	N13
IOB142B/D18	I/O	4	DQ15	D18	Comp_of_IOB142A	True	N14
IOB144A/D17	I/O	4	DQ15	D17	True_of_IOB144B	True	P16
IOB144B/D16	I/O	4	DQ15	D16	Comp_of_IOB144A	True	R17
IOB146A	I/O	4	none			none	N15
IOB169A/TDO	I/O	10	none	TDO	True_of_IOB169B	True	U13
IOB169B/TMS	I/O	10	none	TMS	Comp_of_IOB169A	True	T13
IOB171A/READY	I/O	10	none	READY	True_of_IOB171B	True	U12
IOB171B/DONE	I/O	10	none	DONE	Comp_of_IOB171A	True	G11
IOB173A/TCK	I/O	10	none	TCK	True_of_IOB173B	True	V12
IOB173B/TDI	I/O	10	none	TDI	Comp_of_IOB173A	True	R13
IOB175A/MODE0	I/O	10	none	MODE0	True_of_IOB175B	True	U11
IOB175B/CCLK	I/O	10	none	CCLK	Comp_of_IOB175A	True	L12
IOB177A/MODE1	I/O	10	none	MODE1	True_of_IOB177B	True	U10
IOB177B/MODE2	I/O	10	none	MODE2	Comp_of_IOB177A	True	U9
IOB179A/CFGBVS	I/O	10	none	CFGBVS	True_of_IOB179B	True	U8
IOB179B/RECONFIG_N	I/O	10	none	RECONFIG_N	Comp_of_IOB179A	True	N12

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOB51A	I/O	5	DQ9		True_of_IOB51B	True	AB11
IOB51B	I/O	5	DQ9		Comp_of_IOB51A	True	AB12
IOB53A	I/O	5	DQ9		True_of_IOB53B	True	AA9
IOB53B	I/O	5	DQ9		Comp_of_IOB53A	True	AB10
IOB56A	I/O	5	DQS9		True_of_IOB56B	True	AA10
IOB56B	I/O	5	DQS9		Comp_of_IOB56A	True	AA11
IOB58A	I/O	5	DQ9		True_of_IOB58B	True	V10
IOB58B	I/O	5	DQ9		Comp_of_IOB58A	True	W10
IOB60A/SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	I/O	5	DQ9	SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	True_of_IOB60B	True	Y11
IOB60B/SGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	I/O	5	DQ9	SGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	Comp_of_IOB60A	True	Y12
IOB62A/MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	I/O	5	DQ9	MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	True_of_IOB62B	True	W11
IOB62B/MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	I/O	5	DQ9	MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	Comp_of_IOB62A	True	W12
IOB66A/MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	I/O	5	DQ10	MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	True_of_IOB66B	True	V13
IOB66B/MGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	I/O	5	DQ10	MGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	Comp_of_IOB66A	True	V14
IOB68A/SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	I/O	5	DQ10	SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	True_of_IOB68B	True	U15
IOB68B/SGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	I/O	5	DQ10	SGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	Comp_of_IOB68A	True	V15
IOB70A	I/O	5	DQS10		True_of_IOB70B	True	T14
IOB70B	I/O	5	DQS10		Comp_of_IOB70A	True	T15
IOB72A	I/O	5	DQ10		True_of_IOB72B	True	W15
IOB72B	I/O	5	DQ10		Comp_of_IOB72A	True	W16
IOB76A	I/O	5	DQ10		True_of_IOB76B	True	T16
IOB76B	I/O	5	DQ10		Comp_of_IOB76A	True	U16
IOB78A	I/O	5	DQ11		True_of_IOB78B	True	Y16

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOB78B	I/O	5	DQ11		Comp_of_IOB78A	True	AA16
IOB80A	I/O	5	DQ11		True_of_IOB80B	True	AB16
IOB80B	I/O	5	DQ11		Comp_of_IOB80A	True	AB17
IOB83A	I/O	5	DQ11		True_of_IOB83B	True	AA15
IOB83B	I/O	5	DQ11		Comp_of_IOB83A	True	AB15
IOB85A	I/O	5	DQ11		True_of_IOB85B	True	W14
IOB85B	I/O	5	DQ11		Comp_of_IOB85A	True	Y14
IOB87A	I/O	5	DQ11		True_of_IOB87B	True	Y13
IOB87B	I/O	5	DQ11		Comp_of_IOB87A	True	AA14
IOB89A	I/O	5	DQS11		True_of_IOB89B	True	AA13
IOB89B	I/O	5	DQS11		Comp_of_IOB89A	True	AB13
IOB91A	I/O	5	none			none	Y17
IOB92A	I/O	4	none			none	P20
IOB93A/D00/MOSI	I/O	4	DQ12	D00/MOSI	True_of_IOB93B	True	P22
IOB93B/D01/DIN	I/O	4	DQ12	D01/DIN	Comp_of_IOB93A	True	R22
IOB95A/D02	I/O	4	DQ12	D02	True_of_IOB95B	True	P21
IOB95B/D03	I/O	4	DQ12	D03	Comp_of_IOB95A	True	R21
IOB97A/D04	I/O	4	DQ12	D04	True_of_IOB97B	True	T21
IOB97B/D05/SI	I/O	4	DQ12	D05/SI	Comp_of_IOB97A	True	U21
IOB99A/D06/SSPI_CLK	I/O	4	DQ12	D06/SSPI_CLK	True_of_IOB99B	True	P19
IOB99B/D07/SSPI_WPN	I/O	4	DQ12	D07/SSPI_WPN	Comp_of_IOB99A	True	R19
IOL101A	I/O	6	DQ7		True_of_IOL101B	True	AA8
IOL101B	I/O	6	DQ7		Comp_of_IOL101A	True	AB8
IOL103A	I/O	6	DQ7		True_of_IOL103B	True	Y8
IOL103B	I/O	6	DQ7		Comp_of_IOL103A	True	Y7
IOL105A	I/O	6	DQ7		True_of_IOL105B	True	W9
IOL105B	I/O	6	DQ7		Comp_of_IOL105A	True	Y9
IOL107A	I/O	6	DQ7		True_of_IOL107B	True	V7
IOL107B	I/O	6	DQ7		Comp_of_IOL107A	True	W7
IOL109A	I/O	6	none			none	U7
IOL11A	I/O	7	DQ0		True_of_IOL11B	True	G1

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOL11B	I/O	7	DQ0		Comp_of_IOL11A	True	F1
IOL13A	I/O	7	DQ0		True_of_IOL13B	True	F3
IOL13B	I/O	7	DQ0		Comp_of_IOL13A	True	E3
IOL15A	I/O	7	DQ1		True_of_IOL15B	True	K1
IOL15B	I/O	7	DQ1		Comp_of_IOL15A	True	J1
IOL17A	I/O	7	DQ1		True_of_IOL17B	True	H2
IOL17B	I/O	7	DQ1		Comp_of_IOL17A	True	G2
IOL1A	I/O	7	none			none	F4
IOL20A	I/O	7	DQ1		True_of_IOL20B	True	J5
IOL20B	I/O	7	DQ1		Comp_of_IOL20A	True	H5
IOL22A	I/O	7	DQS1		True_of_IOL22B	True	K2
IOL22B	I/O	7	DQS1		Comp_of_IOL22A	True	J2
IOL24A/MGCLKT_11/LPLL0_T_IN1/LPLL1_T_IN1	I/O	7	DQ1	MGCLKT_11/LPLL0_T_IN1/LPLL1_T_IN1	True_of_IOL24B	True	H4
IOL24B/MGCLKC_11/LPLL0_C_IN1/LPLL1_C_IN1	I/O	7	DQ1	MGCLKC_11/LPLL0_C_IN1/LPLL1_C_IN1	Comp_of_IOL24A	True	G4
IOL26A/SGCLKT_11/LPLL0_T_IN0/LPLL1_T_IN0	I/O	7	DQ1	SGCLKT_11/LPLL0_T_IN0/LPLL1_T_IN0	True_of_IOL26B	True	H3
IOL26B/SGCLKC_11/LPLL0_C_IN0/LPLL1_C_IN0	I/O	7	DQ1	SGCLKC_11/LPLL0_C_IN0/LPLL1_C_IN0	Comp_of_IOL26A	True	G3
IOL29A/MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	I/O	7	DQ2	MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	True_of_IOL29B	True	K4
IOL29B/MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	I/O	7	DQ2	MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	Comp_of_IOL29A	True	J4
IOL2A/ADCINCK0	I/O	7	DQ0	ADCINCK0	True_of_IOL2B	True	B1
IOL2B	I/O	7	DQ0		Comp_of_IOL2A	True	A1
IOL31A/SGCLKT_10/LPLL0_T_FB0/LPLL1_T_FB0	I/O	7	DQ2	SGCLKT_10/LPLL0_T_FB0/LPLL1_T_FB0	True_of_IOL31B	True	L3
IOL31B/SGCLKC_10/LPLL0_C_FB0/LPLL1_C_FB0	I/O	7	DQ2	SGCLKC_10/LPLL0_C_FB0/LPLL1_C_FB0	Comp_of_IOL31A	True	K3
IOL33A	I/O	7	DQ2		True_of_IOL33B	True	M3

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOL33B	I/O	7	DQ2		Comp_of_IOL33A	True	M2
IOL35A	I/O	7	DQS2		True_of_IOL35B	True	M1
IOL35B	I/O	7	DQS2		Comp_of_IOL35A	True	L1
IOL38A	I/O	7	DQ2		True_of_IOL38B	True	K6
IOL38B	I/O	7	DQ2		Comp_of_IOL38A	True	J6
IOL40A	I/O	7	DQ2		True_of_IOL40B	True	L5
IOL40B	I/O	7	DQ2		Comp_of_IOL40A	True	L4
IOL42A	I/O	7	DQ3		True_of_IOL42B	True	N4
IOL42B	I/O	7	DQ3		Comp_of_IOL42A	True	N3
IOL44A	I/O	7	DQ3		True_of_IOL44B	True	R1
IOL44B	I/O	7	DQ3		Comp_of_IOL44A	True	P1
IOL47A	I/O	7	DQ3		True_of_IOL47B	True	P2
IOL47B	I/O	7	DQ3		Comp_of_IOL47A	True	N2
IOL49A	I/O	7	DQS3		True_of_IOL49B	True	P5
IOL49B	I/O	7	DQS3		Comp_of_IOL49A	True	P4
IOL4A	I/O	7	DQS0		True_of_IOL4B	True	E1
IOL4B	I/O	7	DQS0		Comp_of_IOL4A	True	D1
IOL51A	I/O	7	DQ3		True_of_IOL51B	True	M6
IOL51B	I/O	7	DQ3		Comp_of_IOL51A	True	M5
IOL53A	I/O	7	DQ3		True_of_IOL53B	True	P6
IOL53B	I/O	7	DQ3		Comp_of_IOL53A	True	N5
IOL55A	I/O	7	none			none	L6
IOL56A	I/O	6	DQ4		True_of_IOL56B	True	T1
IOL56B	I/O	6	DQ4		Comp_of_IOL56A	True	U1
IOL58A	I/O	6	DQ4		True_of_IOL58B	True	U2
IOL58B	I/O	6	DQ4		Comp_of_IOL58A	True	V2
IOL60A	I/O	6	DQ4		True_of_IOL60B	True	U3
IOL60B	I/O	6	DQ4		Comp_of_IOL60A	True	V3
IOL62A	I/O	6	DQ4		True_of_IOL62B	True	W2
IOL62B	I/O	6	DQ4		Comp_of_IOL62A	True	Y2
IOL65A	I/O	6	DQS4		True_of_IOL65B	True	R3

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOL65B	I/O	6	DQS4		Comp_of_IOL65A	True	R2
IOL67A	I/O	6	DQ4		True_of_IOL67B	True	W1
IOL67B	I/O	6	DQ4		Comp_of_IOL67A	True	Y1
IOL69A	I/O	6	DQS5		True_of_IOL69B	True	Y3
IOL69B	I/O	6	DQS5		Comp_of_IOL69A	True	AA3
IOL6A	I/O	7	DQ0		True_of_IOL6B	True	C2
IOL6B	I/O	7	DQ0		Comp_of_IOL6A	True	B2
IOL71A	I/O	6	DQ5		True_of_IOL71B	True	AA1
IOL71B	I/O	6	DQ5		Comp_of_IOL71A	True	AB1
IOL73A	I/O	6	none			none	T3
IOL74A	I/O	6	DQ5		True_of_IOL74B	True	AB3
IOL74B	I/O	6	DQ5		Comp_of_IOL74A	True	AB2
IOL76A	I/O	6	DQ5		True_of_IOL76B	True	AA5
IOL76B	I/O	6	DQ5		Comp_of_IOL76A	True	AB5
IOL78A/SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	I/O	6	DQ5	SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	True_of_IOL78B	True	Y4
IOL78B/SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	I/O	6	DQ5	SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	Comp_of_IOL78A	True	AA4
IOL80A/MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	I/O	6	DQ5	MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	True_of_IOL80B	True	V4
IOL80B/MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	I/O	6	DQ5	MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	Comp_of_IOL80A	True	W4
IOL83A/MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	I/O	6	DQ6	MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	True_of_IOL83B	True	R4
IOL83B/MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	I/O	6	DQ6	MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	Comp_of_IOL83A	True	T4
IOL85A/SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	I/O	6	DQ6	SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	True_of_IOL85B	True	T5
IOL85B/SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	I/O	6	DQ6	SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	Comp_of_IOL85A	True	U5
IOL87A	I/O	6	DQ6		True_of_IOL87B	True	U6

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOL87B	I/O	6	DQ6		Comp_of_IOL87A	True	V5
IOL89A	I/O	6	DQ6		True_of_IOL89B	True	R6
IOL89B	I/O	6	DQ6		Comp_of_IOL89A	True	T6
IOL8A	I/O	7	DQ0		True_of_IOL8B	True	E2
IOL8B	I/O	7	DQ0		Comp_of_IOL8A	True	D2
IOL92A	I/O	6	DQ6		True_of_IOL92B	True	Y6
IOL92B	I/O	6	DQ6		Comp_of_IOL92A	True	AA6
IOL94A	I/O	6	DQS6		True_of_IOL94B	True	W6
IOL94B	I/O	6	DQS6		Comp_of_IOL94A	True	W5
IOL96A	I/O	6	DQS7		True_of_IOL96B	True	V9
IOL96B	I/O	6	DQS7		Comp_of_IOL96A	True	V8
IOL98A	I/O	6	DQ7		True_of_IOL98B	True	AB7
IOL98B	I/O	6	DQ7		Comp_of_IOL98A	True	AB6
IOR101A	I/O	3	DQ16		True_of_IOR101B	True	J15
IOR101B	I/O	3	DQ16		Comp_of_IOR101A	True	H15
IOR103A	I/O	3	DQS16		True_of_IOR103B	True	J14
IOR103B	I/O	3	DQS16		Comp_of_IOR103A	True	H14
IOR105A	I/O	3	DQ16		True_of_IOR105B	True	G15
IOR105B	I/O	3	DQ16		Comp_of_IOR105A	True	G16
IOR107A/ADCINCK1	I/O	3	DQ16	ADCINCK1	True_of_IOR107B	True	G17
IOR107B	I/O	3	DQ16		Comp_of_IOR107A	True	G18
IOR109A	I/O	3	none			none	J16
IOR11A	I/O	2	DQ23		True_of_IOR11B	True	F13
IOR11B	I/O	2	DQ23		Comp_of_IOR11A	True	F14
IOR13A	I/O	2	DQ23		True_of_IOR13B	True	F16
IOR13B	I/O	2	DQ23		Comp_of_IOR13A	True	E17
IOR15A	I/O	2	DQ22		True_of_IOR15B	True	A13
IOR15B	I/O	2	DQ22		Comp_of_IOR15A	True	A14
IOR17A	I/O	2	DQ22		True_of_IOR17B	True	C13
IOR17B	I/O	2	DQ22		Comp_of_IOR17A	True	B13
IOR1A	I/O	2	none			none	F15

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOR20A	I/O	2	DQS22		True_of_IOR20B	True	A15
IOR20B	I/O	2	DQS22		Comp_of_IOR20A	True	A16
IOR22A	I/O	2	DQ22		True_of_IOR22B	True	B15
IOR22B	I/O	2	DQ22		Comp_of_IOR22A	True	B16
IOR24A/SGCLKT_1/RPLL0_T_FB0/ RPLL1_T_FB0	I/O	2	DQ22	SGCLKT_1/RPLL0_T_FB0/RPL L1_T_FB0	True_of_IOR24B	True	B17
IOR24B/SGCLKC_1/RPLL0_C_FB0/ RPLL1_C_FB0	I/O	2	DQ22	SGCLKC_1/RPLL0_C_FB0/RP LL1_C_FB0	Comp_of_IOR24A	True	B18
IOR26A/MGCLKT_1/RPLL0_T_FB1/ RPLL1_T_FB1	I/O	2	DQ22	MGCLKT_1/RPLL0_T_FB1/RP LL1_T_FB1	True_of_IOR26B	True	D17
IOR26B/MGCLKC_1/RPLL0_C_FB1/ RPLL1_C_FB1	I/O	2	DQ22	MGCLKC_1/RPLL0_C_FB1/RP LL1_C_FB1	Comp_of_IOR26A	True	C17
IOR29A/SGCLKT_0/RPLL0_T_IN0/R PLL1_T_IN0	I/O	2	DQ21	SGCLKT_0/RPLL0_T_IN0/RPL L1_T_IN0	True_of_IOR29B	True	E19
IOR29B/SGCLKC_0/RPLL0_C_IN0/R PLL1_C_IN0	I/O	2	DQ21	SGCLKC_0/RPLL0_C_IN0/RPL L1_C_IN0	Comp_of_IOR29A	True	D19
IOR2A	I/O	2	DQ23		True_of_IOR2B	True	E13
IOR2B	I/O	2	DQ23		Comp_of_IOR2A	True	E14
IOR31A/MGCLKT_0/RPLL0_T_IN1/R PLL1_T_IN1	I/O	2	DQ21	MGCLKT_0/RPLL0_T_IN1/RPL L1_T_IN1	True_of_IOR31B	True	C18
IOR31B/MGCLKC_0/RPLL0_C_IN1/ RPLL1_C_IN1	I/O	2	DQ21	MGCLKC_0/RPLL0_C_IN1/RP LL1_C_IN1	Comp_of_IOR31A	True	C19
IOR33A	I/O	2	DQ21		True_of_IOR33B	True	F19
IOR33B	I/O	2	DQ21		Comp_of_IOR33A	True	F20
IOR35A	I/O	2	DQS21		True_of_IOR35B	True	F18
IOR35B	I/O	2	DQS21		Comp_of_IOR35A	True	E18
IOR38A	I/O	2	DQ21		True_of_IOR38B	True	A18
IOR38B	I/O	2	DQ21		Comp_of_IOR38A	True	A19
IOR40A	I/O	2	DQ21		True_of_IOR40B	True	B20
IOR40B	I/O	2	DQ21		Comp_of_IOR40A	True	A20
IOR42A	I/O	2	DQ20		True_of_IOR42B	True	C22

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOR42B	I/O	2	DQ20		Comp_of_IOR42A	True	B22
IOR44A	I/O	2	DQS20		True_of_IOR44B	True	B21
IOR44B	I/O	2	DQS20		Comp_of_IOR44A	True	A21
IOR47A	I/O	2	DQ20		True_of_IOR47B	True	D20
IOR47B	I/O	2	DQ20		Comp_of_IOR47A	True	C20
IOR49A	I/O	2	DQ20		True_of_IOR49B	True	E22
IOR49B	I/O	2	DQ20		Comp_of_IOR49A	True	D22
IOR4A	I/O	2	DQ23		True_of_IOR4B	True	D14
IOR4B	I/O	2	DQ23		Comp_of_IOR4A	True	D15
IOR51A	I/O	2	DQ20		True_of_IOR51B	True	E21
IOR51B	I/O	2	DQ20		Comp_of_IOR51A	True	D21
IOR53A	I/O	2	DQ20		True_of_IOR53B	True	G21
IOR53B	I/O	2	DQ20		Comp_of_IOR53A	True	G22
IOR55A	I/O	2	none			none	F21
IOR56A	I/O	3	DQ19		True_of_IOR56B	True	M13
IOR56B	I/O	3	DQ19		Comp_of_IOR56A	True	L13
IOR58A	I/O	3	DQS19		True_of_IOR58B	True	K17
IOR58B	I/O	3	DQS19		Comp_of_IOR58A	True	J17
IOR60A	I/O	3	DQ19		True_of_IOR60B	True	K13
IOR60B	I/O	3	DQ19		Comp_of_IOR60A	True	K14
IOR62A	I/O	3	DQ19		True_of_IOR62B	True	L14
IOR62B	I/O	3	DQ19		Comp_of_IOR62A	True	L15
IOR65A	I/O	3	DQ19		True_of_IOR65B	True	L16
IOR65B	I/O	3	DQ19		Comp_of_IOR65A	True	K16
IOR67A	I/O	3	DQ19		True_of_IOR67B	True	M15
IOR67B	I/O	3	DQ19		Comp_of_IOR67A	True	M16
IOR69A	I/O	3	DQ18		True_of_IOR69B	True	H20
IOR69B	I/O	3	DQ18		Comp_of_IOR69A	True	G20
IOR6A	I/O	2	DQ23		True_of_IOR6B	True	E16
IOR6B	I/O	2	DQ23		Comp_of_IOR6A	True	D16
IOR71A	I/O	3	DQ18		True_of_IOR71B	True	M21

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOR71B	I/O	3	DQ18		Comp_of_IOR71A	True	L21
IOR73A	I/O	3	none			none	M17
IOR74A	I/O	3	DQ18		True_of_IOR74B	True	J22
IOR74B	I/O	3	DQ18		Comp_of_IOR74A	True	H22
IOR76A	I/O	3	DQS18		True_of_IOR76B	True	K21
IOR76B	I/O	3	DQS18		Comp_of_IOR76A	True	K22
IOR78A/SGCLKT_3/RPLL2_T_FB0/ RPLL3_T_FB0	I/O	3	DQ18	SGCLKT_3/RPLL2_T_FB0/RPL L3_T_FB0	True_of_IOR78B	True	J20
IOR78B/SGCLKC_3/RPLL2_C_FB0/ RPLL3_C_FB0	I/O	3	DQ18	SGCLKC_3/RPLL2_C_FB0/RP LL3_C_FB0	Comp_of_IOR78A	True	J21
IOR80A/MGCLKT_3/RPLL2_T_FB1/ RPLL3_T_FB1	I/O	3	DQ18	MGCLKT_3/RPLL2_T_FB1/RP LL3_T_FB1	True_of_IOR80B	True	J19
IOR80B/MGCLKC_3/RPLL2_C_FB1/ RPLL3_C_FB1	I/O	3	DQ18	MGCLKC_3/RPLL2_C_FB1/RP LL3_C_FB1	Comp_of_IOR80A	True	H19
IOR83A/MGCLKT_2/RPLL2_T_IN1/R PLL3_T_IN1	I/O	3	DQ17	MGCLKT_2/RPLL2_T_IN1/RPL L3_T_IN1	True_of_IOR83B	True	K18
IOR83B/MGCLKC_2/RPLL2_C_IN1/ RPLL3_C_IN1	I/O	3	DQ17	MGCLKC_2/RPLL2_C_IN1/RP LL3_C_IN1	Comp_of_IOR83A	True	K19
IOR85A/SGCLKT_2/RPLL2_T_IN0/R PLL3_T_IN0	I/O	3	DQ17	SGCLKT_2/RPLL2_T_IN0/RPL L3_T_IN0	True_of_IOR85B	True	L19
IOR85B/SGCLKC_2/RPLL2_C_IN0/R PLL3_C_IN0	I/O	3	DQ17	SGCLKC_2/RPLL2_C_IN0/RPL L3_C_IN0	Comp_of_IOR85A	True	L20
IOR87A	I/O	3	DQ17		True_of_IOR87B	True	M18
IOR87B	I/O	3	DQ17		Comp_of_IOR87A	True	L18
IOR89A	I/O	3	DQS17		True_of_IOR89B	True	N22
IOR89B	I/O	3	DQS17		Comp_of_IOR89A	True	M22
IOR8A	I/O	2	DQS23		True_of_IOR8B	True	C14
IOR8B	I/O	2	DQS23		Comp_of_IOR8A	True	C15
IOR92A	I/O	3	DQ17		True_of_IOR92B	True	N18
IOR92B	I/O	3	DQ17		Comp_of_IOR92A	True	N19
IOR94A	I/O	3	DQ17		True_of_IOR94B	True	N20

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOR94B	I/O	3	DQ17		Comp_of_IOR94A	True	M20
IOR96A	I/O	3	DQ16		True_of_IOR96B	True	H17
IOR96B	I/O	3	DQ16		Comp_of_IOR96A	True	H18
IOR98A	I/O	3	DQ16		True_of_IOR98B	True	H13
IOR98B	I/O	3	DQ16		Comp_of_IOR98A	True	G13
Q0_LN0_RXM_I	DIO	Q0	none			none	A8
Q0_LN0_RXP_I	DIO	Q0	none			none	B8
Q0_LN0_TXM_O	DIO	Q0	none			none	A4
Q0_LN0_TXP_O	DIO	Q0	none			none	B4
Q0_LN1_RXM_I	DIO	Q0	none			none	C9
Q0_LN1_RXP_I	DIO	Q0	none			none	D9
Q0_LN1_TXM_O	DIO	Q0	none			none	C7
Q0_LN1_TXP_O	DIO	Q0	none			none	D7
Q0_LN2_RXM_I	DIO	Q0	none			none	A10
Q0_LN2_RXP_I	DIO	Q0	none			none	B10
Q0_LN2_TXM_O	DIO	Q0	none			none	A6
Q0_LN2_TXP_O	DIO	Q0	none			none	B6
Q0_LN3_RXM_I	DIO	Q0	none			none	C11
Q0_LN3_RXP_I	DIO	Q0	none			none	D11
Q0_LN3_TXM_O	DIO	Q0	none			none	C5
Q0_LN3_TXP_O	DIO	Q0	none			none	D5
Q0_REFCLKM_0	DIO	Q0	none			none	E6
Q0_REFCLKM_1	DIO	Q0	none			none	E10
Q0_REFCLKP_0	DIO	Q0	none			none	F6
Q0_REFCLKP_1	DIO	Q0	none			none	F10
ADCTN	DIO	ADC	none			none	N9
ADCTP	DIO	ADC	none			none	N10
ADCVN	DIO	ADC	none			none	M9
ADCVP	DIO	ADC	none			none	L10
VDDAQ0	Power	N/A					F7
VDDAQ0	Power	N/A					D10

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
VDDAQ0	Power	N/A					D6
VDDHAQ0	Power	N/A					H12
VDDTQ0	Power	N/A					E8
VDDTQ0	Power	N/A					F9
VDDHAQ1	Power	N/A					K12
VCC	Power	N/A					H8
VCC	Power	N/A					T8
VCC	Power	N/A					R9
VCC	Power	N/A					H10
VCC	Power	N/A					P8
VCC	Power	N/A					N7
VCC	Power	N/A					J7
VCC	Power	N/A					R7
VCC	Power	N/A					K8
VCC	Power	N/A					L7
VCC	Power	N/A					P10
VCC	Power	N/A					T10
VCC	Power	N/A					J9
VCC	Power	N/A					M8
VCCLDO	Power	N/A					B7
VCCLDO	Power	N/A					B9
VCCLDO	Power	N/A					B5
VCCLDO	Power	N/A					B11
VCCLDO	Power	N/A					C4
VCCLDO	Power	N/A					C8
VCCIO10	Power	N/A					F12
VCCIO10	Power	N/A					T12
VCCX	Power	N/A					P12
VCCX	Power	N/A					M12
VCCX	Power	N/A					R11
VCCIO2	Power	N/A					B14

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
VCCIO2	Power	N/A					D18
VCCIO2	Power	N/A					E15
VCCIO2	Power	N/A					F22
VCCIO2	Power	N/A					A17
VCCIO2	Power	N/A					C21
VCCIO3	Power	N/A					L17
VCCIO3	Power	N/A					J13
VCCIO3	Power	N/A					H16
VCCIO3	Power	N/A					G19
VCCIO3	Power	N/A					K20
VCCIO3	Power	N/A					N21
VCCIO4	Power	N/A					U19
VCCIO4	Power	N/A					Y20
VCCIO4	Power	N/A					T22
VCCIO4	Power	N/A					M14
VCCIO4	Power	N/A					P18
VCCIO4	Power	N/A					R15
VCCIO5	Power	N/A					W13
VCCIO5	Power	N/A					V16
VCCIO5	Power	N/A					AA17
VCCIO5	Power	N/A					AB14
VCCIO5	Power	N/A					Y10
VCCIO6	Power	N/A					AA7
VCCIO6	Power	N/A					V6
VCCIO6	Power	N/A					W3
VCCIO6	Power	N/A					T2
VCCIO6	Power	N/A					R5
VCCIO6	Power	N/A					AB4
VCCIO7	Power	N/A					C1
VCCIO7	Power	N/A					J3
VCCIO7	Power	N/A					N1

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
VCCIO7	Power	N/A					M4
VCCIO7	Power	N/A					H6
VCCIO7	Power	N/A					F2
VSS	Ground	N/A					K9
VSS	Ground	N/A					D8
VSS	Ground	N/A					A2
VSS	Ground	N/A					A3
VSS	Ground	N/A					A5
VSS	Ground	N/A					A7
VSS	Ground	N/A					A9
VSS	Ground	N/A					A11
VSS	Ground	N/A					A12
VSS	Ground	N/A					A22
VSS	Ground	N/A					AA2
VSS	Ground	N/A					AA12
VSS	Ground	N/A					AA22
VSS	Ground	N/A					AB9
VSS	Ground	N/A					AB19
VSS	Ground	N/A					B3
VSS	Ground	N/A					B12
VSS	Ground	N/A					B19
VSS	Ground	N/A					C3
VSS	Ground	N/A					C6
VSS	Ground	N/A					C10
VSS	Ground	N/A					C12
VSS	Ground	N/A					C16
VSS	Ground	N/A					D3
VSS	Ground	N/A					D4
VSS	Ground	N/A					D12
VSS	Ground	N/A					D13
VSS	Ground	N/A					E4

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
VSS	Ground	N/A					E5
VSS	Ground	N/A					E7
VSS	Ground	N/A					E9
VSS	Ground	N/A					E11
VSS	Ground	N/A					E20
VSS	Ground	N/A					F5
VSS	Ground	N/A					F11
VSS	Ground	N/A					F17
VSS	Ground	N/A					G5
VSS	Ground	N/A					G6
VSS	Ground	N/A					G7
VSS	Ground	N/A					G8
VSS	Ground	N/A					G9
VSS	Ground	N/A					G10
VSS	Ground	N/A					G12
VSS	Ground	N/A					G14
VSS	Ground	N/A					H1
VSS	Ground	N/A					H7
VSS	Ground	N/A					H9
VSS	Ground	N/A					H11
VSS	Ground	N/A					H21
VSS	Ground	N/A					J8
VSS	Ground	N/A					J10
VSS	Ground	N/A					J12
VSS	Ground	N/A					J18
VSS	Ground	N/A					K5
VSS	Ground	N/A					K7
VSS	Ground	N/A					K11
VSS	Ground	N/A					K15
VSS	Ground	N/A					L2
VSS	Ground	N/A					L8

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
VSS	Ground	N/A					L22
VSS	Ground	N/A					M7
VSS	Ground	N/A					M11
VSS	Ground	N/A					M19
VSS	Ground	N/A					N6
VSS	Ground	N/A					N8
VSS	Ground	N/A					N16
VSS	Ground	N/A					P3
VSS	Ground	N/A					P7
VSS	Ground	N/A					P9
VSS	Ground	N/A					P11
VSS	Ground	N/A					P13
VSS	Ground	N/A					R8
VSS	Ground	N/A					R10
VSS	Ground	N/A					R12
VSS	Ground	N/A					R20
VSS	Ground	N/A					T7
VSS	Ground	N/A					T9
VSS	Ground	N/A					T11
VSS	Ground	N/A					T17
VSS	Ground	N/A					U4
VSS	Ground	N/A					U14
VSS	Ground	N/A					V1
VSS	Ground	N/A					V11
VSS	Ground	N/A					V21
VSS	Ground	N/A					W8
VSS	Ground	N/A					W18
VSS	Ground	N/A					Y5
VSS	Ground	N/A					Y15
NC	N/A	N/A					M10
NC	N/A	N/A					L9

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
NC	N/A	N/A					E12
NC	N/A	N/A					F8
NC	N/A	N/A					J11
NC	N/A	N/A					L11
NC	N/A	N/A					N11
NC	N/A	N/A					K10

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOB102A/MCS_N	I/O	4	DQ12	MCS_N	True_of_IOB102B	True	T19
IOB102B/D08/SO	I/O	4	DQ12	D08/SO	Comp_of_IOB102A	True	T20
IOB104A/PUDC_B	I/O	4	DQS12	PUDC_B	True_of_IOB104B	True	U22
IOB104B/EMCCLK	I/O	4	DQS12	EMCCLK	Comp_of_IOB104A	True	V22
IOB106A/CSI_B	I/O	4	DQ13	CSI_B	True_of_IOB106B	True	V17
IOB106B/D31	I/O	4	DQ13	D31	Comp_of_IOB106A	True	W17
IOB108A/D30	I/O	4	DQ13	D30	True_of_IOB108B	True	AA18
IOB108B/D29	I/O	4	DQ13	D29	Comp_of_IOB108A	True	AB18
IOB110A/RDWR	I/O	4	DQS13	RDWR	True_of_IOB110B	True	AA19
IOB110B/DOUT_CSO_B	I/O	4	DQS13	DOUT_CSO_B	Comp_of_IOB110A	True	AB20
IOB112A/D28	I/O	4	DQ13	D28	True_of_IOB112B	True	U17
IOB112B/D27	I/O	4	DQ13	D27	Comp_of_IOB112A	True	U18
IOB114A/SGCLKT_4/BPLL2_T_FB1/ BPLL3_T_FB1	I/O	4	DQ13	SGCLKT_4/BPLL2_T_FB1/BPL L3_T_FB1	True_of_IOB114B	True	V18
IOB114B/SGCLKC_4/BPLL2_C_FB1/ BPLL3_C_FB1	I/O	4	DQ13	SGCLKC_4/BPLL2_C_FB1/BPL L3_C_FB1	Comp_of_IOB114A	True	V19
IOB116A/MGCLKT_4/BPLL2_T_FB0/ BPLL3_T_FB0	I/O	4	DQ13	MGCLKT_4/BPLL2_T_FB0/BPL L3_T_FB0	True_of_IOB116B	True	Y18
IOB116B/MGCLKC_4/BPLL2_C_FB 0/BPLL3_C_FB0	I/O	4	DQ13	MGCLKC_4/BPLL2_C_FB0/BPL L3_C_FB0	Comp_of_IOB116A	True	Y19
IOB120A/SGCLKT_5/BPLL2_T_IN0/ BPLL3_T_IN0	I/O	4	DQ14	SGCLKT_5/BPLL2_T_IN0/BPLL 3_T_IN0	True_of_IOB120B	True	U20
IOB120B/SGCLKC_5/BPLL2_C_IN0/ BPLL3_C_IN0	I/O	4	DQ14	SGCLKC_5/BPLL2_C_IN0/BPLL 3_C_IN0	Comp_of_IOB120A	True	V20
IOB122A/MGCLKT_5/BPLL2_T_IN1/ BPLL3_T_IN1	I/O	4	DQ14	MGCLKT_5/BPLL2_T_IN1/BPLL 3_T_IN1	True_of_IOB122B	True	W19
IOB122B/MGCLKC_5/BPLL2_C_IN1/ BPLL3_C_IN1	I/O	4	DQ14	MGCLKC_5/BPLL2_C_IN1/BPL L3_C_IN1	Comp_of_IOB122A	True	W20
IOB124A/D09	I/O	4	DQ14	D09	True_of_IOB124B	True	W21
IOB124B/D10	I/O	4	DQ14	D10	Comp_of_IOB124A	True	W22
IOB126A/D11	I/O	4	DQ14	D11	True_of_IOB126B	True	AA20

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOB126B/D12	I/O	4	DQ14	D12	Comp_of_IOB126A	True	AA21
IOB129A/D14	I/O	4	DQ14	D14	True_of_IOB129B	True	AB21
IOB129B/D15	I/O	4	DQ14	D15	Comp_of_IOB129A	True	AB22
IOB131A/SSPI_CS_N	I/O	4	DQS14	SSPI_CS_N	True_of_IOB131B	True	Y21
IOB131B/D13	I/O	4	DQS14	D13	Comp_of_IOB131A	True	Y22
IOB133A/D26	I/O	4	DQ15	D26	True_of_IOB133B	True	P14
IOB133B/D25	I/O	4	DQ15	D25	Comp_of_IOB133A	True	R14
IOB135A/CLKHOLD_N	I/O	4	DQS15	CLKHOLD_N	True_of_IOB135B	True	N17
IOB135B/D22	I/O	4	DQS15	D22	Comp_of_IOB135A	True	P17
IOB138A/D24	I/O	4	DQ15	D24	True_of_IOB138B	True	R18
IOB138B/D23	I/O	4	DQ15	D23	Comp_of_IOB138A	True	T18
IOB140A/D21	I/O	4	DQ15	D21	True_of_IOB140B	True	P15
IOB140B/D20	I/O	4	DQ15	D20	Comp_of_IOB140A	True	R16
IOB142A/D19	I/O	4	DQ15	D19	True_of_IOB142B	True	N13
IOB142B/D18	I/O	4	DQ15	D18	Comp_of_IOB142A	True	N14
IOB144A/D17	I/O	4	DQ15	D17	True_of_IOB144B	True	P16
IOB144B/D16	I/O	4	DQ15	D16	Comp_of_IOB144A	True	R17
IOB146A	I/O	4	none			none	N15
IOB169A/TDO	I/O	10	none	TDO	True_of_IOB169B	True	U13
IOB169B/TMS	I/O	10	none	TMS	Comp_of_IOB169A	True	T13
IOB171A/READY	I/O	10	none	READY	True_of_IOB171B	True	U12
IOB171B/DONE	I/O	10	none	DONE	Comp_of_IOB171A	True	G11
IOB173A/TCK	I/O	10	none	TCK	True_of_IOB173B	True	V12
IOB173B/TDI	I/O	10	none	TDI	Comp_of_IOB173A	True	R13
IOB175A/MODE0	I/O	10	none	MODE0	True_of_IOB175B	True	U11
IOB175B/CCLK	I/O	10	none	CCLK	Comp_of_IOB175A	True	L12
IOB177A/MODE1	I/O	10	none	MODE1	True_of_IOB177B	True	U10
IOB177B/MODE2	I/O	10	none	MODE2	Comp_of_IOB177A	True	U9
IOB179A/CFGBVS	I/O	10	none	CFGBVS	True_of_IOB179B	True	U8
IOB179B/RECONFIG_N	I/O	10	none	RECONFIG_N	Comp_of_IOB179A	True	N12
IOB51A	I/O	5	DQ9		True_of_IOB51B	True	AB11

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOB51B	I/O	5	DQ9		Comp_of_IOB51A	True	AB12
IOB53A	I/O	5	DQ9		True_of_IOB53B	True	AA9
IOB53B	I/O	5	DQ9		Comp_of_IOB53A	True	AB10
IOB56A	I/O	5	DQS9		True_of_IOB56B	True	AA10
IOB56B	I/O	5	DQS9		Comp_of_IOB56A	True	AA11
IOB58A	I/O	5	DQ9		True_of_IOB58B	True	V10
IOB58B	I/O	5	DQ9		Comp_of_IOB58A	True	W10
IOB60A/SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	I/O	5	DQ9	SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	True_of_IOB60B	True	Y11
IOB60B/SGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	I/O	5	DQ9	SGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	Comp_of_IOB60A	True	Y12
IOB62A/MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	I/O	5	DQ9	MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	True_of_IOB62B	True	W11
IOB62B/MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	I/O	5	DQ9	MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	Comp_of_IOB62A	True	W12
IOB66A/MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	I/O	5	DQ10	MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	True_of_IOB66B	True	V13
IOB66B/MGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	I/O	5	DQ10	MGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	Comp_of_IOB66A	True	V14
IOB68A/SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	I/O	5	DQ10	SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	True_of_IOB68B	True	U15
IOB68B/SGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	I/O	5	DQ10	SGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	Comp_of_IOB68A	True	V15
IOB70A	I/O	5	DQS10		True_of_IOB70B	True	T14
IOB70B	I/O	5	DQS10		Comp_of_IOB70A	True	T15
IOB72A	I/O	5	DQ10		True_of_IOB72B	True	W15
IOB72B	I/O	5	DQ10		Comp_of_IOB72A	True	W16
IOB76A	I/O	5	DQ10		True_of_IOB76B	True	T16
IOB76B	I/O	5	DQ10		Comp_of_IOB76A	True	U16
IOB78A	I/O	5	DQ11		True_of_IOB78B	True	Y16
IOB78B	I/O	5	DQ11		Comp_of_IOB78A	True	AA16

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOB80A	I/O	5	DQ11		True_of_IOB80B	True	AB16
IOB80B	I/O	5	DQ11		Comp_of_IOB80A	True	AB17
IOB83A	I/O	5	DQ11		True_of_IOB83B	True	AA15
IOB83B	I/O	5	DQ11		Comp_of_IOB83A	True	AB15
IOB85A	I/O	5	DQ11		True_of_IOB85B	True	W14
IOB85B	I/O	5	DQ11		Comp_of_IOB85A	True	Y14
IOB87A	I/O	5	DQ11		True_of_IOB87B	True	Y13
IOB87B	I/O	5	DQ11		Comp_of_IOB87A	True	AA14
IOB89A	I/O	5	DQS11		True_of_IOB89B	True	AA13
IOB89B	I/O	5	DQS11		Comp_of_IOB89A	True	AB13
IOB91A	I/O	5	none			none	Y17
IOB92A	I/O	4	none			none	P20
IOB93A/D00/MOSI	I/O	4	DQ12	D00/MOSI	True_of_IOB93B	True	P22
IOB93B/D01/DIN	I/O	4	DQ12	D01/DIN	Comp_of_IOB93A	True	R22
IOB95A/D02	I/O	4	DQ12	D02	True_of_IOB95B	True	P21
IOB95B/D03	I/O	4	DQ12	D03	Comp_of_IOB95A	True	R21
IOB97A/D04	I/O	4	DQ12	D04	True_of_IOB97B	True	T21
IOB97B/D05/SI	I/O	4	DQ12	D05/SI	Comp_of_IOB97A	True	U21
IOB99A/D06/SSPI_CLK	I/O	4	DQ12	D06/SSPI_CLK	True_of_IOB99B	True	P19
IOB99B/D07/SSPI_WPN	I/O	4	DQ12	D07/SSPI_WPN	Comp_of_IOB99A	True	R19
IOL101A	I/O	6	DQ7		True_of_IOL101B	True	AA8
IOL101B	I/O	6	DQ7		Comp_of_IOL101A	True	AB8
IOL103A	I/O	6	DQ7		True_of_IOL103B	True	Y8
IOL103B	I/O	6	DQ7		Comp_of_IOL103A	True	Y7
IOL105A	I/O	6	DQ7		True_of_IOL105B	True	W9
IOL105B	I/O	6	DQ7		Comp_of_IOL105A	True	Y9
IOL107A	I/O	6	DQ7		True_of_IOL107B	True	V7
IOL107B	I/O	6	DQ7		Comp_of_IOL107A	True	W7
IOL109A	I/O	6	none			none	U7
IOL1A	I/O	7	none			none	F4
IOL20A	I/O	7	DQ1		True_of_IOL20B	True	J5

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOL20B	I/O	7	DQ1		Comp_of_IOL20A	True	H5
IOL24A/MGCLKT_11/LPLL0_T_IN1/ LPLL1_T_IN1	I/O	7	DQ1	MGCLKT_11/LPLL0_T_IN1/LPL L1_T_IN1	True_of_IOL24B	True	H4
IOL24B/MGCLKC_11/LPLL0_C_IN1/ LPLL1_C_IN1	I/O	7	DQ1	MGCLKC_11/LPLL0_C_IN1/LPL L1_C_IN1	Comp_of_IOL24A	True	G4
IOL29A/MGCLKT_10/LPLL0_T_FB1/ LPLL1_T_FB1	I/O	7	DQ2	MGCLKT_10/LPLL0_T_FB1/LPL L1_T_FB1	True_of_IOL29B	True	K4
IOL29B/MGCLKC_10/LPLL0_C_FB1/ LPLL1_C_FB1	I/O	7	DQ2	MGCLKC_10/LPLL0_C_FB1/LP LL1_C_FB1	Comp_of_IOL29A	True	J4
IOL31A/SGCLKT_10/LPLL0_T_FB0/ LPLL1_T_FB0	I/O	7	DQ2	SGCLKT_10/LPLL0_T_FB0/LPL L1_T_FB0	True_of_IOL31B	True	L3
IOL31B/SGCLKC_10/LPLL0_C_FB0/ LPLL1_C_FB0	I/O	7	DQ2	SGCLKC_10/LPLL0_C_FB0/LP LL1_C_FB0	Comp_of_IOL31A	True	K3
IOL33A	I/O	7	DQ2		True_of_IOL33B	True	M3
IOL33B	I/O	7	DQ2		Comp_of_IOL33A	True	M2
IOL35A	I/O	7	DQS2		True_of_IOL35B	True	M1
IOL35B	I/O	7	DQS2		Comp_of_IOL35A	True	L1
IOL38A	I/O	7	DQ2		True_of_IOL38B	True	K6
IOL38B	I/O	7	DQ2		Comp_of_IOL38A	True	J6
IOL40A	I/O	7	DQ2		True_of_IOL40B	True	L5
IOL40B	I/O	7	DQ2		Comp_of_IOL40A	True	L4
IOL42A	I/O	7	DQ3		True_of_IOL42B	True	N4
IOL42B	I/O	7	DQ3		Comp_of_IOL42A	True	N3
IOL44A	I/O	7	DQ3		True_of_IOL44B	True	R1
IOL44B	I/O	7	DQ3		Comp_of_IOL44A	True	P1
IOL47A	I/O	7	DQ3		True_of_IOL47B	True	P2
IOL47B	I/O	7	DQ3		Comp_of_IOL47A	True	N2
IOL49A	I/O	7	DQS3		True_of_IOL49B	True	P5
IOL49B	I/O	7	DQS3		Comp_of_IOL49A	True	P4
IOL51A	I/O	7	DQ3		True_of_IOL51B	True	M6
IOL51B	I/O	7	DQ3		Comp_of_IOL51A	True	M5

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOL53A	I/O	7	DQ3		True_of_IOL53B	True	P6
IOL53B	I/O	7	DQ3		Comp_of_IOL53A	True	N5
IOL55A	I/O	7	none			none	L6
IOL56A	I/O	6	DQ4		True_of_IOL56B	True	T1
IOL56B	I/O	6	DQ4		Comp_of_IOL56A	True	U1
IOL58A	I/O	6	DQ4		True_of_IOL58B	True	U2
IOL58B	I/O	6	DQ4		Comp_of_IOL58A	True	V2
IOL60A	I/O	6	DQ4		True_of_IOL60B	True	U3
IOL60B	I/O	6	DQ4		Comp_of_IOL60A	True	V3
IOL62A	I/O	6	DQ4		True_of_IOL62B	True	W2
IOL62B	I/O	6	DQ4		Comp_of_IOL62A	True	Y2
IOL65A	I/O	6	DQS4		True_of_IOL65B	True	R3
IOL65B	I/O	6	DQS4		Comp_of_IOL65A	True	R2
IOL67A	I/O	6	DQ4		True_of_IOL67B	True	W1
IOL67B	I/O	6	DQ4		Comp_of_IOL67A	True	Y1
IOL69A	I/O	6	DQS5		True_of_IOL69B	True	Y3
IOL69B	I/O	6	DQS5		Comp_of_IOL69A	True	AA3
IOL71A	I/O	6	DQ5		True_of_IOL71B	True	AA1
IOL71B	I/O	6	DQ5		Comp_of_IOL71A	True	AB1
IOL73A	I/O	6	none			none	T3
IOL74A	I/O	6	DQ5		True_of_IOL74B	True	AB3
IOL74B	I/O	6	DQ5		Comp_of_IOL74A	True	AB2
IOL76A	I/O	6	DQ5		True_of_IOL76B	True	AA5
IOL76B	I/O	6	DQ5		Comp_of_IOL76A	True	AB5
IOL78A/SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	I/O	6	DQ5	SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	True_of_IOL78B	True	Y4
IOL78B/SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	I/O	6	DQ5	SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	Comp_of_IOL78A	True	AA4
IOL80A/MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	I/O	6	DQ5	MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	True_of_IOL80B	True	V4

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOL80B/MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	I/O	6	DQ5	MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	Comp_of_IOL80A	True	W4
IOL83A/MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	I/O	6	DQ6	MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	True_of_IOL83B	True	R4
IOL83B/MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	I/O	6	DQ6	MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	Comp_of_IOL83A	True	T4
IOL85A/SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	I/O	6	DQ6	SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	True_of_IOL85B	True	T5
IOL85B/SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	I/O	6	DQ6	SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	Comp_of_IOL85A	True	U5
IOL87A	I/O	6	DQ6		True_of_IOL87B	True	U6
IOL87B	I/O	6	DQ6		Comp_of_IOL87A	True	V5
IOL89A	I/O	6	DQ6		True_of_IOL89B	True	R6
IOL89B	I/O	6	DQ6		Comp_of_IOL89A	True	T6
IOL92A	I/O	6	DQ6		True_of_IOL92B	True	Y6
IOL92B	I/O	6	DQ6		Comp_of_IOL92A	True	AA6
IOL94A	I/O	6	DQS6		True_of_IOL94B	True	W6
IOL94B	I/O	6	DQS6		Comp_of_IOL94A	True	W5
IOL96A	I/O	6	DQS7		True_of_IOL96B	True	V9
IOL96B	I/O	6	DQS7		Comp_of_IOL96A	True	V8
IOL98A	I/O	6	DQ7		True_of_IOL98B	True	AB7
IOL98B	I/O	6	DQ7		Comp_of_IOL98A	True	AB6
IOR101A	I/O	3	DQ16		True_of_IOR101B	True	J15
IOR101B	I/O	3	DQ16		Comp_of_IOR101A	True	H15
IOR103A	I/O	3	DQS16		True_of_IOR103B	True	J14
IOR103B	I/O	3	DQS16		Comp_of_IOR103A	True	H14
IOR105A	I/O	3	DQ16		True_of_IOR105B	True	G15
IOR105B	I/O	3	DQ16		Comp_of_IOR105A	True	G16
IOR107A/ADCINCK1	I/O	3	DQ16	ADCINCK1	True_of_IOR107B	True	G17
IOR107B	I/O	3	DQ16		Comp_of_IOR107A	True	G18
IOR109A	I/O	3	none			none	J16

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOR11A	I/O	2	DQ23		True_of_IOR11B	True	F13
IOR11B	I/O	2	DQ23		Comp_of_IOR11A	True	F14
IOR13A	I/O	2	DQ23		True_of_IOR13B	True	F16
IOR13B	I/O	2	DQ23		Comp_of_IOR13A	True	E17
IOR15A	I/O	2	DQ22		True_of_IOR15B	True	A13
IOR15B	I/O	2	DQ22		Comp_of_IOR15A	True	A14
IOR17A	I/O	2	DQ22		True_of_IOR17B	True	C13
IOR17B	I/O	2	DQ22		Comp_of_IOR17A	True	B13
IOR1A	I/O	2	none			none	F15
IOR20A	I/O	2	DQS22		True_of_IOR20B	True	A15
IOR20B	I/O	2	DQS22		Comp_of_IOR20A	True	A16
IOR22A	I/O	2	DQ22		True_of_IOR22B	True	B15
IOR22B	I/O	2	DQ22		Comp_of_IOR22A	True	B16
IOR24A/SGCLKT_1/RPLL0_T_FB0/ RPLL1_T_FB0	I/O	2	DQ22	SGCLKT_1/RPLL0_T_FB0/RPL L1_T_FB0	True_of_IOR24B	True	B17
IOR24B/SGCLKC_1/RPLL0_C_FB0/ RPLL1_C_FB0	I/O	2	DQ22	SGCLKC_1/RPLL0_C_FB0/RPL L1_C_FB0	Comp_of_IOR24A	True	B18
IOR26A/MGCLKT_1/RPLL0_T_FB1/ RPLL1_T_FB1	I/O	2	DQ22	MGCLKT_1/RPLL0_T_FB1/RPL L1_T_FB1	True_of_IOR26B	True	D17
IOR26B/MGCLKC_1/RPLL0_C_FB1/ RPLL1_C_FB1	I/O	2	DQ22	MGCLKC_1/RPLL0_C_FB1/RPL L1_C_FB1	Comp_of_IOR26A	True	C17
IOR29A/SGCLKT_0/RPLL0_T_IN0/R PLL1_T_IN0	I/O	2	DQ21	SGCLKT_0/RPLL0_T_IN0/RPLL 1_T_IN0	True_of_IOR29B	True	E19
IOR29B/SGCLKC_0/RPLL0_C_IN0/ RPLL1_C_IN0	I/O	2	DQ21	SGCLKC_0/RPLL0_C_IN0/RPL L1_C_IN0	Comp_of_IOR29A	True	D19
IOR2A	I/O	2	DQ23		True_of_IOR2B	True	E13
IOR2B	I/O	2	DQ23		Comp_of_IOR2A	True	E14
IOR31A/MGCLKT_0/RPLL0_T_IN1/ RPLL1_T_IN1	I/O	2	DQ21	MGCLKT_0/RPLL0_T_IN1/RPL L1_T_IN1	True_of_IOR31B	True	C18

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOR31B/MGCLKC_0/RPLL0_C_IN1/ RPLL1_C_IN1	I/O	2	DQ21	MGCLKC_0/RPLL0_C_IN1/RPL L1_C_IN1	Comp_of_IOR31A	True	C19
IOR33A	I/O	2	DQ21		True_of_IOR33B	True	F19
IOR33B	I/O	2	DQ21		Comp_of_IOR33A	True	F20
IOR35A	I/O	2	DQS21		True_of_IOR35B	True	F18
IOR35B	I/O	2	DQS21		Comp_of_IOR35A	True	E18
IOR38A	I/O	2	DQ21		True_of_IOR38B	True	A18
IOR38B	I/O	2	DQ21		Comp_of_IOR38A	True	A19
IOR40A	I/O	2	DQ21		True_of_IOR40B	True	B20
IOR40B	I/O	2	DQ21		Comp_of_IOR40A	True	A20
IOR42A	I/O	2	DQ20		True_of_IOR42B	True	C22
IOR42B	I/O	2	DQ20		Comp_of_IOR42A	True	B22
IOR44A	I/O	2	DQS20		True_of_IOR44B	True	B21
IOR44B	I/O	2	DQS20		Comp_of_IOR44A	True	A21
IOR47A	I/O	2	DQ20		True_of_IOR47B	True	D20
IOR47B	I/O	2	DQ20		Comp_of_IOR47A	True	C20
IOR49A	I/O	2	DQ20		True_of_IOR49B	True	E22
IOR49B	I/O	2	DQ20		Comp_of_IOR49A	True	D22
IOR4A	I/O	2	DQ23		True_of_IOR4B	True	D14
IOR4B	I/O	2	DQ23		Comp_of_IOR4A	True	D15
IOR51A	I/O	2	DQ20		True_of_IOR51B	True	E21
IOR51B	I/O	2	DQ20		Comp_of_IOR51A	True	D21
IOR53A	I/O	2	DQ20		True_of_IOR53B	True	G21
IOR53B	I/O	2	DQ20		Comp_of_IOR53A	True	G22
IOR55A	I/O	2	none			none	F21
IOR56A	I/O	3	DQ19		True_of_IOR56B	True	M13
IOR56B	I/O	3	DQ19		Comp_of_IOR56A	True	L13
IOR58A	I/O	3	DQS19		True_of_IOR58B	True	K17
IOR58B	I/O	3	DQS19		Comp_of_IOR58A	True	J17
IOR60A	I/O	3	DQ19		True_of_IOR60B	True	K13
IOR60B	I/O	3	DQ19		Comp_of_IOR60A	True	K14

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOR62A	I/O	3	DQ19		True_of_IOR62B	True	L14
IOR62B	I/O	3	DQ19		Comp_of_IOR62A	True	L15
IOR65A	I/O	3	DQ19		True_of_IOR65B	True	L16
IOR65B	I/O	3	DQ19		Comp_of_IOR65A	True	K16
IOR67A	I/O	3	DQ19		True_of_IOR67B	True	M15
IOR67B	I/O	3	DQ19		Comp_of_IOR67A	True	M16
IOR69A	I/O	3	DQ18		True_of_IOR69B	True	H20
IOR69B	I/O	3	DQ18		Comp_of_IOR69A	True	G20
IOR6A	I/O	2	DQ23		True_of_IOR6B	True	E16
IOR6B	I/O	2	DQ23		Comp_of_IOR6A	True	D16
IOR71A	I/O	3	DQ18		True_of_IOR71B	True	M21
IOR71B	I/O	3	DQ18		Comp_of_IOR71A	True	L21
IOR73A	I/O	3	none			none	M17
IOR74A	I/O	3	DQ18		True_of_IOR74B	True	J22
IOR74B	I/O	3	DQ18		Comp_of_IOR74A	True	H22
IOR76A	I/O	3	DQS18		True_of_IOR76B	True	K21
IOR76B	I/O	3	DQS18		Comp_of_IOR76A	True	K22
IOR78A/SGCLKT_3/RPLL2_T_FB0/ RPLL3_T_FB0	I/O	3	DQ18	SGCLKT_3/RPLL2_T_FB0/RPL L3_T_FB0	True_of_IOR78B	True	J20
IOR78B/SGCLKC_3/RPLL2_C_FB0/ RPLL3_C_FB0	I/O	3	DQ18	SGCLKC_3/RPLL2_C_FB0/RPL L3_C_FB0	Comp_of_IOR78A	True	J21
IOR80A/MGCLKT_3/RPLL2_T_FB1/ RPLL3_T_FB1	I/O	3	DQ18	MGCLKT_3/RPLL2_T_FB1/RPL L3_T_FB1	True_of_IOR80B	True	J19
IOR80B/MGCLKC_3/RPLL2_C_FB1/ RPLL3_C_FB1	I/O	3	DQ18	MGCLKC_3/RPLL2_C_FB1/RPL L3_C_FB1	Comp_of_IOR80A	True	H19
IOR83A/MGCLKT_2/RPLL2_T_IN1/ RPLL3_T_IN1	I/O	3	DQ17	MGCLKT_2/RPLL2_T_IN1/RPL L3_T_IN1	True_of_IOR83B	True	K18
IOR83B/MGCLKC_2/RPLL2_C_IN1/ RPLL3_C_IN1	I/O	3	DQ17	MGCLKC_2/RPLL2_C_IN1/RPL L3_C_IN1	Comp_of_IOR83A	True	K19
IOR85A/SGCLKT_2/RPLL2_T_IN0/R PLL3_T_IN0	I/O	3	DQ17	SGCLKT_2/RPLL2_T_IN0/RPLL 3_T_IN0	True_of_IOR85B	True	L19

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOR85B/SGCLKC_2/RPLL2_C_IN0/ RPLL3_C_IN0	I/O	3	DQ17	SGCLKC_2/RPLL2_C_IN0/RPL L3_C_IN0	Comp_of_IOR85A	True	L20
IOR87A	I/O	3	DQ17		True_of_IOR87B	True	M18
IOR87B	I/O	3	DQ17		Comp_of_IOR87A	True	L18
IOR89A	I/O	3	DQS17		True_of_IOR89B	True	N22
IOR89B	I/O	3	DQS17		Comp_of_IOR89A	True	M22
IOR8A	I/O	2	DQS23		True_of_IOR8B	True	C14
IOR8B	I/O	2	DQS23		Comp_of_IOR8A	True	C15
IOR92A	I/O	3	DQ17		True_of_IOR92B	True	N18
IOR92B	I/O	3	DQ17		Comp_of_IOR92A	True	N19
IOR94A	I/O	3	DQ17		True_of_IOR94B	True	N20
IOR94B	I/O	3	DQ17		Comp_of_IOR94A	True	M20
IOR96A	I/O	3	DQ16		True_of_IOR96B	True	H17
IOR96B	I/O	3	DQ16		Comp_of_IOR96A	True	H18
IOR98A	I/O	3	DQ16		True_of_IOR98B	True	H13
IOR98B	I/O	3	DQ16		Comp_of_IOR98A	True	G13
ADCTN	DIO	ADC	none			none	N9
ADCTP	DIO	ADC	none			none	N10
ADCVN	DIO	ADC	none			none	M9
ADCVP	DIO	ADC	none			none	L10
M0_CKN	DIO	MIPI	none			none	D1
M0_CKP	DIO	MIPI	none			none	E1
M0_D0N	DIO	MIPI	none			none	A1
M0_D0P	DIO	MIPI	none			none	B1
M0_D1N	DIO	MIPI	none			none	B2
M0_D1P	DIO	MIPI	none			none	C2
M0_D2N	DIO	MIPI	none			none	F1
M0_D2P	DIO	MIPI	none			none	G1
M0_D3N	DIO	MIPI	none			none	J1
M0_D3P	DIO	MIPI	none			none	K1

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
M1_CKN	DIO	MIPI	none			none	E3
M1_CKP	DIO	MIPI	none			none	F3
M1_D0N	DIO	MIPI	none			none	D2
M1_D0P	DIO	MIPI	none			none	E2
M1_D1N	DIO	MIPI	none			none	G2
M1_D1P	DIO	MIPI	none			none	H2
M1_D2N	DIO	MIPI	none			none	G3
M1_D2P	DIO	MIPI	none			none	H3
M1_D3N	DIO	MIPI	none			none	J2
M1_D3P	DIO	MIPI	none			none	K2
Q0_LN0_RXM_I	DIO	Q0	none			none	A8
Q0_LN0_RXP_I	DIO	Q0	none			none	B8
Q0_LN0_TXM_O	DIO	Q0	none			none	A4
Q0_LN0_TXP_O	DIO	Q0	none			none	B4
Q0_LN1_RXM_I	DIO	Q0	none			none	C9
Q0_LN1_RXP_I	DIO	Q0	none			none	D9
Q0_LN1_TXM_O	DIO	Q0	none			none	C7
Q0_LN1_TXP_O	DIO	Q0	none			none	D7
Q0_LN2_RXM_I	DIO	Q0	none			none	A10
Q0_LN2_RXP_I	DIO	Q0	none			none	B10
Q0_LN2_TXM_O	DIO	Q0	none			none	A6
Q0_LN2_TXP_O	DIO	Q0	none			none	B6
Q0_LN3_RXM_I	DIO	Q0	none			none	C11
Q0_LN3_RXP_I	DIO	Q0	none			none	D11
Q0_LN3_TXM_O	DIO	Q0	none			none	C5
Q0_LN3_TXP_O	DIO	Q0	none			none	D5
Q0_REFCLKM_0	DIO	Q0	none			none	E6
Q0_REFCLKM_1	DIO	Q0	none			none	E10
Q0_REFCLKP_0	DIO	Q0	none			none	F6
Q0_REFCLKP_1	DIO	Q0	none			none	F10

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
VDDAQ0	Power	N/A					D10
VDDAQ0	Power	N/A					D6
VDDAQ0	Power	N/A					F7
VDDTQ0	Power	N/A					E8
VDDTQ0	Power	N/A					F9
VDDHAQ0	Power	N/A					K12
VDDHAQ0	Power	N/A					H12
VDDXM	Power	N/A					K10
VDDAM_VDDDM	Power	N/A					J7
VDDAM_VDDDM	Power	N/A					L7
VCC	Power	N/A					L11
VCC	Power	N/A					K8
VCC	Power	N/A					P8
VCC	Power	N/A					H8
VCC	Power	N/A					N11
VCC	Power	N/A					M8
VCC	Power	N/A					P10
VCC	Power	N/A					T8
VCC	Power	N/A					J9
VCC	Power	N/A					R7
VCC	Power	N/A					R9
VCC	Power	N/A					N7
VCC	Power	N/A					H10
VCC	Power	N/A					T10
VCCLDO	Power	N/A					C8
VCCLDO	Power	N/A					B7
VCCLDO	Power	N/A					B9
VCCLDO	Power	N/A					B11
VCCLDO	Power	N/A					C4
VCCLDO	Power	N/A					B5
VCCIO10	Power	N/A					F12

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
VCCIO10	Power	N/A					T12
VCCX	Power	N/A					M12
VCCX	Power	N/A					P12
VCCX	Power	N/A					R11
VCCIO2	Power	N/A					C21
VCCIO2	Power	N/A					B14
VCCIO2	Power	N/A					A17
VCCIO2	Power	N/A					F22
VCCIO2	Power	N/A					E15
VCCIO2	Power	N/A					D18
VCCIO3	Power	N/A					K20
VCCIO3	Power	N/A					H16
VCCIO3	Power	N/A					J13
VCCIO3	Power	N/A					G19
VCCIO3	Power	N/A					N21
VCCIO3	Power	N/A					L17
VCCIO4	Power	N/A					Y20
VCCIO4	Power	N/A					R15
VCCIO4	Power	N/A					M14
VCCIO4	Power	N/A					T22
VCCIO4	Power	N/A					U19
VCCIO4	Power	N/A					P18
VCCIO5	Power	N/A					Y10
VCCIO5	Power	N/A					W13
VCCIO5	Power	N/A					AA17
VCCIO5	Power	N/A					V16
VCCIO5	Power	N/A					AB14
VCCIO6	Power	N/A					R5
VCCIO6	Power	N/A					V6
VCCIO6	Power	N/A					T2
VCCIO6	Power	N/A					W3

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
VCCIO6	Power	N/A					AA7
VCCIO6	Power	N/A					AB4
VCCIO7	Power	N/A					M4
VCCIO7	Power	N/A					H6
VCCIO7	Power	N/A					C1
VCCIO7	Power	N/A					F2
VCCIO7	Power	N/A					N1
VCCIO7	Power	N/A					J3
VSS	Ground	N/A					K9
VSS	Ground	N/A					D8
VSS	Ground	N/A					A2
VSS	Ground	N/A					A3
VSS	Ground	N/A					A5
VSS	Ground	N/A					A7
VSS	Ground	N/A					A9
VSS	Ground	N/A					A11
VSS	Ground	N/A					A12
VSS	Ground	N/A					A22
VSS	Ground	N/A					AA2
VSS	Ground	N/A					AA12
VSS	Ground	N/A					AA22
VSS	Ground	N/A					AB9
VSS	Ground	N/A					AB19
VSS	Ground	N/A					B3
VSS	Ground	N/A					B12
VSS	Ground	N/A					B19
VSS	Ground	N/A					C3
VSS	Ground	N/A					C6
VSS	Ground	N/A					C10
VSS	Ground	N/A					C12
VSS	Ground	N/A					C16

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
VSS	Ground	N/A					D3
VSS	Ground	N/A					D4
VSS	Ground	N/A					D12
VSS	Ground	N/A					D13
VSS	Ground	N/A					E4
VSS	Ground	N/A					E5
VSS	Ground	N/A					E7
VSS	Ground	N/A					E9
VSS	Ground	N/A					E11
VSS	Ground	N/A					E20
VSS	Ground	N/A					F5
VSS	Ground	N/A					F11
VSS	Ground	N/A					F17
VSS	Ground	N/A					G5
VSS	Ground	N/A					G6
VSS	Ground	N/A					G7
VSS	Ground	N/A					G8
VSS	Ground	N/A					G9
VSS	Ground	N/A					G10
VSS	Ground	N/A					G12
VSS	Ground	N/A					G14
VSS	Ground	N/A					H1
VSS	Ground	N/A					H7
VSS	Ground	N/A					H9
VSS	Ground	N/A					H11
VSS	Ground	N/A					H21
VSS	Ground	N/A					J8
VSS	Ground	N/A					J10
VSS	Ground	N/A					J12
VSS	Ground	N/A					J18
VSS	Ground	N/A					K5

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
VSS	Ground	N/A					K7
VSS	Ground	N/A					K11
VSS	Ground	N/A					K15
VSS	Ground	N/A					L2
VSS	Ground	N/A					L8
VSS	Ground	N/A					L22
VSS	Ground	N/A					M7
VSS	Ground	N/A					M11
VSS	Ground	N/A					M19
VSS	Ground	N/A					N6
VSS	Ground	N/A					N8
VSS	Ground	N/A					N16
VSS	Ground	N/A					P3
VSS	Ground	N/A					P7
VSS	Ground	N/A					P9
VSS	Ground	N/A					P11
VSS	Ground	N/A					P13
VSS	Ground	N/A					R8
VSS	Ground	N/A					R10
VSS	Ground	N/A					R12
VSS	Ground	N/A					R20
VSS	Ground	N/A					T7
VSS	Ground	N/A					T9
VSS	Ground	N/A					T11
VSS	Ground	N/A					T17
VSS	Ground	N/A					U4
VSS	Ground	N/A					U14
VSS	Ground	N/A					V1
VSS	Ground	N/A					V11
VSS	Ground	N/A					V21
VSS	Ground	N/A					W8

Note! ^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
VSS	Ground	N/A					W18
VSS	Ground	N/A					Y5
VSS	Ground	N/A					Y15
NC	N/A	N/A					M10
NC	N/A	N/A					L9
NC	N/A	N/A					E12
NC	N/A	N/A					F8
NC	N/A	N/A					J11

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOB102A/MCS_N	I/O	4	DQ12	MCS_N	True_of_IOB102B	True	P18
IOB102B/D08/SO	I/O	4	DQ12	D08/SO	Comp_of_IOB102A	True	N18
IOB104A/PUDC_B	I/O	4	DQS12	PUDC_B	True_of_IOB104B	True	P15
IOB104B/EMCCLK	I/O	4	DQS12	EMCCLK	Comp_of_IOB104A	True	P16
IOB106A/CSI_B	I/O	4	DQ13	CSI_B	True_of_IOB106B	True	P19
IOB106B/D31	I/O	4	DQ13	D31	Comp_of_IOB106A	True	N19
IOB108A/D30	I/O	4	DQ13	D30	True_of_IOB108B	True	P23
IOB108B/D29	I/O	4	DQ13	D29	Comp_of_IOB108A	True	P24
IOB110A/RDWR	I/O	4	DQS13	RDWR	True_of_IOB110B	True	N23
IOB110B/DOOUT_CSO_B	I/O	4	DQS13	DOOUT_CSO_B	Comp_of_IOB110A	True	N24
IOB112A/D28	I/O	4	DQ13	D28	True_of_IOB112B	True	R20
IOB112B/D27	I/O	4	DQ13	D27	Comp_of_IOB112A	True	R21
IOB114A/SGCLKT_4/BPLL2_T_FB1/BPLL3_T_FB1	I/O	4	DQ13	SGCLKT_4/BPLL2_T_FB1/BPLL3_T_FB1	True_of_IOB114B	True	P20
IOB114B/SGCLKC_4/BPLL2_C_FB1/BPLL3_C_FB1	I/O	4	DQ13	SGCLKC_4/BPLL2_C_FB1/BPLL3_C_FB1	Comp_of_IOB114A	True	P21
IOB116A/MGCLKT_4/BPLL2_T_FB0/BPLL3_T_FB0	I/O	4	DQ13	MGCLKT_4/BPLL2_T_FB0/BPLL3_T_FB0	True_of_IOB116B	True	N21
IOB116B/MGCLKC_4/BPLL2_C_FB0/BPLL3_C_FB0	I/O	4	DQ13	MGCLKC_4/BPLL2_C_FB0/BPLL3_C_FB0	Comp_of_IOB116A	True	N22
IOB120A/SGCLKT_5/BPLL2_T_IN0/BPLL3_T_IN0	I/O	4	DQ14	SGCLKT_5/BPLL2_T_IN0/BPLL3_T_IN0	True_of_IOB120B	True	L22
IOB120B/SGCLKC_5/BPLL2_C_IN0/BPLL3_C_IN0	I/O	4	DQ14	SGCLKC_5/BPLL2_C_IN0/BPLL3_C_IN0	Comp_of_IOB120A	True	L23
IOB122A/MGCLKT_5/BPLL2_T_IN1/BPLL3_T_IN1	I/O	4	DQ14	MGCLKT_5/BPLL2_T_IN1/BPLL3_T_IN1	True_of_IOB122B	True	M21
IOB122B/MGCLKC_5/BPLL2_C_IN1/BPLL3_C_IN1	I/O	4	DQ14	MGCLKC_5/BPLL2_C_IN1/BPLL3_C_IN1	Comp_of_IOB122A	True	M22
IOB124A/D09	I/O	4	DQ14	D09	True_of_IOB124B	True	K25
IOB124B/D10	I/O	4	DQ14	D10	Comp_of_IOB124A	True	K26

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOB126A/D11	I/O	4	DQ14	D11	True_of_IOB126B	True	M20
IOB126B/D12	I/O	4	DQ14	D12	Comp_of_IOB126A	True	L20
IOB129A/D14	I/O	4	DQ14	D14	True_of_IOB129B	True	M24
IOB129B/D15	I/O	4	DQ14	D15	Comp_of_IOB129A	True	M25
IOB131A/SSPI_CS_N	I/O	4	DQS14	SSPI_CS_N	True_of_IOB131B	True	L24
IOB131B/D13	I/O	4	DQS14	D13	Comp_of_IOB131A	True	L25
IOB133A/D26	I/O	4	DQ15	D26	True_of_IOB133B	True	R25
IOB133B/D25	I/O	4	DQ15	D25	Comp_of_IOB133A	True	P25
IOB135A/CLKHOLD_N	I/O	4	DQS15	CLKHOLD_N	True_of_IOB135B	True	T24
IOB135B/D22	I/O	4	DQS15	D22	Comp_of_IOB135A	True	T25
IOB138A/D24	I/O	4	DQ15	D24	True_of_IOB138B	True	N26
IOB138B/D23	I/O	4	DQ15	D23	Comp_of_IOB138A	True	M26
IOB140A/D21	I/O	4	DQ15	D21	True_of_IOB140B	True	R26
IOB140B/D20	I/O	4	DQ15	D20	Comp_of_IOB140A	True	P26
IOB142A/D19	I/O	4	DQ15	D19	True_of_IOB142B	True	T22
IOB142B/D18	I/O	4	DQ15	D18	Comp_of_IOB142A	True	R22
IOB144A/D17	I/O	4	DQ15	D17	True_of_IOB144B	True	T23
IOB144B/D16	I/O	4	DQ15	D16	Comp_of_IOB144A	True	R23
IOB146A	I/O	4	none			none	R18
IOB169A/TDO	I/O	10	none	TDO	True_of_IOB169B	True	J10
IOB169B/TMS	I/O	10	none	TMS	Comp_of_IOB169A	True	H11
IOB171A/READY	I/O	10	none	READY	True_of_IOB171B	True	V11
IOB171B/DONE	I/O	10	none	DONE	Comp_of_IOB171A	True	W10
IOB173A/TCK	I/O	10	none	TCK	True_of_IOB173B	True	H12
IOB173B/TDI	I/O	10	none	TDI	Comp_of_IOB173A	True	H10
IOB175A/MODE0	I/O	10	none	MODE0	True_of_IOB175B	True	AB7
IOB175B/CCLK	I/O	10	none	CCLK	Comp_of_IOB175A	True	H13
IOB177A/MODE1	I/O	10	none	MODE1	True_of_IOB177B	True	Y9
IOB177B/MODE2	I/O	10	none	MODE2	Comp_of_IOB177A	True	W9
IOB179A/CFGBVS	I/O	10	none	CFGBVS	True_of_IOB179B	True	AB15
IOB179B/RECONFIG_N	I/O	10	none	RECONFIG_N	Comp_of_IOB179A	True	AE16

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOB37A	I/O	5	none			none	U17
IOB38A	I/O	5	DQ8		True_of_IOB38B	True	V16
IOB38B	I/O	5	DQ8		Comp_of_IOB38A	True	V17
IOB40A	I/O	5	DQ8		True_of_IOB40B	True	T14
IOB40B	I/O	5	DQ8		Comp_of_IOB40A	True	T15
IOB42A	I/O	5	DQS8		True_of_IOB42B	True	T17
IOB42B	I/O	5	DQS8		Comp_of_IOB42A	True	T18
IOB44A	I/O	5	DQ8		True_of_IOB44B	True	U15
IOB44B	I/O	5	DQ8		Comp_of_IOB44A	True	U16
IOB47A	I/O	5	DQ8		True_of_IOB47B	True	U14
IOB47B	I/O	5	DQ8		Comp_of_IOB47A	True	V14
IOB49A	I/O	5	DQ8		True_of_IOB49B	True	V18
IOB49B	I/O	5	DQ8		Comp_of_IOB49A	True	W18
IOB51A	I/O	5	DQ9		True_of_IOB51B	True	AA24
IOB51B	I/O	5	DQ9		Comp_of_IOB51A	True	AB25
IOB53A	I/O	5	DQ9		True_of_IOB53B	True	AA22
IOB53B	I/O	5	DQ9		Comp_of_IOB53A	True	AA23
IOB56A	I/O	5	DQS9		True_of_IOB56B	True	AB24
IOB56B	I/O	5	DQS9		Comp_of_IOB56A	True	AC24
IOB58A	I/O	5	DQ9		True_of_IOB58B	True	V23
IOB58B	I/O	5	DQ9		Comp_of_IOB58A	True	W23
IOB60A/SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	I/O	5	DQ9	SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	True_of_IOB60B	True	Y22
IOB60B/SGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	I/O	5	DQ9	SGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	Comp_of_IOB60A	True	Y23
IOB62A/MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	I/O	5	DQ9	MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	True_of_IOB62B	True	U22
IOB62B/MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	I/O	5	DQ9	MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	Comp_of_IOB62A	True	V22
IOB66A/MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	I/O	5	DQ10	MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	True_of_IOB66B	True	U21

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOB66B/MGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	I/O	5	DQ10	MGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	Comp_of_IOB66A	True	V21
IOB68A/SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	I/O	5	DQ10	SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	True_of_IOB68B	True	W21
IOB68B/SGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	I/O	5	DQ10	SGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	Comp_of_IOB68A	True	Y21
IOB70A	I/O	5	DQS10		True_of_IOB70B	True	T20
IOB70B	I/O	5	DQS10		Comp_of_IOB70A	True	U20
IOB72A	I/O	5	DQ10		True_of_IOB72B	True	W20
IOB72B	I/O	5	DQ10		Comp_of_IOB72A	True	Y20
IOB74A	I/O	5	DQ10		True_of_IOB74B	True	V19
IOB74B	I/O	5	DQ10		Comp_of_IOB74A	True	W19
IOB76A	I/O	5	DQ10		True_of_IOB76B	True	T19
IOB76B	I/O	5	DQ10		Comp_of_IOB76A	True	U19
IOB78A	I/O	5	DQ11		True_of_IOB78B	True	U25
IOB78B	I/O	5	DQ11		Comp_of_IOB78A	True	U26
IOB80A	I/O	5	DQ11		True_of_IOB80B	True	V26
IOB80B	I/O	5	DQ11		Comp_of_IOB80A	True	W26
IOB83A	I/O	5	DQ11		True_of_IOB83B	True	W25
IOB83B	I/O	5	DQ11		Comp_of_IOB83A	True	Y26
IOB85A	I/O	5	DQ11		True_of_IOB85B	True	V24
IOB85B	I/O	5	DQ11		Comp_of_IOB85A	True	W24
IOB87A	I/O	5	DQ11		True_of_IOB87B	True	Y25
IOB87B	I/O	5	DQ11		Comp_of_IOB87A	True	AA25
IOB89A	I/O	5	DQS11		True_of_IOB89B	True	AB26
IOB89B	I/O	5	DQS11		Comp_of_IOB89A	True	AC26
IOB91A	I/O	5	none			none	U24
IOB92A	I/O	4	none			none	M19
IOB93A/D00/MOSI	I/O	4	DQ12	D00/MOSI	True_of_IOB93B	True	R14
IOB93B/D01/DIN	I/O	4	DQ12	D01/DIN	Comp_of_IOB93A	True	R15
IOB95A/D02	I/O	4	DQ12	D02	True_of_IOB95B	True	P14

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOB95B/D03	I/O	4	DQ12	D03	Comp_of_IOB95A	True	N14
IOB97A/D04	I/O	4	DQ12	D04	True_of_IOB97B	True	N16
IOB97B/D05/SI	I/O	4	DQ12	D05/SI	Comp_of_IOB97A	True	N17
IOB99A/D06/SSPI_CLK	I/O	4	DQ12	D06/SSPI_CLK	True_of_IOB99B	True	R16
IOB99B/D07/SSPI_WPN	I/O	4	DQ12	D07/SSPI_WPN	Comp_of_IOB99A	True	R17
IOL101A	I/O	6	DQ7		True_of_IOL101B	True	R8
IOL101B	I/O	6	DQ7		Comp_of_IOL101A	True	P8
IOL103A	I/O	6	DQ7		True_of_IOL103B	True	R7
IOL103B	I/O	6	DQ7		Comp_of_IOL103A	True	R6
IOL105A	I/O	6	DQ7		True_of_IOL105B	True	T8
IOL105B	I/O	6	DQ7		Comp_of_IOL105A	True	T7
IOL107A	I/O	6	DQ7		True_of_IOL107B	True	P6
IOL107B	I/O	6	DQ7		Comp_of_IOL107A	True	P5
IOL109A	I/O	6	none			none	U4
IOL11A	I/O	7	DQ0		True_of_IOL11B	True	H9
IOL11B	I/O	7	DQ0		Comp_of_IOL11A	True	G9
IOL13A	I/O	7	DQ0		True_of_IOL13B	True	H6
IOL13B	I/O	7	DQ0		Comp_of_IOL13A	True	G6
IOL15A	I/O	7	DQ1		True_of_IOL15B	True	B4
IOL15B	I/O	7	DQ1		Comp_of_IOL15A	True	A4
IOL17A	I/O	7	DQ1		True_of_IOL17B	True	D3
IOL17B	I/O	7	DQ1		Comp_of_IOL17A	True	C3
IOL1A	I/O	7	none			none	J8
IOL20A	I/O	7	DQ1		True_of_IOL20B	True	F3
IOL20B	I/O	7	DQ1		Comp_of_IOL20A	True	E3
IOL22A	I/O	7	DQS1		True_of_IOL22B	True	B5
IOL22B	I/O	7	DQS1		Comp_of_IOL22A	True	A5
IOL24A/MGCLKT_11/LPLL0_T_IN1/LPLL1_T_IN1	I/O	7	DQ1	MGCLKT_11/LPLL0_T_IN1/LPLL1_T_IN1	True_of_IOL24B	True	E5
IOL24B/MGCLKC_11/LPLL0_C_IN1/LPLL1_C_IN1	I/O	7	DQ1	MGCLKC_11/LPLL0_C_IN1/LPLL1_C_IN1	Comp_of_IOL24A	True	D5

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOL26A/SGCLKT_11/LPLL0_T_IN0/LPLL1_T_IN0	I/O	7	DQ1	SGCLKT_11/LPLL0_T_IN0/LPLL1_T_IN0	True_of_IOL26B	True	D4
IOL26B/SGCLKC_11/LPLL0_C_IN0/LPLL1_C_IN0	I/O	7	DQ1	SGCLKC_11/LPLL0_C_IN0/LPLL1_C_IN0	Comp_of_IOL26A	True	C4
IOL29A/MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	I/O	7	DQ2	MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	True_of_IOL29B	True	G5
IOL29B/MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	I/O	7	DQ2	MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	Comp_of_IOL29A	True	F5
IOL2A/ADCINCK0	I/O	7	DQ0	ADCINCK0	True_of_IOL2B	True	E6
IOL2B	I/O	7	DQ0		Comp_of_IOL2A	True	D6
IOL31A/SGCLKT_10/LPLL0_T_FB0/LPLL1_T_FB0	I/O	7	DQ2	SGCLKT_10/LPLL0_T_FB0/LPLL1_T_FB0	True_of_IOL31B	True	G4
IOL31B/SGCLKC_10/LPLL0_C_FB0/LPLL1_C_FB0	I/O	7	DQ2	SGCLKC_10/LPLL0_C_FB0/LPLL1_C_FB0	Comp_of_IOL31A	True	F4
IOL33A	I/O	7	DQ2		True_of_IOL33B	True	J6
IOL33B	I/O	7	DQ2		Comp_of_IOL33A	True	J5
IOL35A	I/O	7	DQS2		True_of_IOL35B	True	J4
IOL35B	I/O	7	DQS2		Comp_of_IOL35A	True	H4
IOL38A	I/O	7	DQ2		True_of_IOL38B	True	L8
IOL38B	I/O	7	DQ2		Comp_of_IOL38A	True	K8
IOL40A	I/O	7	DQ2		True_of_IOL40B	True	K7
IOL40B	I/O	7	DQ2		Comp_of_IOL40A	True	K6
IOL42A	I/O	7	DQ3		True_of_IOL42B	True	A3
IOL42B	I/O	7	DQ3		Comp_of_IOL42A	True	A2
IOL44A	I/O	7	DQ3		True_of_IOL44B	True	C2
IOL44B	I/O	7	DQ3		Comp_of_IOL44A	True	B2
IOL47A	I/O	7	DQ3		True_of_IOL47B	True	F2
IOL47B	I/O	7	DQ3		Comp_of_IOL47A	True	E2
IOL49A	I/O	7	DQS3		True_of_IOL49B	True	C1
IOL49B	I/O	7	DQS3		Comp_of_IOL49A	True	B1
IOL4A	I/O	7	DQS0		True_of_IOL4B	True	H7

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOL4B	I/O	7	DQS0		Comp_of_IOL4A	True	G7
IOL51A	I/O	7	DQ3		True_of_IOL51B	True	E1
IOL51B	I/O	7	DQ3		Comp_of_IOL51A	True	D1
IOL53A	I/O	7	DQ3		True_of_IOL53B	True	G2
IOL53B	I/O	7	DQ3		Comp_of_IOL53A	True	G1
IOL55A	I/O	7	none			none	H3
IOL56A	I/O	6	DQ4		True_of_IOL56B	True	K3
IOL56B	I/O	6	DQ4		Comp_of_IOL56A	True	J3
IOL58A	I/O	6	DQ4		True_of_IOL58B	True	M7
IOL58B	I/O	6	DQ4		Comp_of_IOL58A	True	L7
IOL60A	I/O	6	DQ4		True_of_IOL60B	True	M6
IOL60B	I/O	6	DQ4		Comp_of_IOL60A	True	M5
IOL62A	I/O	6	DQ4		True_of_IOL62B	True	L5
IOL62B	I/O	6	DQ4		Comp_of_IOL62A	True	K5
IOL65A	I/O	6	DQS4		True_of_IOL65B	True	M4
IOL65B	I/O	6	DQS4		Comp_of_IOL65A	True	L4
IOL67A	I/O	6	DQ4		True_of_IOL67B	True	N7
IOL67B	I/O	6	DQ4		Comp_of_IOL67A	True	N6
IOL69A	I/O	6	DQS5		True_of_IOL69B	True	N1
IOL69B	I/O	6	DQS5		Comp_of_IOL69A	True	M1
IOL6A	I/O	7	DQ0		True_of_IOL6B	True	H8
IOL6B	I/O	7	DQ0		Comp_of_IOL6A	True	G8
IOL71A	I/O	6	DQ5		True_of_IOL71B	True	K1
IOL71B	I/O	6	DQ5		Comp_of_IOL71A	True	J1
IOL73A	I/O	6	none			none	N8
IOL74A	I/O	6	DQ5		True_of_IOL74B	True	L3
IOL74B	I/O	6	DQ5		Comp_of_IOL74A	True	K2
IOL76A	I/O	6	DQ5		True_of_IOL76B	True	H2
IOL76B	I/O	6	DQ5		Comp_of_IOL76A	True	H1
IOL78A/SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	I/O	6	DQ5	SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	True_of_IOL78B	True	M2

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOL78B/SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	I/O	6	DQ5	SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	Comp_of_IOL78A	True	L2
IOL80A/MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	I/O	6	DQ5	MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	True_of_IOL80B	True	N3
IOL80B/MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	I/O	6	DQ5	MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	Comp_of_IOL80A	True	N2
IOL83A/MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	I/O	6	DQ6	MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	True_of_IOL83B	True	R3
IOL83B/MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	I/O	6	DQ6	MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	Comp_of_IOL83A	True	P3
IOL85A/SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	I/O	6	DQ6	SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	True_of_IOL85B	True	P4
IOL85B/SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	I/O	6	DQ6	SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	Comp_of_IOL85A	True	N4
IOL87A	I/O	6	DQ6		True_of_IOL87B	True	T4
IOL87B	I/O	6	DQ6		Comp_of_IOL87A	True	T3
IOL89A	I/O	6	DQ6		True_of_IOL89B	True	T2
IOL89B	I/O	6	DQ6		Comp_of_IOL89A	True	R2
IOL8A	I/O	7	DQ0		True_of_IOL8B	True	F8
IOL8B	I/O	7	DQ0		Comp_of_IOL8A	True	F7
IOL92A	I/O	6	DQ6		True_of_IOL92B	True	U2
IOL92B	I/O	6	DQ6		Comp_of_IOL92A	True	U1
IOL94A	I/O	6	DQS6		True_of_IOL94B	True	R1
IOL94B	I/O	6	DQS6		Comp_of_IOL94A	True	P1
IOL96A	I/O	6	DQS7		True_of_IOL96B	True	U6
IOL96B	I/O	6	DQS7		Comp_of_IOL96A	True	U5
IOL98A	I/O	6	DQ7		True_of_IOL98B	True	T5
IOL98B	I/O	6	DQ7		Comp_of_IOL98A	True	R5
IOR101A	I/O	3	DQ16		True_of_IOR101B	True	M14
IOR101B	I/O	3	DQ16		Comp_of_IOR101A	True	L14
IOR103A	I/O	3	DQS16		True_of_IOR103B	True	K16

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOR103B	I/O	3	DQS16		Comp_of_IOR103A	True	K17
IOR105A	I/O	3	DQ16		True_of_IOR105B	True	J14
IOR105B	I/O	3	DQ16		Comp_of_IOR105A	True	J15
IOR107A/ADCINCK1	I/O	3	DQ16	ADCINCK1	True_of_IOR107B	True	K15
IOR107B	I/O	3	DQ16		Comp_of_IOR107A	True	J16
IOR109A	I/O	3	none			none	K18
IOR11A	I/O	2	DQ23		True_of_IOR11B	True	G19
IOR11B	I/O	2	DQ23		Comp_of_IOR11A	True	F20
IOR13A	I/O	2	DQ23		True_of_IOR13B	True	H16
IOR13B	I/O	2	DQ23		Comp_of_IOR13A	True	G16
IOR15A	I/O	2	DQ22		True_of_IOR15B	True	C17
IOR15B	I/O	2	DQ22		Comp_of_IOR15A	True	B17
IOR17A	I/O	2	DQ22		True_of_IOR17B	True	E16
IOR17B	I/O	2	DQ22		Comp_of_IOR17A	True	D16
IOR1A	I/O	2	none			none	H17
IOR20A	I/O	2	DQS22		True_of_IOR20B	True	A17
IOR20B	I/O	2	DQS22		Comp_of_IOR20A	True	A18
IOR22A	I/O	2	DQ22		True_of_IOR22B	True	B19
IOR22B	I/O	2	DQ22		Comp_of_IOR22A	True	A19
IOR24A/SGCLKT_1/RPLL0_T_FB0/RPLL1_T_FB0	I/O	2	DQ22	SGCLKT_1/RPLL0_T_FB0/RPLL1_T_FB0	True_of_IOR24B	True	E17
IOR24B/SGCLKC_1/RPLL0_C_FB0/RPLL1_C_FB0	I/O	2	DQ22	SGCLKC_1/RPLL0_C_FB0/RPLL1_C_FB0	Comp_of_IOR24A	True	E18
IOR26A/MGCLKT_1/RPLL0_T_FB1/RPLL1_T_FB1	I/O	2	DQ22	MGCLKT_1/RPLL0_T_FB1/RPLL1_T_FB1	True_of_IOR26B	True	D18
IOR26B/MGCLKC_1/RPLL0_C_FB1/RPLL1_C_FB1	I/O	2	DQ22	MGCLKC_1/RPLL0_C_FB1/RPLL1_C_FB1	Comp_of_IOR26A	True	C18
IOR29A/SGCLKT_0/RPLL0_T_IN0/RPLL1_T_IN0	I/O	2	DQ21	SGCLKT_0/RPLL0_T_IN0/RPLL1_T_IN0	True_of_IOR29B	True	E20
IOR29B/SGCLKC_0/RPLL0_C_IN0/RPLL1_C_IN0	I/O	2	DQ21	SGCLKC_0/RPLL0_C_IN0/RPLL1_C_IN0	Comp_of_IOR29A	True	D20

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOR2A	I/O	2	DQ23		True_of_IOR2B	True	H14
IOR2B	I/O	2	DQ23		Comp_of_IOR2A	True	H15
IOR31A/MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	I/O	2	DQ21	MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	True_of_IOR31B	True	D19
IOR31B/MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	I/O	2	DQ21	MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	Comp_of_IOR31A	True	C19
IOR33A	I/O	2	DQ21		True_of_IOR33B	True	C21
IOR33B	I/O	2	DQ21		Comp_of_IOR33A	True	B21
IOR35A	I/O	2	DQS21		True_of_IOR35B	True	B20
IOR35B	I/O	2	DQS21		Comp_of_IOR35A	True	A20
IOR38A	I/O	2	DQ21		True_of_IOR38B	True	B22
IOR38B	I/O	2	DQ21		Comp_of_IOR38A	True	A22
IOR40A	I/O	2	DQ21		True_of_IOR40B	True	E21
IOR40B	I/O	2	DQ21		Comp_of_IOR40A	True	D21
IOR42A	I/O	2	DQ20		True_of_IOR42B	True	C22
IOR42B	I/O	2	DQ20		Comp_of_IOR42A	True	C23
IOR44A	I/O	2	DQS20		True_of_IOR44B	True	A23
IOR44B	I/O	2	DQS20		Comp_of_IOR44A	True	A24
IOR47A	I/O	2	DQ20		True_of_IOR47B	True	B25
IOR47B	I/O	2	DQ20		Comp_of_IOR47A	True	A25
IOR49A	I/O	2	DQ20		True_of_IOR49B	True	C26
IOR49B	I/O	2	DQ20		Comp_of_IOR49A	True	B26
IOR4A	I/O	2	DQ23		True_of_IOR4B	True	G17
IOR4B	I/O	2	DQ23		Comp_of_IOR4A	True	F17
IOR51A	I/O	2	DQ20		True_of_IOR51B	True	C24
IOR51B	I/O	2	DQ20		Comp_of_IOR51A	True	B24
IOR53A	I/O	2	DQ20		True_of_IOR53B	True	D23
IOR53B	I/O	2	DQ20		Comp_of_IOR53A	True	D24
IOR55A	I/O	2	none			none	E22
IOR56A	I/O	3	DQ19		True_of_IOR56B	True	J25
IOR56B	I/O	3	DQ19		Comp_of_IOR56A	True	J26

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOR58A	I/O	3	DQS19		True_of_IOR58B	True	E26
IOR58B	I/O	3	DQS19		Comp_of_IOR58A	True	D26
IOR60A	I/O	3	DQ19		True_of_IOR60B	True	G25
IOR60B	I/O	3	DQ19		Comp_of_IOR60A	True	F25
IOR62A	I/O	3	DQ19		True_of_IOR62B	True	H26
IOR62B	I/O	3	DQ19		Comp_of_IOR62A	True	G26
IOR65A	I/O	3	DQ19		True_of_IOR65B	True	E25
IOR65B	I/O	3	DQ19		Comp_of_IOR65A	True	D25
IOR67A	I/O	3	DQ19		True_of_IOR67B	True	G24
IOR67B	I/O	3	DQ19		Comp_of_IOR67A	True	F24
IOR69A	I/O	3	DQ18		True_of_IOR69B	True	K22
IOR69B	I/O	3	DQ18		Comp_of_IOR69A	True	K23
IOR6A	I/O	2	DQ23		True_of_IOR6B	True	G15
IOR6B	I/O	2	DQ23		Comp_of_IOR6A	True	F15
IOR71A	I/O	3	DQ18		True_of_IOR71B	True	F23
IOR71B	I/O	3	DQ18		Comp_of_IOR71A	True	E23
IOR73A	I/O	3	none			none	L19
IOR74A	I/O	3	DQ18		True_of_IOR74B	True	J24
IOR74B	I/O	3	DQ18		Comp_of_IOR74A	True	H24
IOR76A	I/O	3	DQS18		True_of_IOR76B	True	G22
IOR76B	I/O	3	DQS18		Comp_of_IOR76A	True	F22
IOR78A/SGCLKT_3/RPLL2_T_FB0/RPLL3_T_FB0	I/O	3	DQ18	SGCLKT_3/RPLL2_T_FB0/RPLL3_T_FB0	True_of_IOR78B	True	J23
IOR78B/SGCLKC_3/RPLL2_C_FB0/RPLL3_C_FB0	I/O	3	DQ18	SGCLKC_3/RPLL2_C_FB0/RPLL3_C_FB0	Comp_of_IOR78A	True	H23
IOR80A/MGCLKT_3/RPLL2_T_FB1/RPLL3_T_FB1	I/O	3	DQ18	MGCLKT_3/RPLL2_T_FB1/RPLL3_T_FB1	True_of_IOR80B	True	H21
IOR80B/MGCLKC_3/RPLL2_C_FB1/RPLL3_C_FB1	I/O	3	DQ18	MGCLKC_3/RPLL2_C_FB1/RPLL3_C_FB1	Comp_of_IOR80A	True	H22
IOR83A/MGCLKT_2/RPLL2_T_IN1/RPLL3_T_IN1	I/O	3	DQ17	MGCLKT_2/RPLL2_T_IN1/RPLL3_T_IN1	True_of_IOR83B	True	K21

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOR83B/MGCLKC_2/RPLL2_C_IN1/RPLL3_C_IN1	I/O	3	DQ17	MGCLKC_2/RPLL2_C_IN1/RPLL3_C_IN1	Comp_of_IOR83A	True	J21
IOR85A/SGCLKT_2/RPLL2_T_IN0/RPLL3_T_IN0	I/O	3	DQ17	SGCLKT_2/RPLL2_T_IN0/RPLL3_T_IN0	True_of_IOR85B	True	G20
IOR85B/SGCLKC_2/RPLL2_C_IN0/RPLL3_C_IN0	I/O	3	DQ17	SGCLKC_2/RPLL2_C_IN0/RPLL3_C_IN0	Comp_of_IOR85A	True	G21
IOR87A	I/O	3	DQ17		True_of_IOR87B	True	J18
IOR87B	I/O	3	DQ17		Comp_of_IOR87A	True	H18
IOR89A	I/O	3	DQS17		True_of_IOR89B	True	K20
IOR89B	I/O	3	DQS17		Comp_of_IOR89A	True	J20
IOR8A	I/O	2	DQS23		True_of_IOR8B	True	F18
IOR8B	I/O	2	DQS23		Comp_of_IOR8A	True	F19
IOR92A	I/O	3	DQ17		True_of_IOR92B	True	L17
IOR92B	I/O	3	DQ17		Comp_of_IOR92A	True	L18
IOR94A	I/O	3	DQ17		True_of_IOR94B	True	J19
IOR94B	I/O	3	DQ17		Comp_of_IOR94A	True	H19
IOR96A	I/O	3	DQ16		True_of_IOR96B	True	M16
IOR96B	I/O	3	DQ16		Comp_of_IOR96A	True	M17
IOR98A	I/O	3	DQ16		True_of_IOR98B	True	M15
IOR98B	I/O	3	DQ16		Comp_of_IOR98A	True	L15
M0_CKN	DIO	MIPI	none			none	AA3
M0_CKP	DIO	MIPI	none			none	Y3
M0_D0N	DIO	MIPI	none			none	W1
M0_D0P	DIO	MIPI	none			none	V1
M0_D1N	DIO	MIPI	none			none	AA2
M0_D1P	DIO	MIPI	none			none	Y2
M0_D2N	DIO	MIPI	none			none	W3
M0_D2P	DIO	MIPI	none			none	V3
M0_D3N	DIO	MIPI	none			none	W4
M0_D3P	DIO	MIPI	none			none	V4
M1_CKN	DIO	MIPI	none			none	AF2

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
M1_CKP	DIO	MIPI	none			none	AE2
M1_D0N	DIO	MIPI	none			none	AC1
M1_D0P	DIO	MIPI	none			none	AB1
M1_D1N	DIO	MIPI	none			none	AE1
M1_D1P	DIO	MIPI	none			none	AD1
M1_D2N	DIO	MIPI	none			none	AF3
M1_D2P	DIO	MIPI	none			none	AE3
M1_D3N	DIO	MIPI	none			none	AF5
M1_D3P	DIO	MIPI	none			none	AE5
Q0_LN0_RXM_I	DIO	Q0	none			none	AD12
Q0_LN0_RXP_I	DIO	Q0	none			none	AC12
Q0_LN0_TXM_O	DIO	Q0	none			none	AD10
Q0_LN0_TXP_O	DIO	Q0	none			none	AC10
Q0_LN1_RXM_I	DIO	Q0	none			none	AF13
Q0_LN1_RXP_I	DIO	Q0	none			none	AE13
Q0_LN1_TXM_O	DIO	Q0	none			none	AF9
Q0_LN1_TXP_O	DIO	Q0	none			none	AE9
Q0_LN2_RXM_I	DIO	Q0	none			none	AD14
Q0_LN2_RXP_I	DIO	Q0	none			none	AC14
Q0_LN2_TXM_O	DIO	Q0	none			none	AD8
Q0_LN2_TXP_O	DIO	Q0	none			none	AC8
Q0_LN3_RXM_I	DIO	Q0	none			none	AF11
Q0_LN3_RXP_I	DIO	Q0	none			none	AE11
Q0_LN3_TXM_O	DIO	Q0	none			none	AF7
Q0_LN3_TXP_O	DIO	Q0	none			none	AE7
Q0_REFCLKM_0	DIO	Q0	none			none	AB13
Q0_REFCLKM_1	DIO	Q0	none			none	AB11
Q0_REFCLKP_0	DIO	Q0	none			none	AA13
Q0_REFCLKP_1	DIO	Q0	none			none	AA11
Q1_LN0_RXM_I	DIO	Q1	none			none	A11
Q1_LN0_RXP_I	DIO	Q1	none			none	B11

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
Q1_LN0_TXM_O	DIO	Q1	none			none	A7
Q1_LN0_TXP_O	DIO	Q1	none			none	B7
Q1_LN1_RXM_I	DIO	Q1	none			none	C14
Q1_LN1_RXP_I	DIO	Q1	none			none	D14
Q1_LN1_TXM_O	DIO	Q1	none			none	C8
Q1_LN1_TXP_O	DIO	Q1	none			none	D8
Q1_LN2_RXM_I	DIO	Q1	none			none	A13
Q1_LN2_RXP_I	DIO	Q1	none			none	B13
Q1_LN2_TXM_O	DIO	Q1	none			none	A9
Q1_LN2_TXP_O	DIO	Q1	none			none	B9
Q1_LN3_RXM_I	DIO	Q1	none			none	C12
Q1_LN3_RXP_I	DIO	Q1	none			none	D12
Q1_LN3_TXM_O	DIO	Q1	none			none	C10
Q1_LN3_TXP_O	DIO	Q1	none			none	D10
Q1_REFCLKM_0	DIO	Q1	none			none	E11
Q1_REFCLKM_1	DIO	Q1	none			none	E13
Q1_REFCLKP_0	DIO	Q1	none			none	F11
Q1_REFCLKP_1	DIO	Q1	none			none	F13
ADCTN	DIO	ADC	none			none	R11
ADCTP	DIO	ADC	none			none	R12
ADCVN	DIO	ADC	none			none	P11
ADCVP	DIO	ADC	none			none	N12
VDDAM	Power	N/A					N13
VDDDM	Power	N/A					U13
VDDAM	Power	N/A					R13
VDDDM	Power	N/A					W13
VDDXM	Power	N/A					M12
VDDAQ0	Power	N/A					AA12
VDDAQ0	Power	N/A					AC11
VDDAQ0	Power	N/A					AA10
VDDHAQ0_VDDHAQ1	Power	N/A					U9

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
VDDHAQ0_VDDHAQ1	Power	N/A					R9
VDDTQ0	Power	N/A					AC13
VDDTQ0	Power	N/A					AC9
VDDAQ1	Power	N/A					F12
VDDAQ1	Power	N/A					D9
VDDAQ1	Power	N/A					F10
VDDTQ1	Power	N/A					D13
VDDTQ1	Power	N/A					D11
VCC	Power	N/A					V12
VCC	Power	N/A					V10
VCC	Power	N/A					T12
VCC	Power	N/A					L13
VCC	Power	N/A					T10
VCC	Power	N/A					L11
VCC	Power	N/A					K12
VCC	Power	N/A					M10
VCC	Power	N/A					U11
VCC	Power	N/A					K10
VCC	Power	N/A					J11
VCC	Power	N/A					P10
VCC	Power	N/A					J13
VCC_LDO	Power	N/A					B12
VCC_LDO	Power	N/A					B10
VCC_LDO	Power	N/A					C7
VCC_LDO	Power	N/A					C15
VCC_LDO	Power	N/A					B14
VCC_LDO	Power	N/A					B8
VCCIO10	Power	N/A					Y14
VCCIO10	Power	N/A					W11
VCCX	Power	N/A					N9
VCCX	Power	N/A					L9

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
VCCX	Power	N/A					J9
VCCIO2	Power	N/A					E19
VCCIO2	Power	N/A					C25
VCCIO2	Power	N/A					A21
VCCIO2	Power	N/A					D22
VCCIO2	Power	N/A					F16
VCCIO2	Power	N/A					B18
VCCIO3	Power	N/A					G23
VCCIO3	Power	N/A					K14
VCCIO3	Power	N/A					M18
VCCIO3	Power	N/A					H20
VCCIO3	Power	N/A					F26
VCCIO3	Power	N/A					J17
VCCIO4	Power	N/A					L21
VCCIO4	Power	N/A					P22
VCCIO4	Power	N/A					N15
VCCIO4	Power	N/A					K24
VCCIO4	Power	N/A					N25
VCCIO4	Power	N/A					R19
VCCIO5	Power	N/A					Y24
VCCIO5	Power	N/A					T16
VCCIO5	Power	N/A					U23
VCCIO5	Power	N/A					V20
VCCIO5	Power	N/A					AC25
VCCIO5	Power	N/A					T26
VCCIO6	Power	N/A					L1
VCCIO6	Power	N/A					P2
VCCIO6	Power	N/A					N5
VCCIO6	Power	N/A					T6
VCCIO6	Power	N/A					K4
VCCIO6	Power	N/A					M8

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
VCCIO7	Power	N/A					J7
VCCIO7	Power	N/A					D2
VCCIO7	Power	N/A					A1
VCCIO7	Power	N/A					G3
VCCIO7	Power	N/A					C5
VCCIO7	Power	N/A					F6
VSS	Ground	N/A					M11
VSS	Ground	N/A					AE15
VSS	Ground	N/A					B15
VSS	Ground	N/A					A10
VSS	Ground	N/A					A12
VSS	Ground	N/A					A14
VSS	Ground	N/A					A16
VSS	Ground	N/A					A26
VSS	Ground	N/A					A6
VSS	Ground	N/A					A8
VSS	Ground	N/A					AA14
VSS	Ground	N/A					AA16
VSS	Ground	N/A					AA26
VSS	Ground	N/A					AA6
VSS	Ground	N/A					AB10
VSS	Ground	N/A					AB12
VSS	Ground	N/A					AB14
VSS	Ground	N/A					AB23
VSS	Ground	N/A					AB3
VSS	Ground	N/A					AA9
VSS	Ground	N/A					AB8
VSS	Ground	N/A					AC15
VSS	Ground	N/A					AC20
VSS	Ground	N/A					AC7
VSS	Ground	N/A					AD11

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
VSS	Ground	N/A					AD13
VSS	Ground	N/A					AD6
VSS	Ground	N/A					AD9
VSS	Ground	N/A					AD16
VSS	Ground	N/A					AE24
VSS	Ground	N/A					AE4
VSS	Ground	N/A					AE6
VSS	Ground	N/A					AF1
VSS	Ground	N/A					AF10
VSS	Ground	N/A					AF12
VSS	Ground	N/A					AF14
VSS	Ground	N/A					AF16
VSS	Ground	N/A					AF21
VSS	Ground	N/A					AF6
VSS	Ground	N/A					AF8
VSS	Ground	N/A					B16
VSS	Ground	N/A					B23
VSS	Ground	N/A					B3
VSS	Ground	N/A					B6
VSS	Ground	N/A					C11
VSS	Ground	N/A					C13
VSS	Ground	N/A					C16
VSS	Ground	N/A					C20
VSS	Ground	N/A					C6
VSS	Ground	N/A					C9
VSS	Ground	N/A					D15
VSS	Ground	N/A					D17
VSS	Ground	N/A					D7
VSS	Ground	N/A					E10
VSS	Ground	N/A					E12
VSS	Ground	N/A					E14

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
VSS	Ground	N/A					E24
VSS	Ground	N/A					E4
VSS	Ground	N/A					E7
VSS	Ground	N/A					E8
VSS	Ground	N/A					E9
VSS	Ground	N/A					F1
VSS	Ground	N/A					F14
VSS	Ground	N/A					F21
VSS	Ground	N/A					F9
VSS	Ground	N/A					G10
VSS	Ground	N/A					G11
VSS	Ground	N/A					AB9
VSS	Ground	N/A					G13
VSS	Ground	N/A					Y12
VSS	Ground	N/A					G18
VSS	Ground	N/A					G12
VSS	Ground	N/A					H25
VSS	Ground	N/A					H5
VSS	Ground	N/A					J12
VSS	Ground	N/A					J2
VSS	Ground	N/A					J22
VSS	Ground	N/A					K11
VSS	Ground	N/A					K13
VSS	Ground	N/A					K19
VSS	Ground	N/A					K9
VSS	Ground	N/A					L10
VSS	Ground	N/A					L12
VSS	Ground	N/A					L16
VSS	Ground	N/A					L26
VSS	Ground	N/A					L6
VSS	Ground	N/A					M13

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
VSS	Ground	N/A					M23
VSS	Ground	N/A					M3
VSS	Ground	N/A					M9
VSS	Ground	N/A					N10
VSS	Ground	N/A					N20
VSS	Ground	N/A					P13
VSS	Ground	N/A					P17
VSS	Ground	N/A					P7
VSS	Ground	N/A					P9
VSS	Ground	N/A					R10
VSS	Ground	N/A					R24
VSS	Ground	N/A					R4
VSS	Ground	N/A					T1
VSS	Ground	N/A					T11
VSS	Ground	N/A					T13
VSS	Ground	N/A					T21
VSS	Ground	N/A					T9
VSS	Ground	N/A					U10
VSS	Ground	N/A					U12
VSS	Ground	N/A					U18
VSS	Ground	N/A					U8
VSS	Ground	N/A					V15
VSS	Ground	N/A					V25
VSS	Ground	N/A					V5
VSS	Ground	N/A					E15
VSS	Ground	N/A					W12
VSS	Ground	N/A					W2
VSS	Ground	N/A					W22
VSS	Ground	N/A					Y11
VSS	Ground	N/A					Y10
VSS	Ground	N/A					Y13

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
VSS	Ground	N/A					Y19
VSS	Ground	N/A					V13
NC	N/A	N/A					AD15
NC	N/A	N/A					AD7
NC	N/A	N/A					AE10
NC	N/A	N/A					AE12
NC	N/A	N/A					AE14
NC	N/A	N/A					AE8
NC	N/A	N/A					AA1
NC	N/A	N/A					AC5
NC	N/A	N/A					AD2
NC	N/A	N/A					U3
NC	N/A	N/A					W7
NC	N/A	N/A					Y4
NC	N/A	N/A					G14
NC	N/A	N/A					P12
NC	N/A	N/A					N11
NC	N/A	N/A					AA21
NC	N/A	N/A					AB18
NC	N/A	N/A					AD22
NC	N/A	N/A					AE19
NC	N/A	N/A					AF26
NC	N/A	N/A					W17
NC	N/A	N/A					AA17
NC	N/A	N/A					AA4
NC	N/A	N/A					AA7
NC	N/A	N/A					AB16
NC	N/A	N/A					AB17
NC	N/A	N/A					AB19
NC	N/A	N/A					AB2
NC	N/A	N/A					AB21

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
NC	N/A	N/A					AB22
NC	N/A	N/A					AB5
NC	N/A	N/A					AB6
NC	N/A	N/A					AC16
NC	N/A	N/A					AC17
NC	N/A	N/A					AC18
NC	N/A	N/A					AC19
NC	N/A	N/A					AC2
NC	N/A	N/A					AC23
NC	N/A	N/A					AC3
NC	N/A	N/A					AC6
NC	N/A	N/A					AD17
NC	N/A	N/A					AD18
NC	N/A	N/A					AD19
NC	N/A	N/A					AD20
NC	N/A	N/A					AD24
NC	N/A	N/A					AD25
NC	N/A	N/A					AD26
NC	N/A	N/A					AD3
NC	N/A	N/A					AD4
NC	N/A	N/A					AD5
NC	N/A	N/A					AE17
NC	N/A	N/A					AE18
NC	N/A	N/A					AE20
NC	N/A	N/A					AE21
NC	N/A	N/A					AE25
NC	N/A	N/A					AE26
NC	N/A	N/A					AA5
NC	N/A	N/A					AF17
NC	N/A	N/A					AF18
NC	N/A	N/A					AF19

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
NC	N/A	N/A					AF20
NC	N/A	N/A					AF24
NC	N/A	N/A					AF25
NC	N/A	N/A					AF4
NC	N/A	N/A					V2
NC	N/A	N/A					AB4
NC	N/A	N/A					A15
NC	N/A	N/A					W15
NC	N/A	N/A					W16
NC	N/A	N/A					AC4
NC	N/A	N/A					Y1
NC	N/A	N/A					Y15
NC	N/A	N/A					Y16
NC	N/A	N/A					Y17
NC	N/A	N/A					Y18
NC	N/A	N/A					AA15
NC	N/A	N/A					V9
NC	N/A	N/A					W14
NC	N/A	N/A					V6
NC	N/A	N/A					Y5
NC	N/A	N/A					W5
NC	N/A	N/A					Y8
NC	N/A	N/A					U7
NC	N/A	N/A					Y6
NC	N/A	N/A					W6
NC	N/A	N/A					Y7
NC	N/A	N/A					AA8
NC	N/A	N/A					V7
NC	N/A	N/A					W8
NC	N/A	N/A					V8
NC	N/A	N/A					AF23

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
NC	N/A	N/A					AF22
NC	N/A	N/A					AE23
NC	N/A	N/A					AE22
NC	N/A	N/A					AD23
NC	N/A	N/A					AD21
NC	N/A	N/A					AC22
NC	N/A	N/A					AC21
NC	N/A	N/A					AB20
NC	N/A	N/A					AA20
NC	N/A	N/A					AA19
NC	N/A	N/A					AA18
NC	N/A	N/A					AF15

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
IOB102A/MCS_N	I/O	4	DQ12	MCS_N	True_of_IOB102B	True	L15
IOB102B/D08/SO	I/O	4	DQ12	D08/SO	Comp_of_IOB102A	True	M15
IOB104A/PUDC_B	I/O	4	DQS12	PUDC_B	True_of_IOB104B	True	J18
IOB104B/EMCCLK	I/O	4	DQS12	EMCCLK	Comp_of_IOB104A	True	K18
IOB106A/CSI_B	I/O	4	DQ13	CSI_B	True_of_IOB106B	True	T17
IOB106B/D31	I/O	4	DQ13	D31	Comp_of_IOB106A	True	U17
IOB108A/D30	I/O	4	DQ13	D30	True_of_IOB108B	True	U15
IOB108B/D29	I/O	4	DQ13	D29	Comp_of_IOB108A	True	U16
IOB110A/RDWR	I/O	4	DQS13	RDWR	True_of_IOB110B	True	R18
IOB110B/DOUT_CSO_B	I/O	4	DQS13	DOUT_CSO_B	Comp_of_IOB110A	True	T18
IOB112A/D28	I/O	4	DQ13	D28	True_of_IOB112B	True	V16
IOB112B/D27	I/O	4	DQ13	D27	Comp_of_IOB112A	True	V17
IOB114A/SGCLKT_4/BPLL2_T_FB1/BPLL3_T_FB1	I/O	4	DQ13	SGCLKT_4/BPLL2_T_FB1/BPLL3_T_FB1	True_of_IOB114B	True	R16
IOB114B/SGCLKC_4/BPLL2_C_FB1/BPLL3_C_FB1	I/O	4	DQ13	SGCLKC_4/BPLL2_C_FB1/BPLL3_C_FB1	Comp_of_IOB114A	True	R17
IOB116A/MGCLKT_4/BPLL2_T_FB0/BPLL3_T_FB0	I/O	4	DQ13	MGCLKT_4/BPLL2_T_FB0/BPLL3_T_FB0	True_of_IOB116B	True	T14
IOB116B/MGCLKC_4/BPLL2_C_FB0/BPLL3_C_FB0	I/O	4	DQ13	MGCLKC_4/BPLL2_C_FB0/BPLL3_C_FB0	Comp_of_IOB116A	True	T15
IOB120A/SGCLKT_5/BPLL2_T_IN0/BPLL3_T_IN0	I/O	4	DQ14	SGCLKT_5/BPLL2_T_IN0/BPLL3_T_IN0	True_of_IOB120B	True	P15
IOB120B/SGCLKC_5/BPLL2_C_IN0/BPLL3_C_IN0	I/O	4	DQ14	SGCLKC_5/BPLL2_C_IN0/BPLL3_C_IN0	Comp_of_IOB120A	True	P16
IOB122A/MGCLKT_5/BPLL2_T_IN1/BPLL3_T_IN1	I/O	4	DQ14	MGCLKT_5/BPLL2_T_IN1/BPLL3_T_IN1	True_of_IOB122B	True	P14
IOB122B/MGCLKC_5/BPLL2_C_IN1/BPLL3_C_IN1	I/O	4	DQ14	MGCLKC_5/BPLL2_C_IN1/BPLL3_C_IN1	Comp_of_IOB122A	True	R15
IOB124A/D09	I/O	4	DQ14	D09	True_of_IOB124B	True	M16
IOB124B/D10	I/O	4	DQ14	D10	Comp_of_IOB124A	True	M17

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
IOB126A/D11	I/O	4	DQ14	D11	True_of_IOB126B	True	M14
IOB126B/D12	I/O	4	DQ14	D12	Comp_of_IOB126A	True	N14
IOB129A/D14	I/O	4	DQ14	D14	True_of_IOB129B	True	N18
IOB129B/D15	I/O	4	DQ14	D15	Comp_of_IOB129A	True	P18
IOB131A/SSPI_CS_N	I/O	4	DQS14	SSPI_CS_N	True_of_IOB131B	True	N16
IOB131B/D13	I/O	4	DQS14	D13	Comp_of_IOB131A	True	N17
IOB133A/D26	I/O	4	DQ15	D26	True_of_IOB133B	True	R13
IOB133B/D25	I/O	4	DQ15	D25	Comp_of_IOB133A	True	T13
IOB135A/CLKHOLD_N	I/O	4	DQS15	CLKHOLD_N	True_of_IOB135B	True	V12
IOB135B/D22	I/O	4	DQS15	D22	Comp_of_IOB135A	True	V13
IOB138A/D24	I/O	4	DQ15	D24	True_of_IOB138B	True	U14
IOB138B/D23	I/O	4	DQ15	D23	Comp_of_IOB138A	True	V14
IOB140A/D21	I/O	4	DQ15	D21	True_of_IOB140B	True	T12
IOB140B/D20	I/O	4	DQ15	D20	Comp_of_IOB140A	True	U12
IOB142A/D19	I/O	4	DQ15	D19	True_of_IOB142B	True	U11
IOB142B/D18	I/O	4	DQ15	D18	Comp_of_IOB142A	True	V11
IOB144A/D17	I/O	4	DQ15	D17	True_of_IOB144B	True	U9
IOB144B/D16	I/O	4	DQ15	D16	Comp_of_IOB144A	True	V9
IOB146A	I/O	4	none			none	L14
IOB169A/TDO	I/O	10	none	TDO	True_of_IOB169B	True	T8
IOB169B/TMS	I/O	10	none	TMS	Comp_of_IOB169A	True	R8
IOB171A/READY	I/O	10	none	READY	True_of_IOB171B	True	T10
IOB171B/DONE	I/O	10	none	DONE	Comp_of_IOB171A	True	F12
IOB173A/TCK	I/O	10	none	TCK	True_of_IOB173B	True	F8
IOB173B/TDI	I/O	10	none	TDI	Comp_of_IOB173A	True	T9
IOB175A/MODE0	I/O	10	none	MODE0	True_of_IOB175B	True	R12
IOB175B/CCLK	I/O	10	none	CCLK	Comp_of_IOB175A	True	E8
IOB177A/MODE1	I/O	10	none	MODE1	True_of_IOB177B	True	R11
IOB177B/MODE2	I/O	10	none	MODE2	Comp_of_IOB177A	True	F13
IOB179A/CFGBVS	I/O	10	none	CFGBVS	True_of_IOB179B	True	E12
IOB179B/RECONFIG_N	I/O	10	none	RECONFIG_N	Comp_of_IOB179A	True	P10

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
IOB37A	I/O	5	none			none	R6
IOB38A	I/O	5	DQ8		True_of_IOB38B	True	U4
IOB38B	I/O	5	DQ8		Comp_of_IOB38A	True	V4
IOB40A	I/O	5	DQ8		True_of_IOB40B	True	U6
IOB40B	I/O	5	DQ8		Comp_of_IOB40A	True	U5
IOB42A	I/O	5	DQS8		True_of_IOB42B	True	T4
IOB42B	I/O	5	DQS8		Comp_of_IOB42A	True	T3
IOB44A	I/O	5	DQ8		True_of_IOB44B	True	R7
IOB44B	I/O	5	DQ8		Comp_of_IOB44A	True	T7
IOB47A	I/O	5	DQ8		True_of_IOB47B	True	U7
IOB47B	I/O	5	DQ8		Comp_of_IOB47A	True	V6
IOB49A	I/O	5	DQ8		True_of_IOB49B	True	V8
IOB49B	I/O	5	DQ8		Comp_of_IOB49A	True	V7
IOB92A	I/O	4	none			none	U10
IOB93A/D00/MOSI	I/O	4	DQ12	D00/MOSI	True_of_IOB93B	True	K16
IOB93B/D01/DIN	I/O	4	DQ12	D01/DIN	Comp_of_IOB93A	True	L17
IOB95A/D02	I/O	4	DQ12	D02	True_of_IOB95B	True	J15
IOB95B/D03	I/O	4	DQ12	D03	Comp_of_IOB95A	True	J16
IOB97A/D04	I/O	4	DQ12	D04	True_of_IOB97B	True	K17
IOB97B/D05/SI	I/O	4	DQ12	D05/SI	Comp_of_IOB97A	True	L18
IOB99A/D06/SSPI_CLK	I/O	4	DQ12	D06/SSPI_CLK	True_of_IOB99B	True	J14
IOB99B/D07/SSPI_WPN	I/O	4	DQ12	D07/SSPI_WPN	Comp_of_IOB99A	True	K15
IOL29A/MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	I/O	7	DQ2	MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	True_of_IOL29B	True	P4
IOL29B/MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	I/O	7	DQ2	MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	Comp_of_IOL29A	True	P3
IOL35A	I/O	7	DQS2		True_of_IOL35B	True	N1
IOL35B	I/O	7	DQS2		Comp_of_IOL35A	True	P1
IOL73A	I/O	6	none			none	J6
IOL83A/MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	I/O	6	DQ6	MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	True_of_IOL83B	True	R2

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
IOL83B/MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	I/O	6	DQ6	MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	Comp_of_IOL83A	True	R1
IOL85A/SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	I/O	6	DQ6	SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	True_of_IOL85B	True	R3
IOL85B/SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	I/O	6	DQ6	SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	Comp_of_IOL85A	True	T2
IOL87A	I/O	6	DQ6		True_of_IOL87B	True	V3
IOL87B	I/O	6	DQ6		Comp_of_IOL87A	True	V2
IOL89A	I/O	6	DQ6		True_of_IOL89B	True	P6
IOL89B	I/O	6	DQ6		Comp_of_IOL89A	True	P5
IOL92A	I/O	6	DQ6		True_of_IOL92B	True	R5
IOL92B	I/O	6	DQ6		Comp_of_IOL92A	True	T5
IOL94A	I/O	6	DQS6		True_of_IOL94B	True	U2
IOL94B	I/O	6	DQS6		Comp_of_IOL94A	True	U1
IOR101A	I/O	3	DQ16		True_of_IOR101B	True	G14
IOR101B	I/O	3	DQ16		Comp_of_IOR101A	True	F14
IOR103A	I/O	3	DQS16		True_of_IOR103B	True	G15
IOR103B	I/O	3	DQS16		Comp_of_IOR103A	True	F15
IOR105A	I/O	3	DQ16		True_of_IOR105B	True	H17
IOR105B	I/O	3	DQ16		Comp_of_IOR105A	True	H18
IOR107A/ADCINCK1	I/O	3	DQ16	ADCINCK1	True_of_IOR107B	True	F17
IOR107B	I/O	3	DQ16		Comp_of_IOR107A	True	E18
IOR109A	I/O	3	none			none	H14
IOR11A	I/O	2	DQ23		True_of_IOR11B	True	B10
IOR11B	I/O	2	DQ23		Comp_of_IOR11A	True	A10
IOR13A	I/O	2	DQ23		True_of_IOR13B	True	D11
IOR13B	I/O	2	DQ23		Comp_of_IOR13A	True	C12
IOR1A	I/O	2	none			none	D10
IOR29A/SGCLKT_0/RPLL0_T_IN0/RPLL1_T_IN0	I/O	2	DQ21	SGCLKT_0/RPLL0_T_IN0/RPLL1_T_IN0	True_of_IOR29B	True	D13

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
IOR29B/SGCLKC_0/RPLL0_C_IN0/RPLL1_C_IN0	I/O	2	DQ21	SGCLKC_0/RPLL0_C_IN0/RPLL1_C_IN0	Comp_of_IOR29A	True	C13
IOR2A	I/O	2	DQ23		True_of_IOR2B	True	D8
IOR2B	I/O	2	DQ23		Comp_of_IOR2A	True	C8
IOR31A/MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	I/O	2	DQ21	MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	True_of_IOR31B	True	E13
IOR31B/MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	I/O	2	DQ21	MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	Comp_of_IOR31A	True	D14
IOR33A	I/O	2	DQ21		True_of_IOR33B	True	B12
IOR33B	I/O	2	DQ21		Comp_of_IOR33A	True	A12
IOR35A	I/O	2	DQS21		True_of_IOR35B	True	C14
IOR35B	I/O	2	DQS21		Comp_of_IOR35A	True	B15
IOR38A	I/O	2	DQ21		True_of_IOR38B	True	A13
IOR38B	I/O	2	DQ21		Comp_of_IOR38A	True	A14
IOR40A	I/O	2	DQ21		True_of_IOR40B	True	B14
IOR40B	I/O	2	DQ21		Comp_of_IOR40A	True	A15
IOR4A	I/O	2	DQ23		True_of_IOR4B	True	D9
IOR4B	I/O	2	DQ23		Comp_of_IOR4A	True	C9
IOR69A	I/O	3	DQ18		True_of_IOR69B	True	C16
IOR69B	I/O	3	DQ18		Comp_of_IOR69A	True	B17
IOR6A	I/O	2	DQ23		True_of_IOR6B	True	C11
IOR6B	I/O	2	DQ23		Comp_of_IOR6A	True	B11
IOR71A	I/O	3	DQ18		True_of_IOR71B	True	E17
IOR71B	I/O	3	DQ18		Comp_of_IOR71A	True	D18
IOR74A	I/O	3	DQ18		True_of_IOR74B	True	C17
IOR74B	I/O	3	DQ18		Comp_of_IOR74A	True	C18
IOR76A	I/O	3	DQS18		True_of_IOR76B	True	B16
IOR76B	I/O	3	DQS18		Comp_of_IOR76A	True	A17
IOR78A/SGCLKT_3/RPLL2_T_FB0/RPLL3_T_FB0	I/O	3	DQ18	SGCLKT_3/RPLL2_T_FB0/RPLL3_T_FB0	True_of_IOR78B	True	E16

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
IOR78B/SGCLKC_3/RPLL2_C_FB0/RPLL3_C_FB0	I/O	3	DQ18	SGCLKC_3/RPLL2_C_FB0/RPLL3_C_FB0	Comp_of_IOR78A	True	D16
IOR80A/MGCLKT_3/RPLL2_T_FB1/RPLL3_T_FB1	I/O	3	DQ18	MGCLKT_3/RPLL2_T_FB1/RPLL3_T_FB1	True_of_IOR80B	True	E15
IOR80B/MGCLKC_3/RPLL2_C_FB1/RPLL3_C_FB1	I/O	3	DQ18	MGCLKC_3/RPLL2_C_FB1/RPLL3_C_FB1	Comp_of_IOR80A	True	D15
IOR8A	I/O	2	DQS23		True_of_IOR8B	True	B9
IOR8B	I/O	2	DQS23		Comp_of_IOR8A	True	A9
IOR96A	I/O	3	DQ16		True_of_IOR96B	True	G17
IOR96B	I/O	3	DQ16		Comp_of_IOR96A	True	F18
IOR98A	I/O	3	DQ16		True_of_IOR98B	True	H16
IOR98B	I/O	3	DQ16		Comp_of_IOR98A	True	G16
M0_CKN	DIO	MIPI	none			none	L4
M0_CKP	DIO	MIPI	none			none	L3
M0_D0N	DIO	MIPI	none			none	K5
M0_D0P	DIO	MIPI	none			none	K6
M0_D1N	DIO	MIPI	none			none	J4
M0_D1P	DIO	MIPI	none			none	J5
M0_D2N	DIO	MIPI	none			none	K1
M0_D2P	DIO	MIPI	none			none	K2
M0_D3N	DIO	MIPI	none			none	K3
M0_D3P	DIO	MIPI	none			none	L2
M1_CKN	DIO	MIPI	none			none	N6
M1_CKP	DIO	MIPI	none			none	M6
M1_D0N	DIO	MIPI	none			none	M1
M1_D0P	DIO	MIPI	none			none	M2
M1_D1N	DIO	MIPI	none			none	N3
M1_D1P	DIO	MIPI	none			none	N2
M1_D2N	DIO	MIPI	none			none	L5
M1_D2P	DIO	MIPI	none			none	M5
M1_D3N	DIO	MIPI	none			none	M4

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
M1_D3P	DIO	MIPI	none			none	N4
Q0_LN0_RXM_I	DIO	Q0	none			none	E3
Q0_LN0_RXP_I	DIO	Q0	none			none	E4
Q0_LN0_TXM_O	DIO	Q0	none			none	H1
Q0_LN0_TXP_O	DIO	Q0	none			none	H2
Q0_LN1_RXM_I	DIO	Q0	none			none	A3
Q0_LN1_RXP_I	DIO	Q0	none			none	A4
Q0_LN1_TXM_O	DIO	Q0	none			none	F1
Q0_LN1_TXP_O	DIO	Q0	none			none	F2
Q0_LN2_RXM_I	DIO	Q0	none			none	C3
Q0_LN2_RXP_I	DIO	Q0	none			none	C4
Q0_LN2_TXM_O	DIO	Q0	none			none	D1
Q0_LN2_TXP_O	DIO	Q0	none			none	D2
Q0_LN3_RXM_I	DIO	Q0	none			none	G3
Q0_LN3_RXP_I	DIO	Q0	none			none	G4
Q0_LN3_TXM_O	DIO	Q0	none			none	B1
Q0_LN3_TXP_O	DIO	Q0	none			none	B2
Q0_REFCLKM_0	DIO	Q0	none			none	D5
Q0_REFCLKM_1	DIO	Q0	none			none	B5
Q0_REFCLKP_0	DIO	Q0	none			none	D6
Q0_REFCLKP_1	DIO	Q0	none			none	B6
ADCTN	DIO	ADC	none			none	M9
ADCTP	DIO	ADC	none			none	M10
ADCVN	DIO	ADC	none			none	L9
ADCVP	DIO	ADC	none			none	K10
VDDXM	Power	N/A					J10
VDDAM_VDDDM	Power	N/A					F11
VDDAM_VDDDM	Power	N/A					H11
VDDAM_VDDDM	Power	N/A					G10
VDDAQ0	Power	N/A					E5
VDDAQ0	Power	N/A					F5

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
VDDHAQ0	Power	N/A					G12
VDDTQ0	Power	N/A					B4
VDDTQ0	Power	N/A					C5
VCC	Power	N/A					P9
VCC	Power	N/A					K7
VCC	Power	N/A					L12
VCC	Power	N/A					N8
VCC	Power	N/A					N10
VCC	Power	N/A					F9
VCC	Power	N/A					G8
VCC	Power	N/A					H7
VCC	Power	N/A					P11
VCC	Power	N/A					F7
VCC	Power	N/A					L8
VCC	Power	N/A					K11
VCC	Power	N/A					M7
VCC	Power	N/A					J8
VCC	Power	N/A					H9
VCC	Power	N/A					M11
VCC	Power	N/A					J12
VCC	Power	N/A					N12
VCCLDO	Power	N/A					H13
VCCIO10	Power	N/A					E10
VCCIO10	Power	N/A					R10
VCCX	Power	N/A					K13
VCCX	Power	N/A					M13
VCCX	Power	N/A					P13
VCCIO2	Power	N/A					A16
VCCIO2	Power	N/A					C10
VCCIO2	Power	N/A					D17
VCCIO2	Power	N/A					B13

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
VCCIO3	Power	N/A					H15
VCCIO3	Power	N/A					G18
VCCIO3	Power	N/A					E14
VCCIO4	Power	N/A					T11
VCCIO4	Power	N/A					P17
VCCIO4	Power	N/A					V15
VCCIO4	Power	N/A					R14
VCCIO4	Power	N/A					L16
VCCIO4	Power	N/A					U8
VCCIO4	Power	N/A					U18
VCCIO5_6_7	Power	N/A					T1
VCCIO5_6_7	Power	N/A					V5
VCCIO5_6_7	Power	N/A					P7
VCCIO5_6_7	Power	N/A					R4
VCCIO5_6_7	Power	N/A					L6
VCCIO5_6_7	Power	N/A					M3
VSS	Ground	N/A					A1
VSS	Ground	N/A					A5
VSS	Ground	N/A					A7
VSS	Ground	N/A					A8
VSS	Ground	N/A					A11
VSS	Ground	N/A					A18
VSS	Ground	N/A					B3
VSS	Ground	N/A					B7
VSS	Ground	N/A					B8
VSS	Ground	N/A					B18
VSS	Ground	N/A					C2
VSS	Ground	N/A					C6
VSS	Ground	N/A					C7
VSS	Ground	N/A					C15
VSS	Ground	N/A					D3

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
VSS	Ground	N/A					D4
VSS	Ground	N/A					D7
VSS	Ground	N/A					D12
VSS	Ground	N/A					E2
VSS	Ground	N/A					E6
VSS	Ground	N/A					E7
VSS	Ground	N/A					E9
VSS	Ground	N/A					F4
VSS	Ground	N/A					F6
VSS	Ground	N/A					F10
VSS	Ground	N/A					F16
VSS	Ground	N/A					G1
VSS	Ground	N/A					G5
VSS	Ground	N/A					G6
VSS	Ground	N/A					G7
VSS	Ground	N/A					G9
VSS	Ground	N/A					G11
VSS	Ground	N/A					G13
VSS	Ground	N/A					H3
VSS	Ground	N/A					H4
VSS	Ground	N/A					H5
VSS	Ground	N/A					H6
VSS	Ground	N/A					H8
VSS	Ground	N/A					H10
VSS	Ground	N/A					H12
VSS	Ground	N/A					J1
VSS	Ground	N/A					J2
VSS	Ground	N/A					J3
VSS	Ground	N/A					J7
VSS	Ground	N/A					J11
VSS	Ground	N/A					J13

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
VSS	Ground	N/A					J17
VSS	Ground	N/A					K4
VSS	Ground	N/A					K8
VSS	Ground	N/A					K12
VSS	Ground	N/A					K14
VSS	Ground	N/A					L1
VSS	Ground	N/A					L7
VSS	Ground	N/A					L11
VSS	Ground	N/A					L13
VSS	Ground	N/A					M8
VSS	Ground	N/A					M12
VSS	Ground	N/A					M18
VSS	Ground	N/A					N5
VSS	Ground	N/A					N7
VSS	Ground	N/A					N9
VSS	Ground	N/A					N11
VSS	Ground	N/A					N13
VSS	Ground	N/A					N15
VSS	Ground	N/A					P2
VSS	Ground	N/A					P8
VSS	Ground	N/A					P12
VSS	Ground	N/A					R9
VSS	Ground	N/A					T6
VSS	Ground	N/A					T16
VSS	Ground	N/A					U3
VSS	Ground	N/A					U13
VSS	Ground	N/A					V1
VSS	Ground	N/A					V10
VSS	Ground	N/A					V18
VSS	Ground	N/A					J9
NC	N/A	N/A					L10

Note! ^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
NC	N/A	N/A					E11
NC	N/A	N/A					K9
NC	N/A	N/A					A2
NC	N/A	N/A					C1
NC	N/A	N/A					E1
NC	N/A	N/A					F3
NC	N/A	N/A					G2
NC	N/A	N/A					A6

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
IOB102A	I/O	4	DQ12		True_of_IOB102B	True	M16
IOB102B/VREF	I/O	4	DQ12	VREF	Comp_of_IOB102A	True	M17
IOB104A	I/O	4	DQS12		True_of_IOB104B	True	K16
IOB104B	I/O	4	DQS12		Comp_of_IOB104A	True	K17
IOB106A	I/O	4	DQ13		True_of_IOB106B	True	K22
IOB106B	I/O	4	DQ13		Comp_of_IOB106A	True	K23
IOB108A	I/O	4	DQ13		True_of_IOB108B	True	F23
IOB108B	I/O	4	DQ13		Comp_of_IOB108A	True	E23
IOB110A	I/O	4	DQS13		True_of_IOB110B	True	G22
IOB110B	I/O	4	DQS13		Comp_of_IOB110A	True	F22
IOB112A	I/O	4	DQ13		True_of_IOB112B	True	J24
IOB112B	I/O	4	DQ13		Comp_of_IOB112A	True	H24
IOB114A/SGCLKT_4/BPLL2_T_FB1/BPLL3_T_FB1	I/O	4	DQ13	SGCLKT_4/BPLL2_T_FB1/BPLL3_T_FB1	True_of_IOB114B	True	J23
IOB114B/SGCLKC_4/BPLL2_C_FB1/BPLL3_C_FB1	I/O	4	DQ13	SGCLKC_4/BPLL2_C_FB1/BPLL3_C_FB1	Comp_of_IOB114A	True	H23
IOB116A/MGCLKT_4/BPLL2_T_FB0/BPLL3_T_FB0	I/O	4	DQ13	MGCLKT_4/BPLL2_T_FB0/BPLL3_T_FB0	True_of_IOB116B	True	H21
IOB116B/MGCLKC_4/BPLL2_C_FB0/BPLL3_C_FB0	I/O	4	DQ13	MGCLKC_4/BPLL2_C_FB0/BPLL3_C_FB0	Comp_of_IOB116A	True	H22
IOB120A/SGCLKT_5/BPLL2_T_IN0/BPLL3_T_IN0	I/O	4	DQ14	SGCLKT_5/BPLL2_T_IN0/BPLL3_T_IN0	True_of_IOB120B	True	G20
IOB120B/SGCLKC_5/BPLL2_C_IN0/BPLL3_C_IN0	I/O	4	DQ14	SGCLKC_5/BPLL2_C_IN0/BPLL3_C_IN0	Comp_of_IOB120A	True	G21
IOB122A/MGCLKT_5/BPLL2_T_IN1/BPLL3_T_IN1	I/O	4	DQ14	MGCLKT_5/BPLL2_T_IN1/BPLL3_T_IN1	True_of_IOB122B	True	K21
IOB122B/MGCLKC_5/BPLL2_C_IN1/BPLL3_C_IN1	I/O	4	DQ14	MGCLKC_5/BPLL2_C_IN1/BPLL3_C_IN1	Comp_of_IOB122A	True	J21
IOB124A	I/O	4	DQ14		True_of_IOB124B	True	L17
IOB124B	I/O	4	DQ14		Comp_of_IOB124A	True	L18

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
IOB126A	I/O	4	DQ14		True_of_IOB126B	True	J19
IOB126B	I/O	4	DQ14		Comp_of_IOB126A	True	H19
IOB129A	I/O	4	DQ14		True_of_IOB129B	True	J18
IOB129B	I/O	4	DQ14		Comp_of_IOB129A	True	H18
IOB131A	I/O	4	DQS14		True_of_IOB131B	True	K20
IOB131B	I/O	4	DQS14		Comp_of_IOB131A	True	J20
IOB133A	I/O	4	DQ15		True_of_IOB133B	True	E25
IOB133B/VREF	I/O	4	DQ15	VREF	Comp_of_IOB133A	True	D25
IOB135A	I/O	4	DQS15		True_of_IOB135B	True	E26
IOB135B	I/O	4	DQS15		Comp_of_IOB135A	True	D26
IOB138A	I/O	4	DQ15		True_of_IOB138B	True	H26
IOB138B	I/O	4	DQ15		Comp_of_IOB138A	True	G26
IOB140A	I/O	4	DQ15		True_of_IOB140B	True	G24
IOB140B	I/O	4	DQ15		Comp_of_IOB140A	True	F24
IOB142A	I/O	4	DQ15		True_of_IOB142B	True	J25
IOB142B	I/O	4	DQ15		Comp_of_IOB142A	True	J26
IOB144A	I/O	4	DQ15		True_of_IOB144B	True	G25
IOB144B	I/O	4	DQ15		Comp_of_IOB144A	True	F25
IOB146A	I/O	4	none			none	K18
IOB169A/TDO	I/O	10	none	TDO	True_of_IOB169B	True	J10
IOB169B/TMS	I/O	10	none	TMS	Comp_of_IOB169A	True	H11
IOB171A/READY	I/O	10	none	READY	True_of_IOB171B	True	V11
IOB171B/DONE	I/O	10	none	DONE	Comp_of_IOB171A	True	W10
IOB173A/TCK	I/O	10	none	TCK	True_of_IOB173B	True	H12
IOB173B/TDI	I/O	10	none	TDI	Comp_of_IOB173A	True	H10
IOB175A/MODE0	I/O	10	none	MODE0	True_of_IOB175B	True	AB7
IOB175B/CCLK	I/O	10	none	CCLK	Comp_of_IOB175A	True	H13
IOB177A/MODE1	I/O	10	none	MODE1	True_of_IOB177B	True	Y9
IOB177B/MODE2	I/O	10	none	MODE2	Comp_of_IOB177A	True	W9
IOB179A/CFGBVS	I/O	10	none	CFGBVS	True_of_IOB179B	True	AB15

Note!							
^[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
IOB179B/RECONFIG_N	I/O	10	none	RECONFIG_N	Comp_of_IOB179A	True	AE16
IOB37A	I/O	5	none			none	E22
IOB38A	I/O	5	DQ8		True_of_IOB38B	True	D23
IOB38B	I/O	5	DQ8		Comp_of_IOB38A	True	D24
IOB40A	I/O	5	DQ8		True_of_IOB40B	True	C24
IOB40B	I/O	5	DQ8		Comp_of_IOB40A	True	B24
IOB42A	I/O	5	DQS8		True_of_IOB42B	True	A23
IOB42B	I/O	5	DQS8		Comp_of_IOB42A	True	A24
IOB44A	I/O	5	DQ8		True_of_IOB44B	True	B25
IOB44B	I/O	5	DQ8		Comp_of_IOB44A	True	A25
IOB47A	I/O	5	DQ8		True_of_IOB47B	True	C26
IOB47B	I/O	5	DQ8		Comp_of_IOB47A	True	B26
IOB49A	I/O	5	DQ8		True_of_IOB49B	True	C22
IOB49B/VREF	I/O	5	DQ8	VREF	Comp_of_IOB49A	True	C23
IOB51A	I/O	5	DQ9		True_of_IOB51B	True	C17
IOB51B	I/O	5	DQ9		Comp_of_IOB51A	True	B17
IOB53A	I/O	5	DQ9		True_of_IOB53B	True	E16
IOB53B	I/O	5	DQ9		Comp_of_IOB53A	True	D16
IOB56A	I/O	5	DQS9		True_of_IOB56B	True	A17
IOB56B	I/O	5	DQS9		Comp_of_IOB56A	True	A18
IOB58A	I/O	5	DQ9		True_of_IOB58B	True	B19
IOB58B	I/O	5	DQ9		Comp_of_IOB58A	True	A19
IOB60A/SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	I/O	5	DQ9	SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	True_of_IOB60B	True	E17
IOB60BSGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	I/O	5	DQ9	SGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	Comp_of_IOB60A	True	E18
IOB62A/MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	I/O	5	DQ9	MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	True_of_IOB62B	True	D18
IOB62B/MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	I/O	5	DQ9	MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	Comp_of_IOB62A	True	C18

Note!							
^[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
IOB66A/MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	I/O	5	DQ10	MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	True_of_IOB66B	True	D19
IOB66B/MGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	I/O	5	DQ10	MGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	Comp_of_IOB66A	True	C19
IOB68A/SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	I/O	5	DQ10	SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	True_of_IOB68B	True	E20
IOB68B/SGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	I/O	5	DQ10	SGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	Comp_of_IOB68A	True	D20
IOB70A	I/O	5	DQS10		True_of_IOB70B	True	B20
IOB70B	I/O	5	DQS10		Comp_of_IOB70A	True	A20
IOB72A	I/O	5	DQ10		True_of_IOB72B	True	C21
IOB72B	I/O	5	DQ10		Comp_of_IOB72A	True	B21
IOB74A	I/O	5	DQ10		True_of_IOB74B	True	B22
IOB74B	I/O	5	DQ10		Comp_of_IOB74A	True	A22
IOB76A	I/O	5	DQ10		True_of_IOB76B	True	E21
IOB76B	I/O	5	DQ10		Comp_of_IOB76A	True	D21
IOB78A	I/O	5	DQ11		True_of_IOB78B	True	G15
IOB78B	I/O	5	DQ11		Comp_of_IOB78A	True	F15
IOB80A	I/O	5	DQ11		True_of_IOB80B	True	G17
IOB80B	I/O	5	DQ11		Comp_of_IOB80A	True	F17
IOB83A	I/O	5	DQ11		True_of_IOB83B	True	H14
IOB83B	I/O	5	DQ11		Comp_of_IOB83A	True	H15
IOB85A	I/O	5	DQ11		True_of_IOB85B	True	H16
IOB85B/VREF	I/O	5	DQ11	VREF	Comp_of_IOB85A	True	G16
IOB87A	I/O	5	DQ11		True_of_IOB87B	True	G19
IOB87B	I/O	5	DQ11		Comp_of_IOB87A	True	F20
IOB89A	I/O	5	DQS11		True_of_IOB89B	True	F18
IOB89B	I/O	5	DQS11		Comp_of_IOB89A	True	F19
IOB91A	I/O	5	none			none	H17
IOB92A	I/O	4	none			none	L19

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
IOB93A	I/O	4	DQ12		True_of_IOB93B	True	M14
IOB93B	I/O	4	DQ12		Comp_of_IOB93A	True	L14
IOB95A/ADCINCK1	I/O	4	DQ12	ADCINCK1	True_of_IOB95B	True	M15
IOB95B	I/O	4	DQ12		Comp_of_IOB95A	True	L15
IOB97A	I/O	4	DQ12		True_of_IOB97B	True	J14
IOB97B	I/O	4	DQ12		Comp_of_IOB97A	True	J15
IOB99A	I/O	4	DQ12		True_of_IOB99B	True	K15
IOB99B	I/O	4	DQ12		Comp_of_IOB99A	True	J16
IOL101A	I/O	6	DQ7		True_of_IOL101B	True	E6
IOL101B	I/O	6	DQ7		Comp_of_IOL101A	True	D6
IOL103A	I/O	6	DQ7		True_of_IOL103B	True	H8
IOL103B	I/O	6	DQ7		Comp_of_IOL103A	True	G8
IOL105A	I/O	6	DQ7		True_of_IOL105B	True	F8
IOL105B	I/O	6	DQ7		Comp_of_IOL105A	True	F7
IOL107A	I/O	6	DQ7		True_of_IOL107B	True	H9
IOL107B/VREF	I/O	6	DQ7	VREF	Comp_of_IOL107A	True	G9
IOL109A	I/O	6	none			none	J8
IOL11A	I/O	7	DQ0		True_of_IOL11B	True	P6
IOL11B/VREF	I/O	7	DQ0	VREF	Comp_of_IOL11A	True	P5
IOL13A	I/O	7	DQ0		True_of_IOL13B	True	R8
IOL13B	I/O	7	DQ0		Comp_of_IOL13A	True	P8
IOL15A	I/O	7	DQ1		True_of_IOL15B	True	U2
IOL15B	I/O	7	DQ1		Comp_of_IOL15A	True	U1
IOL17A	I/O	7	DQ1		True_of_IOL17B	True	T2
IOL17B	I/O	7	DQ1		Comp_of_IOL17A	True	R2
IOL1A	I/O	7	none			none	U4
IOL20A	I/O	7	DQ1		True_of_IOL20B	True	T4
IOL20B	I/O	7	DQ1		Comp_of_IOL20A	True	T3
IOL22A	I/O	7	DQS1		True_of_IOL22B	True	R1
IOL22B	I/O	7	DQS1		Comp_of_IOL22A	True	P1

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
IOL24A/MGCLKT_11/LPLL0_T_IN1/LPLL1_T_IN1	I/O	7	DQ1	MGCLKT_11/LPLL0_T_IN1/LPLL1_T_IN1	True_of_IOL24B	True	R3
IOL24B/MGCLKC_11/LPLL0_C_IN1/LPLL1_C_IN1	I/O	7	DQ1	MGCLKC_11/LPLL0_C_IN1/LPLL1_C_IN1	Comp_of_IOL24A	True	P3
IOL26A/SGCLKT_11/LPLL0_T_IN0/LPLL1_T_IN0	I/O	7	DQ1	SGCLKT_11/LPLL0_T_IN0/LPLL1_T_IN0	True_of_IOL26B	True	P4
IOL26B/SGCLKC_11/LPLL0_C_IN0/LPLL1_C_IN0	I/O	7	DQ1	SGCLKC_11/LPLL0_C_IN0/LPLL1_C_IN0	Comp_of_IOL26A	True	N4
IOL29A/MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	I/O	7	DQ2	MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	True_of_IOL29B	True	N3
IOL29B/MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	I/O	7	DQ2	MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	Comp_of_IOL29A	True	N2
IOL2A/ADCINCK0	I/O	7	DQ0	ADCINCK0	True_of_IOL2B	True	T8
IOL2B	I/O	7	DQ0		Comp_of_IOL2A	True	T7
IOL31A/SGCLKT_10/LPLL0_T_FB0/LPLL1_T_FB0	I/O	7	DQ2	SGCLKT_10/LPLL0_T_FB0/LPLL1_T_FB0	True_of_IOL31B	True	M2
IOL31B/SGCLKC_10/LPLL0_C_FB0/LPLL1_C_FB0	I/O	7	DQ2	SGCLKC_10/LPLL0_C_FB0/LPLL1_C_FB0	Comp_of_IOL31A	True	L2
IOL33A	I/O	7	DQ2		True_of_IOL33B	True	K1
IOL33B	I/O	7	DQ2		Comp_of_IOL33A	True	J1
IOL35A	I/O	7	DQS2		True_of_IOL35B	True	N1
IOL35B	I/O	7	DQS2		Comp_of_IOL35A	True	M1
IOL38A	I/O	7	DQ2		True_of_IOL38B	True	L3
IOL38B	I/O	7	DQ2		Comp_of_IOL38A	True	K2
IOL40A	I/O	7	DQ2		True_of_IOL40B	True	H2
IOL40B	I/O	7	DQ2		Comp_of_IOL40A	True	H1
IOL42A	I/O	7	DQ3		True_of_IOL42B	True	N7
IOL42B	I/O	7	DQ3		Comp_of_IOL42A	True	N6
IOL44A	I/O	7	DQ3		True_of_IOL44B	True	M6
IOL44B/VREF	I/O	7	DQ3	VREF	Comp_of_IOL44A	True	M5

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
IOL47A	I/O	7	DQ3		True_of_IOL47B	True	L5
IOL47B	I/O	7	DQ3		Comp_of_IOL47A	True	K5
IOL49A	I/O	7	DQS3		True_of_IOL49B	True	M4
IOL49B	I/O	7	DQS3		Comp_of_IOL49A	True	L4
IOL4A	I/O	7	DQS0		True_of_IOL4B	True	U6
IOL4B	I/O	7	DQS0		Comp_of_IOL4A	True	U5
IOL51A	I/O	7	DQ3		True_of_IOL51B	True	K3
IOL51B	I/O	7	DQ3		Comp_of_IOL51A	True	J3
IOL53A	I/O	7	DQ3		True_of_IOL53B	True	M7
IOL53B	I/O	7	DQ3		Comp_of_IOL53A	True	L7
IOL55A	I/O	7	none			none	N8
IOL56A	I/O	6	DQ4		True_of_IOL56B	True	G2
IOL56B	I/O	6	DQ4		Comp_of_IOL56A	True	G1
IOL58A	I/O	6	DQ4		True_of_IOL58B	True	F2
IOL58B	I/O	6	DQ4		Comp_of_IOL58A	True	E2
IOL60A	I/O	6	DQ4		True_of_IOL60B	True	C2
IOL60B/VREF	I/O	6	DQ4	VREF	Comp_of_IOL60A	True	B2
IOL62A	I/O	6	DQ4		True_of_IOL62B	True	E1
IOL62B	I/O	6	DQ4		Comp_of_IOL62A	True	D1
IOL65A	I/O	6	DQS4		True_of_IOL65B	True	C1
IOL65B	I/O	6	DQS4		Comp_of_IOL65A	True	B1
IOL67A	I/O	6	DQ4		True_of_IOL67B	True	A3
IOL67B	I/O	6	DQ4		Comp_of_IOL67A	True	A2
IOL69A	I/O	6	DQS5		True_of_IOL69B	True	J4
IOL69B	I/O	6	DQS5		Comp_of_IOL69A	True	H4
IOL6A	I/O	7	DQ0		True_of_IOL6B	True	T5
IOL6B	I/O	7	DQ0		Comp_of_IOL6A	True	R5
IOL71A	I/O	6	DQ5		True_of_IOL71B	True	K7
IOL71B	I/O	6	DQ5		Comp_of_IOL71A	True	K6
IOL73A	I/O	6	none			none	H3

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
IOL74A	I/O	6	DQ5		True_of_IOL74B	True	J6
IOL74B	I/O	6	DQ5		Comp_of_IOL74A	True	J5
IOL76A	I/O	6	DQ5		True_of_IOL76B	True	L8
IOL76B	I/O	6	DQ5		Comp_of_IOL76A	True	K8
IOL78A/SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	I/O	6	DQ5	SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	True_of_IOL78B	True	G4
IOL78B/SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	I/O	6	DQ5	SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	Comp_of_IOL78A	True	F4
IOL80A/MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	I/O	6	DQ5	MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	True_of_IOL80B	True	G5
IOL80B/MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	I/O	6	DQ5	MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	Comp_of_IOL80A	True	F5
IOL83A/MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	I/O	6	DQ6	MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	True_of_IOL83B	True	E5
IOL83B/MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	I/O	6	DQ6	MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	Comp_of_IOL83A	True	D5
IOL85A/SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	I/O	6	DQ6	SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	True_of_IOL85B	True	D4
IOL85B/SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	I/O	6	DQ6	SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	Comp_of_IOL85A	True	C4
IOL87A	I/O	6	DQ6		True_of_IOL87B	True	B4
IOL87B	I/O	6	DQ6		Comp_of_IOL87A	True	A4
IOL89A	I/O	6	DQ6		True_of_IOL89B	True	F3
IOL89B	I/O	6	DQ6		Comp_of_IOL89A	True	E3
IOL8A	I/O	7	DQ0		True_of_IOL8B	True	R7
IOL8B	I/O	7	DQ0		Comp_of_IOL8A	True	R6
IOL92A	I/O	6	DQ6		True_of_IOL92B	True	D3
IOL92B	I/O	6	DQ6		Comp_of_IOL92A	True	C3
IOL94A	I/O	6	DQS6		True_of_IOL94B	True	B5
IOL94B	I/O	6	DQS6		Comp_of_IOL94A	True	A5

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
IOL96A	I/O	6	DQS7		True_of_IOL96B	True	H7
IOL96B	I/O	6	DQS7		Comp_of_IOL96A	True	G7
IOL98A	I/O	6	DQ7		True_of_IOL98B	True	H6
IOL98B	I/O	6	DQ7		Comp_of_IOL98A	True	G6
IOR101A/D04	I/O	3	DQ16	D04	True_of_IOR101B	True	N16
IOR101B/D05/SI	I/O	3	DQ16	D05/SI	Comp_of_IOR101A	True	N17
IOR103A/PUDC_B	I/O	3	DQS16	PUDC_B	True_of_IOR103B	True	P15
IOR103B/EMCCLK	I/O	3	DQS16	EMCCLK	Comp_of_IOR103A	True	P16
IOR105A/D00/MOSI	I/O	3	DQ16	D00/MOSI	True_of_IOR105B	True	R14
IOR105B/D01/DIN	I/O	3	DQ16	D01/DIN	Comp_of_IOR105A	True	R15
IOR107A/D02/ADCINCK1	I/O	3	DQ16	D02/ADCINCK1	True_of_IOR107B	True	P14
IOR107B/D03	I/O	3	DQ16	D03	Comp_of_IOR107A	True	N14
IOR109A	I/O	3	none			none	M19
IOR11A	I/O	2	DQ23		True_of_IOR11B	True	U14
IOR11B	I/O	2	DQ23		Comp_of_IOR11A	True	V14
IOR13A	I/O	2	DQ23		True_of_IOR13B	True	T14
IOR13B	I/O	2	DQ23		Comp_of_IOR13A	True	T15
IOR15A	I/O	2	DQ22		True_of_IOR15B	True	V19
IOR15B	I/O	2	DQ22		Comp_of_IOR15A	True	W19
IOR17A	I/O	2	DQ22		True_of_IOR17B	True	W20
IOR17B	I/O	2	DQ22		Comp_of_IOR17A	True	Y20
IOR1A	I/O	2	none			none	U17
IOR20A	I/O	2	DQS22		True_of_IOR20B	True	T20
IOR20B	I/O	2	DQS22		Comp_of_IOR20A	True	U20
IOR22A	I/O	2	DQ22		True_of_IOR22B	True	T19
IOR22B	I/O	2	DQ22		Comp_of_IOR22A	True	U19
IOR24A/SGCLKT_1/RPLL0_T_FB0/RPLL1_T_FB0	I/O	2	DQ22	SGCLKT_1/RPLL0_T_FB0/RPLL1_T_FB0	True_of_IOR24B	True	W21
IOR24B/SGCLKC_1/RPLL0_C_FB0/RPLL1_C_FB0	I/O	2	DQ22	SGCLKC_1/RPLL0_C_FB0/RPLL1_C_FB0	Comp_of_IOR24A	True	Y21

Note!							
^[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
IOR26A/MGCLKT_1/RPLL0_T_FB1/RPLL1_T_FB1	I/O	2	DQ22	MGCLKT_1/RPLL0_T_FB1/RPLL1_T_FB1	True_of_IOR26B	True	U21
IOR26B/MGCLKC_1/RPLL0_C_FB1/RPLL1_C_FB1	I/O	2	DQ22	MGCLKC_1/RPLL0_C_FB1/RPLL1_C_FB1	Comp_of_IOR26A	True	V21
IOR29A/SGCLKT_0/RPLL0_T_IN0/RPLL1_T_IN0	I/O	2	DQ21	SGCLKT_0/RPLL0_T_IN0/RPLL1_T_IN0	True_of_IOR29B	True	Y22
IOR29B/SGCLKC_0/RPLL0_C_IN0/RPLL1_C_IN0	I/O	2	DQ21	SGCLKC_0/RPLL0_C_IN0/RPLL1_C_IN0	Comp_of_IOR29A	True	Y23
IOR2A	I/O	2	DQ23		True_of_IOR2B	True	V16
IOR2B	I/O	2	DQ23		Comp_of_IOR2A	True	V17
IOR31A/MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	I/O	2	DQ21	MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	True_of_IOR31B	True	U22
IOR31B/MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	I/O	2	DQ21	MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	Comp_of_IOR31A	True	V22
IOR33A	I/O	2	DQ21		True_of_IOR33B	True	AA22
IOR33B	I/O	2	DQ21		Comp_of_IOR33A	True	AA23
IOR35A	I/O	2	DQS21		True_of_IOR35B	True	AB24
IOR35B	I/O	2	DQS21		Comp_of_IOR35A	True	AC24
IOR38A	I/O	2	DQ21		True_of_IOR38B	True	V23
IOR38B	I/O	2	DQ21		Comp_of_IOR38A	True	W23
IOR40A	I/O	2	DQ21		True_of_IOR40B	True	AA24
IOR40B	I/O	2	DQ21		Comp_of_IOR40A	True	AB25
IOR42A	I/O	2	DQ20		True_of_IOR42B	True	Y25
IOR42B	I/O	2	DQ20		Comp_of_IOR42A	True	AA25
IOR44A	I/O	2	DQS20		True_of_IOR44B	True	AB26
IOR44B	I/O	2	DQS20		Comp_of_IOR44A	True	AC26
IOR47A	I/O	2	DQ20		True_of_IOR47B	True	V24
IOR47B/VREF	I/O	2	DQ20	VREF	Comp_of_IOR47A	True	W24
IOR49A	I/O	2	DQ20		True_of_IOR49B	True	W25
IOR49B	I/O	2	DQ20		Comp_of_IOR49A	True	Y26

Note!							
^[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
IOR4A	I/O	2	DQ23		True_of_IOR4B	True	V18
IOR4B/VREF	I/O	2	DQ23	VREF	Comp_of_IOR4A	True	W18
IOR51A	I/O	2	DQ20		True_of_IOR51B	True	V26
IOR51B	I/O	2	DQ20		Comp_of_IOR51A	True	W26
IOR53A	I/O	2	DQ20		True_of_IOR53B	True	U25
IOR53B	I/O	2	DQ20		Comp_of_IOR53A	True	U26
IOR55A	I/O	2	none			none	U24
IOR56A/D19	I/O	3	DQ19	D19	True_of_IOR56B	True	T22
IOR56B/D18	I/O	3	DQ19	D18	Comp_of_IOR56A	True	R22
IOR58A/CLKHOLD_N	I/O	3	DQS19	CLKHOLD_N	True_of_IOR58B	True	T24
IOR58B/D22	I/O	3	DQS19	D22	Comp_of_IOR58A	True	T25
IOR60A/D26	I/O	3	DQ19	D26	True_of_IOR60B	True	R25
IOR60B/D25/VREF	I/O	3	DQ19	D25/VREF	Comp_of_IOR60A	True	P25
IOR62A/D17	I/O	3	DQ19	D17	True_of_IOR62B	True	T23
IOR62B/D16	I/O	3	DQ19	D16	Comp_of_IOR62A	True	R23
IOR65A/D21	I/O	3	DQ19	D21	True_of_IOR65B	True	R26
IOR65B/D20	I/O	3	DQ19	D20	Comp_of_IOR65A	True	P26
IOR67A/D24	I/O	3	DQ19	D24	True_of_IOR67B	True	N26
IOR67B/D23	I/O	3	DQ19	D23	Comp_of_IOR67A	True	M26
IOR69A/D28	I/O	3	DQ18	D28	True_of_IOR69B	True	R20
IOR69B/D27	I/O	3	DQ18	D27	Comp_of_IOR69A	True	R21
IOR6A	I/O	2	DQ23		True_of_IOR6B	True	U15
IOR6B	I/O	2	DQ23		Comp_of_IOR6A	True	U16
IOR71A/CSI_B	I/O	3	DQ18	CSI_B	True_of_IOR71B	True	P19
IOR71B/D31	I/O	3	DQ18	D31	Comp_of_IOR71A	True	N19
IOR73A	I/O	3	none			none	R18
IOR74A/D30	I/O	3	DQ18	D30	True_of_IOR74B	True	P23
IOR74B/D29	I/O	3	DQ18	D29	Comp_of_IOR74A	True	P24
IOR76A/RDWR	I/O	3	DQS18	RDWR	True_of_IOR76B	True	N23
IOR76B/DOUT_CSO_B	I/O	3	DQS18	DOUT_CSO_B	Comp_of_IOR76A	True	N24

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
IOR78A/SGCLKT_3/RPLL2_T_FB0/RPLL3_T_FB0	I/O	3	DQ18	SGCLKT_3/RPLL2_T_FB0/RPLL3_T_FB0	True_of_IOR78B	True	P20
IOR78B/SGCLKC_3/RPLL2_C_FB0/RPLL3_C_FB0	I/O	3	DQ18	SGCLKC_3/RPLL2_C_FB0/RPLL3_C_FB0	Comp_of_IOR78A	True	P21
IOR80A/MGCLKT_3/RPLL2_T_FB1/RPLL3_T_FB1	I/O	3	DQ18	MGCLKT_3/RPLL2_T_FB1/RPLL3_T_FB1	True_of_IOR80B	True	N21
IOR80B/MGCLKC_3/RPLL2_C_FB1/RPLL3_C_FB1	I/O	3	DQ18	MGCLKC_3/RPLL2_C_FB1/RPLL3_C_FB1	Comp_of_IOR80A	True	N22
IOR83A/MGCLKT_2/RPLL2_T_IN1/RPLL3_T_IN1	I/O	3	DQ17	MGCLKT_2/RPLL2_T_IN1/RPLL3_T_IN1	True_of_IOR83B	True	M21
IOR83B/MGCLKC_2/RPLL2_C_IN1/RPLL3_C_IN1	I/O	3	DQ17	MGCLKC_2/RPLL2_C_IN1/RPLL3_C_IN1	Comp_of_IOR83A	True	M22
IOR85A/SGCLKC_2/RPLL2_T_IN0/RPLL3_T_IN0	I/O	3	DQ17	SGCLKT_2/RPLL2_T_IN0/RPLL3_T_IN0	True_of_IOR85B	True	L22
IOR85B/SGCLKC_2/RPLL2_C_IN0/RPLL3_C_IN0	I/O	3	DQ17	SGCLKC_2/RPLL2_C_IN0/RPLL3_C_IN0	Comp_of_IOR85A	True	L23
IOR87A/D14	I/O	3	DQ17	D14	True_of_IOR87B	True	M24
IOR87B/D15	I/O	3	DQ17	D15	Comp_of_IOR87A	True	M25
IOR89A/SSPI_CS_N	I/O	3	DQS17	SSPI_CS_N	True_of_IOR89B	True	L24
IOR89B/D13	I/O	3	DQS17	D13	Comp_of_IOR89A	True	L25
IOR8A	I/O	2	DQS23		True_of_IOR8B	True	T17
IOR8B	I/O	2	DQS23		Comp_of_IOR8A	True	T18
IOR92A/D11	I/O	3	DQ17	D11	True_of_IOR92B	True	M20
IOR92B/D12	I/O	3	DQ17	D12	Comp_of_IOR92A	True	L20
IOR94A/D09	I/O	3	DQ17	D09	True_of_IOR94B	True	K25
IOR94B/D10	I/O	3	DQ17	D10	Comp_of_IOR94A	True	K26
IOR96A/MCS_N	I/O	3	DQ16	MCS_N	True_of_IOR96B	True	P18
IOR96B/D08/VREF/SO	I/O	3	DQ16	D08/VREF/SO	Comp_of_IOR96A	True	N18
IOR98A/D06/SSPI_CLK	I/O	3	DQ16	D06/SSPI_CLK	True_of_IOR98B	True	R16
IOR98B/D07/SSPI_WPN	I/O	3	DQ16	D07/SSPI_WPN	Comp_of_IOR98A	True	R17

Note!							
^[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
ADCTN	DIO	ADC	none			none	R11
ADCTP	DIO	ADC	none			none	R12
ADCVN	DIO	ADC	none			none	P11
ADCVP	DIO	ADC	none			none	N12
ATEST_O	DIO	Q0/Q1	none			none	AF15
M0_CKN	DIO	MIPI	none			none	AA3
M0_CKP	DIO	MIPI	none			none	Y3
M0_D0N	DIO	MIPI	none			none	W1
M0_D0P	DIO	MIPI	none			none	V1
M0_D1N	DIO	MIPI	none			none	AA2
M0_D1P	DIO	MIPI	none			none	Y2
M0_D2N	DIO	MIPI	none			none	W3
M0_D2P	DIO	MIPI	none			none	V3
M0_D3N	DIO	MIPI	none			none	W4
M0_D3P	DIO	MIPI	none			none	V4
M1_CKN	DIO	MIPI	none			none	AF2
M1_CKP	DIO	MIPI	none			none	AE2
M1_D0N	DIO	MIPI	none			none	AC1
M1_D0P	DIO	MIPI	none			none	AB1
M1_D1N	DIO	MIPI	none			none	AE1
M1_D1P	DIO	MIPI	none			none	AD1
M1_D2N	DIO	MIPI	none			none	AF3
M1_D2P	DIO	MIPI	none			none	AE3
M1_D3N	DIO	MIPI	none			none	AF5
M1_D3P	DIO	MIPI	none			none	AE5
Q0_LN0_RXM_I	DIO	Q0	none			none	C12
Q0_LN0_RXP_I	DIO	Q0	none			none	D12
Q0_LN0_TXM_O	DIO	Q0	none			none	C10
Q0_LN0_TXP_O	DIO	Q0	none			none	D10
Q0_LN1_RXM_I	DIO	Q0	none			none	C14

Note!							
^[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
Q0_LN1_RXP_I	DIO	Q0	none			none	D14
Q0_LN1_TXM_O	DIO	Q0	none			none	C8
Q0_LN1_TXP_O	DIO	Q0	none			none	D8
Q0_LN2_RXM_I	DIO	Q0	none			none	A13
Q0_LN2_RXP_I	DIO	Q0	none			none	B13
Q0_LN2_TXM_O	DIO	Q0	none			none	A9
Q0_LN2_TXP_O	DIO	Q0	none			none	B9
Q0_LN3_RXM_I	DIO	Q0	none			none	A11
Q0_LN3_RXP_I	DIO	Q0	none			none	B11
Q0_LN3_TXM_O	DIO	Q0	none			none	A7
Q0_LN3_TXP_O	DIO	Q0	none			none	B7
Q0_REFCLKM_0	DIO	Q0	none			none	E11
Q0_REFCLKM_1	DIO	Q0	none			none	E13
Q0_REFCLKP_0	DIO	Q0	none			none	F11
Q0_REFCLKP_1	DIO	Q0	none			none	F13
Q1_LN0_RXM_I	DIO	Q1	none			none	AD14
Q1_LN0_RXP_I	DIO	Q1	none			none	AC14
Q1_LN0_TXM_O	DIO	Q1	none			none	AD8
Q1_LN0_TXP_O	DIO	Q1	none			none	AC8
Q1_LN1_RXM_I	DIO	Q1	none			none	AD12
Q1_LN1_RXP_I	DIO	Q1	none			none	AC12
Q1_LN1_TXM_O	DIO	Q1	none			none	AD10
Q1_LN1_TXP_O	DIO	Q1	none			none	AC10
Q1_LN2_RXM_I	DIO	Q1	none			none	AF13
Q1_LN2_RXP_I	DIO	Q1	none			none	AE13
Q1_LN2_TXM_O	DIO	Q1	none			none	AF9
Q1_LN2_TXP_O	DIO	Q1	none			none	AE9
Q1_LN3_RXM_I	DIO	Q1	none			none	AF11
Q1_LN3_RXP_I	DIO	Q1	none			none	AE11
Q1_LN3_TXM_O	DIO	Q1	none			none	AF7

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
Q1_LN3_TXP_O	DIO	Q1	none			none	AE7
Q1_REFCLKM_0	DIO	Q1	none			none	AB11
Q1_REFCLKM_1	DIO	Q1	none			none	AB13
Q1_REFCLKP_0	DIO	Q1	none			none	AA11
Q1_REFCLKP_1	DIO	Q1	none			none	AA13
VDDAM_VDDDM	Power	N/A					N13
VDDAM_VDDDM	Power	N/A					W13
VDDAM_VDDDM	Power	N/A					U13
VDDAM_VDDDM	Power	N/A					R13
VDDXM	Power	N/A					M12
VDDAQ0_VDDDQ0	Power	N/A					D11
VDDAQ0_VDDDQ0	Power	N/A					D9
VDDAQ0_VDDDQ0	Power	N/A					D13
VDDHAQ0	Power	N/A					U9
VDDTQ0	Power	N/A					F12
VDDTQ0	Power	N/A					F10
VDDAQ1_VDDDQ1	Power	N/A					AC9
VDDAQ1_VDDDQ1	Power	N/A					AC13
VDDAQ1_VDDDQ1	Power	N/A					AC11
VDDHAQ1	Power	N/A					R9
VDDTQ1	Power	N/A					AA10
VDDTQ1	Power	N/A					AA12
VCC	Power	N/A					J13
VCC	Power	N/A					V12
VCC	Power	N/A					U11
VCC	Power	N/A					T12
VCC	Power	N/A					V10
VCC	Power	N/A					K10
VCC	Power	N/A					L13
VCC	Power	N/A					K12

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
VCC	Power	N/A					M10
VCC	Power	N/A					L11
VCC	Power	N/A					T10
VCC	Power	N/A					P10
VCC	Power	N/A					J11
VCCLDO	Power	N/A					B14
VCCLDO	Power	N/A					B8
VCCLDO	Power	N/A					B12
VCCLDO	Power	N/A					C7
VCCLDO	Power	N/A					C15
VCCLDO	Power	N/A					B10
VCCIO10	Power	N/A					W11
VCCIO10	Power	N/A					Y14
VCCX	Power	N/A					J9
VCCX	Power	N/A					N9
VCCX	Power	N/A					L9
VCCIO2	Power	N/A					Y24
VCCIO2	Power	N/A					U23
VCCIO2	Power	N/A					AC25
VCCIO2	Power	N/A					T16
VCCIO2	Power	N/A					T26
VCCIO2	Power	N/A					V20
VCCIO3	Power	N/A					L21
VCCIO3	Power	N/A					K24
VCCIO3	Power	N/A					R19
VCCIO3	Power	N/A					N15
VCCIO3	Power	N/A					N25
VCCIO3	Power	N/A					P22
VCCIO4	Power	N/A					M18
VCCIO4	Power	N/A					H20

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
VCCIO4	Power	N/A					G23
VCCIO4	Power	N/A					J17
VCCIO4	Power	N/A					F26
VCCIO4	Power	N/A					K14
VCCIO5	Power	N/A					C25
VCCIO5	Power	N/A					B18
VCCIO5	Power	N/A					D22
VCCIO5	Power	N/A					E19
VCCIO5	Power	N/A					F16
VCCIO5	Power	N/A					A21
VCCIO6	Power	N/A					J7
VCCIO6	Power	N/A					D2
VCCIO6	Power	N/A					C5
VCCIO6	Power	N/A					A1
VCCIO6	Power	N/A					F6
VCCIO6	Power	N/A					G3
VCCIO7	Power	N/A					L1
VCCIO7	Power	N/A					N5
VCCIO7	Power	N/A					M8
VCCIO7	Power	N/A					K4
VCCIO7	Power	N/A					T6
VCCIO7	Power	N/A					P2
VSS	Ground	N/A					M11
VSS	Ground	N/A					AE15
VSS	Ground	N/A					B15
VSS	Ground	N/A					A10
VSS	Ground	N/A					A12
VSS	Ground	N/A					A14
VSS	Ground	N/A					A16
VSS	Ground	N/A					A26

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
VSS	Ground	N/A					A6
VSS	Ground	N/A					A8
VSS	Ground	N/A					AA14
VSS	Ground	N/A					AA16
VSS	Ground	N/A					AA26
VSS	Ground	N/A					AA6
VSS	Ground	N/A					AB10
VSS	Ground	N/A					AB12
VSS	Ground	N/A					AB14
VSS	Ground	N/A					AB23
VSS	Ground	N/A					AB3
VSS	Ground	N/A					AA9
VSS	Ground	N/A					AB8
VSS	Ground	N/A					AC15
VSS	Ground	N/A					AC20
VSS	Ground	N/A					AC7
VSS	Ground	N/A					AD11
VSS	Ground	N/A					AD13
VSS	Ground	N/A					AD6
VSS	Ground	N/A					AD9
VSS	Ground	N/A					AD16
VSS	Ground	N/A					AE24
VSS	Ground	N/A					AE4
VSS	Ground	N/A					AE6
VSS	Ground	N/A					AF1
VSS	Ground	N/A					AF10
VSS	Ground	N/A					AF12
VSS	Ground	N/A					AF14
VSS	Ground	N/A					AF16
VSS	Ground	N/A					AF21

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
VSS	Ground	N/A					AF6
VSS	Ground	N/A					AF8
VSS	Ground	N/A					B16
VSS	Ground	N/A					B23
VSS	Ground	N/A					B3
VSS	Ground	N/A					B6
VSS	Ground	N/A					C11
VSS	Ground	N/A					C13
VSS	Ground	N/A					C16
VSS	Ground	N/A					C20
VSS	Ground	N/A					C6
VSS	Ground	N/A					C9
VSS	Ground	N/A					D15
VSS	Ground	N/A					D17
VSS	Ground	N/A					D7
VSS	Ground	N/A					E10
VSS	Ground	N/A					E12
VSS	Ground	N/A					E14
VSS	Ground	N/A					E24
VSS	Ground	N/A					E4
VSS	Ground	N/A					E7
VSS	Ground	N/A					E8
VSS	Ground	N/A					E9
VSS	Ground	N/A					F1
VSS	Ground	N/A					F14
VSS	Ground	N/A					F21
VSS	Ground	N/A					F9
VSS	Ground	N/A					G10
VSS	Ground	N/A					G11
VSS	Ground	N/A					AB9

Note!							
^[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
VSS	Ground	N/A					G13
VSS	Ground	N/A					Y12
VSS	Ground	N/A					G18
VSS	Ground	N/A					G12
VSS	Ground	N/A					H25
VSS	Ground	N/A					H5
VSS	Ground	N/A					J12
VSS	Ground	N/A					J2
VSS	Ground	N/A					J22
VSS	Ground	N/A					K11
VSS	Ground	N/A					K13
VSS	Ground	N/A					K19
VSS	Ground	N/A					K9
VSS	Ground	N/A					L10
VSS	Ground	N/A					L12
VSS	Ground	N/A					L16
VSS	Ground	N/A					L26
VSS	Ground	N/A					L6
VSS	Ground	N/A					M13
VSS	Ground	N/A					M23
VSS	Ground	N/A					M3
VSS	Ground	N/A					M9
VSS	Ground	N/A					N10
VSS	Ground	N/A					N20
VSS	Ground	N/A					P13
VSS	Ground	N/A					P17
VSS	Ground	N/A					P7
VSS	Ground	N/A					P9
VSS	Ground	N/A					R10
VSS	Ground	N/A					R24

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
VSS	Ground	N/A					R4
VSS	Ground	N/A					T1
VSS	Ground	N/A					T11
VSS	Ground	N/A					T13
VSS	Ground	N/A					T21
VSS	Ground	N/A					T9
VSS	Ground	N/A					U10
VSS	Ground	N/A					U12
VSS	Ground	N/A					U18
VSS	Ground	N/A					U8
VSS	Ground	N/A					V15
VSS	Ground	N/A					V25
VSS	Ground	N/A					V5
VSS	Ground	N/A					E15
VSS	Ground	N/A					W12
VSS	Ground	N/A					W2
VSS	Ground	N/A					W22
VSS	Ground	N/A					Y11
VSS	Ground	N/A					Y10
VSS	Ground	N/A					Y13
VSS	Ground	N/A					Y19
VSS	Ground	N/A					V13
NC	N/A	N/A					AD15
NC	N/A	N/A					AD7
NC	N/A	N/A					AE10
NC	N/A	N/A					AE12
NC	N/A	N/A					AE14
NC	N/A	N/A					AE8
NC	N/A	N/A					AA1
NC	N/A	N/A					AC5

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
NC	N/A	N/A					AD2
NC	N/A	N/A					U3
NC	N/A	N/A					W7
NC	N/A	N/A					Y4
NC	N/A	N/A					G14
NC	N/A	N/A					P12
NC	N/A	N/A					N11
NC	N/A	N/A					AA21
NC	N/A	N/A					AB18
NC	N/A	N/A					AD22
NC	N/A	N/A					AE19
NC	N/A	N/A					AF26
NC	N/A	N/A					W17
NC	N/A	N/A					AA17
NC	N/A	N/A					AA18
NC	N/A	N/A					AA19
NC	N/A	N/A					AA20
NC	N/A	N/A					AA4
NC	N/A	N/A					AA7
NC	N/A	N/A					AB16
NC	N/A	N/A					AB17
NC	N/A	N/A					AB19
NC	N/A	N/A					AB2
NC	N/A	N/A					AB20
NC	N/A	N/A					AB21
NC	N/A	N/A					AB22
NC	N/A	N/A					AB5
NC	N/A	N/A					AB6
NC	N/A	N/A					AC16
NC	N/A	N/A					AC17

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
NC	N/A	N/A					AC18
NC	N/A	N/A					AC19
NC	N/A	N/A					AC2
NC	N/A	N/A					AC21
NC	N/A	N/A					AC22
NC	N/A	N/A					AC23
NC	N/A	N/A					AC3
NC	N/A	N/A					AC6
NC	N/A	N/A					AD17
NC	N/A	N/A					AD18
NC	N/A	N/A					AD19
NC	N/A	N/A					AD20
NC	N/A	N/A					AD21
NC	N/A	N/A					AD23
NC	N/A	N/A					AD24
NC	N/A	N/A					AD25
NC	N/A	N/A					AD26
NC	N/A	N/A					AD3
NC	N/A	N/A					AD4
NC	N/A	N/A					AD5
NC	N/A	N/A					AE17
NC	N/A	N/A					AE18
NC	N/A	N/A					AE20
NC	N/A	N/A					AE21
NC	N/A	N/A					AE22
NC	N/A	N/A					AE23
NC	N/A	N/A					AE25
NC	N/A	N/A					AE26
NC	N/A	N/A					AA5
NC	N/A	N/A					AF17

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
NC	N/A	N/A					AF18
NC	N/A	N/A					AF19
NC	N/A	N/A					AF20
NC	N/A	N/A					AF22
NC	N/A	N/A					AF23
NC	N/A	N/A					AF24
NC	N/A	N/A					AF25
NC	N/A	N/A					AF4
NC	N/A	N/A					V2
NC	N/A	N/A					AB4
NC	N/A	N/A					A15
NC	N/A	N/A					W15
NC	N/A	N/A					W16
NC	N/A	N/A					AC4
NC	N/A	N/A					Y1
NC	N/A	N/A					Y15
NC	N/A	N/A					Y16
NC	N/A	N/A					Y17
NC	N/A	N/A					Y18
NC	N/A	N/A					AA15
NC	N/A	N/A					V9
NC	N/A	N/A					W14
NC	N/A	N/A					V6
NC	N/A	N/A					Y5
NC	N/A	N/A					W5
NC	N/A	N/A					Y8
NC	N/A	N/A					U7
NC	N/A	N/A					Y6
NC	N/A	N/A					W6
NC	N/A	N/A					Y7

Note!							
[1] LV version							
管脚名称	功能	BANK	DQS	配置功能	差分Pair	LVDS	FPG676A ^[1]
NC	N/A	N/A					AA8
NC	N/A	N/A					V7
NC	N/A	N/A					W8
NC	N/A	N/A					V8

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
BANK10 True LVDS Pair							
IOB169A/TDO	I/O	10	none	TDO	True_of_IOB169B	True	U13
IOB169B/TMS	I/O	10	none	TMS	Comp_of_IOB169A	True	T13
IOB171A/READY	I/O	10	none	READY	True_of_IOB171B	True	U12
IOB171B/DONE	I/O	10	none	DONE	Comp_of_IOB171A	True	G11
IOB173A/TCK	I/O	10	none	TCK	True_of_IOB173B	True	V12
IOB173B/TDI	I/O	10	none	TDI	Comp_of_IOB173A	True	R13
IOB175A/MODE0	I/O	10	none	MODE0	True_of_IOB175B	True	U11
IOB175B/CCLK	I/O	10	none	CCLK	Comp_of_IOB175A	True	L12
IOB177A/MODE1	I/O	10	none	MODE1	True_of_IOB177B	True	U10
IOB177B/MODE2	I/O	10	none	MODE2	Comp_of_IOB177A	True	U9
IOB179A/CFGBVS	I/O	10	none	CFGBVS	True_of_IOB179B	True	U8
IOB179B/RECONFIG_N	I/O	10	none	RECONFIG_N	Comp_of_IOB179A	True	N12
BANK7 True LVDS Pair							
IOL20A	I/O	7	DQ1		True_of_IOL20B	True	J5
IOL20B	I/O	7	DQ1		Comp_of_IOL20A	True	H5
IOL24A/MGCLKT_11/LPLL0_T_IN1/LPLL1_T_IN1	I/O	7	DQ1	MGCLKT_11/LPLL0_T_IN1/LPLL1_T_IN1	True_of_IOL24B	True	H4
IOL24B/MGCLKC_11/LPLL0_C_IN1/LPLL1_C_IN1	I/O	7	DQ1	MGCLKC_11/LPLL0_C_IN1/LPLL1_C_IN1	Comp_of_IOL24A	True	G4
IOL29A/MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	I/O	7	DQ2	MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	True_of_IOL29B	True	K4
IOL29B/MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	I/O	7	DQ2	MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	Comp_of_IOL29A	True	J4
IOL31A/SGCLKT_10/LPLL0_T_FB0/LPLL1_T_FB0	I/O	7	DQ2	SGCLKT_10/LPLL0_T_FB0/LPLL1_T_FB0	True_of_IOL31B	True	L3
IOL31B/SGCLKC_10/LPLL0_C_FB0/LPLL1_C_FB0	I/O	7	DQ2	SGCLKC_10/LPLL0_C_FB0/LPLL1_C_FB0	Comp_of_IOL31A	True	K3
IOL33A	I/O	7	DQ2		True_of_IOL33B	True	M3
IOL33B	I/O	7	DQ2		Comp_of_IOL33A	True	M2

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOL35A	I/O	7	DQS2		True_of_IOL35B	True	M1
IOL35B	I/O	7	DQS2		Comp_of_IOL35A	True	L1
IOL38A	I/O	7	DQ2		True_of_IOL38B	True	K6
IOL38B	I/O	7	DQ2		Comp_of_IOL38A	True	J6
IOL40A	I/O	7	DQ2		True_of_IOL40B	True	L5
IOL40B	I/O	7	DQ2		Comp_of_IOL40A	True	L4
IOL42A	I/O	7	DQ3		True_of_IOL42B	True	N4
IOL42B	I/O	7	DQ3		Comp_of_IOL42A	True	N3
IOL44A	I/O	7	DQ3		True_of_IOL44B	True	R1
IOL44B	I/O	7	DQ3		Comp_of_IOL44A	True	P1
IOL47A	I/O	7	DQ3		True_of_IOL47B	True	P2
IOL47B	I/O	7	DQ3		Comp_of_IOL47A	True	N2
IOL49A	I/O	7	DQS3		True_of_IOL49B	True	P5
IOL49B	I/O	7	DQS3		Comp_of_IOL49A	True	P4
IOL51A	I/O	7	DQ3		True_of_IOL51B	True	M6
IOL51B	I/O	7	DQ3		Comp_of_IOL51A	True	M5
IOL53A	I/O	7	DQ3		True_of_IOL53B	True	P6
IOL53B	I/O	7	DQ3		Comp_of_IOL53A	True	N5
BANK6 True LVDS Pair							
IOL101A	I/O	6	DQ7		True_of_IOL101B	True	AA8
IOL101B	I/O	6	DQ7		Comp_of_IOL101A	True	AB8
IOL103A	I/O	6	DQ7		True_of_IOL103B	True	Y8
IOL103B	I/O	6	DQ7		Comp_of_IOL103A	True	Y7
IOL105A	I/O	6	DQ7		True_of_IOL105B	True	W9
IOL105B	I/O	6	DQ7		Comp_of_IOL105A	True	Y9
IOL107A	I/O	6	DQ7		True_of_IOL107B	True	V7
IOL107B	I/O	6	DQ7		Comp_of_IOL107A	True	W7
IOL56A	I/O	6	DQ4		True_of_IOL56B	True	T1
IOL56B	I/O	6	DQ4		Comp_of_IOL56A	True	U1
IOL58A	I/O	6	DQ4		True_of_IOL58B	True	U2

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOL58B	I/O	6	DQ4		Comp_of_IOL58A	True	V2
IOL60A	I/O	6	DQ4		True_of_IOL60B	True	U3
IOL60B	I/O	6	DQ4		Comp_of_IOL60A	True	V3
IOL62A	I/O	6	DQ4		True_of_IOL62B	True	W2
IOL62B	I/O	6	DQ4		Comp_of_IOL62A	True	Y2
IOL65A	I/O	6	DQS4		True_of_IOL65B	True	R3
IOL65B	I/O	6	DQS4		Comp_of_IOL65A	True	R2
IOL67A	I/O	6	DQ4		True_of_IOL67B	True	W1
IOL67B	I/O	6	DQ4		Comp_of_IOL67A	True	Y1
IOL69A	I/O	6	DQS5		True_of_IOL69B	True	Y3
IOL69B	I/O	6	DQS5		Comp_of_IOL69A	True	AA3
IOL71A	I/O	6	DQ5		True_of_IOL71B	True	AA1
IOL71B	I/O	6	DQ5		Comp_of_IOL71A	True	AB1
IOL74A	I/O	6	DQ5		True_of_IOL74B	True	AB3
IOL74B	I/O	6	DQ5		Comp_of_IOL74A	True	AB2
IOL76A	I/O	6	DQ5		True_of_IOL76B	True	AA5
IOL76B	I/O	6	DQ5		Comp_of_IOL76A	True	AB5
IOL78A/SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	I/O	6	DQ5	SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	True_of_IOL78B	True	Y4
IOL78B/SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	I/O	6	DQ5	SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	Comp_of_IOL78A	True	AA4
IOL80A/MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	I/O	6	DQ5	MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	True_of_IOL80B	True	V4
IOL80B/MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	I/O	6	DQ5	MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	Comp_of_IOL80A	True	W4
IOL83A/MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	I/O	6	DQ6	MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	True_of_IOL83B	True	R4
IOL83B/MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	I/O	6	DQ6	MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	Comp_of_IOL83A	True	T4

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOL85A/SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	I/O	6	DQ6	SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	True_of_IOL85B	True	T5
IOL85B/SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	I/O	6	DQ6	SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	Comp_of_IOL85A	True	U5
IOL87A	I/O	6	DQ6		True_of_IOL87B	True	U6
IOL87B	I/O	6	DQ6		Comp_of_IOL87A	True	V5
IOL89A	I/O	6	DQ6		True_of_IOL89B	True	R6
IOL89B	I/O	6	DQ6		Comp_of_IOL89A	True	T6
IOL92A	I/O	6	DQ6		True_of_IOL92B	True	Y6
IOL92B	I/O	6	DQ6		Comp_of_IOL92A	True	AA6
IOL94A	I/O	6	DQS6		True_of_IOL94B	True	W6
IOL94B	I/O	6	DQS6		Comp_of_IOL94A	True	W5
IOL96A	I/O	6	DQS7		True_of_IOL96B	True	V9
IOL96B	I/O	6	DQS7		Comp_of_IOL96A	True	V8
IOL98A	I/O	6	DQ7		True_of_IOL98B	True	AB7
IOL98B	I/O	6	DQ7		Comp_of_IOL98A	True	AB6
BANK5 True LVDS Pair							
IOB51A	I/O	5	DQ9		True_of_IOB51B	True	AB11
IOB51B	I/O	5	DQ9		Comp_of_IOB51A	True	AB12
IOB53A	I/O	5	DQ9		True_of_IOB53B	True	AA9
IOB53B	I/O	5	DQ9		Comp_of_IOB53A	True	AB10
IOB56A	I/O	5	DQS9		True_of_IOB56B	True	AA10
IOB56B	I/O	5	DQS9		Comp_of_IOB56A	True	AA11
IOB58A	I/O	5	DQ9		True_of_IOB58B	True	V10
IOB58B	I/O	5	DQ9		Comp_of_IOB58A	True	W10
IOB60A/SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	I/O	5	DQ9	SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	True_of_IOB60B	True	Y11
IOB60B/SGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	I/O	5	DQ9	SGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	Comp_of_IOB60A	True	Y12

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOB62A/MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	I/O	5	DQ9	MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	True_of_IOB62B	True	W11
IOB62B/MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	I/O	5	DQ9	MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	Comp_of_IOB62A	True	W12
IOB66A/MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	I/O	5	DQ10	MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	True_of_IOB66B	True	V13
IOB66B/MGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	I/O	5	DQ10	MGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	Comp_of_IOB66A	True	V14
IOB68A/SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	I/O	5	DQ10	SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	True_of_IOB68B	True	U15
IOB68B/SGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	I/O	5	DQ10	SGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	Comp_of_IOB68A	True	V15
IOB70A	I/O	5	DQS10		True_of_IOB70B	True	T14
IOB70B	I/O	5	DQS10		Comp_of_IOB70A	True	T15
IOB72A	I/O	5	DQ10		True_of_IOB72B	True	W15
IOB72B	I/O	5	DQ10		Comp_of_IOB72A	True	W16
IOB76A	I/O	5	DQ10		True_of_IOB76B	True	T16
IOB76B	I/O	5	DQ10		Comp_of_IOB76A	True	U16
IOB78A	I/O	5	DQ11		True_of_IOB78B	True	Y16
IOB78B	I/O	5	DQ11		Comp_of_IOB78A	True	AA16
IOB80A	I/O	5	DQ11		True_of_IOB80B	True	AB16
IOB80B	I/O	5	DQ11		Comp_of_IOB80A	True	AB17
IOB83A	I/O	5	DQ11		True_of_IOB83B	True	AA15
IOB83B	I/O	5	DQ11		Comp_of_IOB83A	True	AB15
IOB85A	I/O	5	DQ11		True_of_IOB85B	True	W14
IOB85B	I/O	5	DQ11		Comp_of_IOB85A	True	Y14
IOB87A	I/O	5	DQ11		True_of_IOB87B	True	Y13
IOB87B	I/O	5	DQ11		Comp_of_IOB87A	True	AA14
IOB89A	I/O	5	DQS11		True_of_IOB89B	True	AA13
IOB89B	I/O	5	DQS11		Comp_of_IOB89A	True	AB13

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
BANK4 True LVDS Pair							
IOB102A/MCS_N	I/O	4	DQ12	MCS_N	True_of_IOB102B	True	T19
IOB102B/D08/SO	I/O	4	DQ12	D08/SO	Comp_of_IOB102A	True	T20
IOB104A/PUDC_B	I/O	4	DQS12	PUDC_B	True_of_IOB104B	True	U22
IOB104B/EMCCLK	I/O	4	DQS12	EMCCLK	Comp_of_IOB104A	True	V22
IOB106A/CSI_B	I/O	4	DQ13	CSI_B	True_of_IOB106B	True	V17
IOB106B/D31	I/O	4	DQ13	D31	Comp_of_IOB106A	True	W17
IOB108A/D30	I/O	4	DQ13	D30	True_of_IOB108B	True	AA18
IOB108B/D29	I/O	4	DQ13	D29	Comp_of_IOB108A	True	AB18
IOB110A/RDWR	I/O	4	DQS13	RDWR	True_of_IOB110B	True	AA19
IOB110B/DOOUT_CSO_B	I/O	4	DQS13	DOOUT_CSO_B	Comp_of_IOB110A	True	AB20
IOB112A/D28	I/O	4	DQ13	D28	True_of_IOB112B	True	U17
IOB112B/D27	I/O	4	DQ13	D27	Comp_of_IOB112A	True	U18
IOB114A/SGCLKT_4/BPLL2_T_FB1/BP LL3_T_FB1	I/O	4	DQ13	SGCLKT_4/BPLL2_T_FB1/BP LL3_T_FB1	True_of_IOB114B	True	V18
IOB114B/SGCLKC_4/BPLL2_C_FB1/BP LL3_C_FB1	I/O	4	DQ13	SGCLKC_4/BPLL2_C_FB1/BP LL3_C_FB1	Comp_of_IOB114A	True	V19
IOB116A/MGCLKT_4/BPLL2_T_FB0/BP LL3_T_FB0	I/O	4	DQ13	MGCLKT_4/BPLL2_T_FB0/BP LL3_T_FB0	True_of_IOB116B	True	Y18
IOB116B/MGCLKC_4/BPLL2_C_FB0/B PLL3_C_FB0	I/O	4	DQ13	MGCLKC_4/BPLL2_C_FB0/B PLL3_C_FB0	Comp_of_IOB116A	True	Y19
IOB120A/SGCLKT_5/BPLL2_T_IN0/BPL L3_T_IN0	I/O	4	DQ14	SGCLKT_5/BPLL2_T_IN0/BPL L3_T_IN0	True_of_IOB120B	True	U20
IOB120B/SGCLKC_5/BPLL2_C_IN0/BP LL3_C_IN0	I/O	4	DQ14	SGCLKC_5/BPLL2_C_IN0/BP LL3_C_IN0	Comp_of_IOB120A	True	V20
IOB122A/MGCLKT_5/BPLL2_T_IN1/BP LL3_T_IN1	I/O	4	DQ14	MGCLKT_5/BPLL2_T_IN1/BP LL3_T_IN1	True_of_IOB122B	True	W19
IOB122B/MGCLKC_5/BPLL2_C_IN1/BP LL3_C_IN1	I/O	4	DQ14	MGCLKC_5/BPLL2_C_IN1/BP LL3_C_IN1	Comp_of_IOB122A	True	W20
IOB124A/D09	I/O	4	DQ14	D09	True_of_IOB124B	True	W21

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOB124B/D10	I/O	4	DQ14	D10	Comp_of_IOB124A	True	W22
IOB126A/D11	I/O	4	DQ14	D11	True_of_IOB126B	True	AA20
IOB126B/D12	I/O	4	DQ14	D12	Comp_of_IOB126A	True	AA21
IOB129A/D14	I/O	4	DQ14	D14	True_of_IOB129B	True	AB21
IOB129B/D15	I/O	4	DQ14	D15	Comp_of_IOB129A	True	AB22
IOB131A/SSPI_CS_N	I/O	4	DQS14	SSPI_CS_N	True_of_IOB131B	True	Y21
IOB131B/D13	I/O	4	DQS14	D13	Comp_of_IOB131A	True	Y22
IOB133A/D26	I/O	4	DQ15	D26	True_of_IOB133B	True	P14
IOB133B/D25	I/O	4	DQ15	D25	Comp_of_IOB133A	True	R14
IOB135A/CLKHOLD_N	I/O	4	DQS15	CLKHOLD_N	True_of_IOB135B	True	N17
IOB135B/D22	I/O	4	DQS15	D22	Comp_of_IOB135A	True	P17
IOB138A/D24	I/O	4	DQ15	D24	True_of_IOB138B	True	R18
IOB138B/D23	I/O	4	DQ15	D23	Comp_of_IOB138A	True	T18
IOB140A/D21	I/O	4	DQ15	D21	True_of_IOB140B	True	P15
IOB140B/D20	I/O	4	DQ15	D20	Comp_of_IOB140A	True	R16
IOB142A/D19	I/O	4	DQ15	D19	True_of_IOB142B	True	N13
IOB142B/D18	I/O	4	DQ15	D18	Comp_of_IOB142A	True	N14
IOB144A/D17	I/O	4	DQ15	D17	True_of_IOB144B	True	P16
IOB144B/D16	I/O	4	DQ15	D16	Comp_of_IOB144A	True	R17
IOB93A/D00/MOSI	I/O	4	DQ12	D00/MOSI	True_of_IOB93B	True	P22
IOB93B/D01/DIN	I/O	4	DQ12	D01/DIN	Comp_of_IOB93A	True	R22
IOB95A/D02	I/O	4	DQ12	D02	True_of_IOB95B	True	P21
IOB95B/D03	I/O	4	DQ12	D03	Comp_of_IOB95A	True	R21
IOB97A/D04	I/O	4	DQ12	D04	True_of_IOB97B	True	T21
IOB97B/D05/SI	I/O	4	DQ12	D05/SI	Comp_of_IOB97A	True	U21
IOB99A/D06/SSPI_CLK	I/O	4	DQ12	D06/SSPI_CLK	True_of_IOB99B	True	P19
IOB99B/D07/SSPI_WPN	I/O	4	DQ12	D07/SSPI_WPN	Comp_of_IOB99A	True	R19

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
BANK3 True LVDS Pair							
IOR101A	I/O	3	DQ16		True_of_IOR101B	True	J15
IOR101B	I/O	3	DQ16		Comp_of_IOR101A	True	H15
IOR103A	I/O	3	DQS16		True_of_IOR103B	True	J14
IOR103B	I/O	3	DQS16		Comp_of_IOR103A	True	H14
IOR105A	I/O	3	DQ16		True_of_IOR105B	True	G15
IOR105B	I/O	3	DQ16		Comp_of_IOR105A	True	G16
IOR107A/ADCINCK1	I/O	3	DQ16	ADCINCK1	True_of_IOR107B	True	G17
IOR107B	I/O	3	DQ16		Comp_of_IOR107A	True	G18
IOR56A	I/O	3	DQ19		True_of_IOR56B	True	M13
IOR56B	I/O	3	DQ19		Comp_of_IOR56A	True	L13
IOR58A	I/O	3	DQS19		True_of_IOR58B	True	K17
IOR58B	I/O	3	DQS19		Comp_of_IOR58A	True	J17
IOR60A	I/O	3	DQ19		True_of_IOR60B	True	K13
IOR60B	I/O	3	DQ19		Comp_of_IOR60A	True	K14
IOR62A	I/O	3	DQ19		True_of_IOR62B	True	L14
IOR62B	I/O	3	DQ19		Comp_of_IOR62A	True	L15
IOR65A	I/O	3	DQ19		True_of_IOR65B	True	L16
IOR65B	I/O	3	DQ19		Comp_of_IOR65A	True	K16
IOR67A	I/O	3	DQ19		True_of_IOR67B	True	M15
IOR67B	I/O	3	DQ19		Comp_of_IOR67A	True	M16
IOR69A	I/O	3	DQ18		True_of_IOR69B	True	H20
IOR69B	I/O	3	DQ18		Comp_of_IOR69A	True	G20
IOR71A	I/O	3	DQ18		True_of_IOR71B	True	M21
IOR71B	I/O	3	DQ18		Comp_of_IOR71A	True	L21
IOR74A	I/O	3	DQ18		True_of_IOR74B	True	J22
IOR74B	I/O	3	DQ18		Comp_of_IOR74A	True	H22
IOR76A	I/O	3	DQS18		True_of_IOR76B	True	K21
IOR76B	I/O	3	DQS18		Comp_of_IOR76A	True	K22

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOR78A/SGCLKT_3/RPLL2_T_FB0/RPLL3_T_FB0	I/O	3	DQ18	SGCLKT_3/RPLL2_T_FB0/RPLL3_T_FB0	True_of_IOR78B	True	J20
IOR78B/SGCLKC_3/RPLL2_C_FB0/RPLL3_C_FB0	I/O	3	DQ18	SGCLKC_3/RPLL2_C_FB0/RPLL3_C_FB0	Comp_of_IOR78A	True	J21
IOR80A/MGCLKT_3/RPLL2_T_FB1/RPLL3_T_FB1	I/O	3	DQ18	MGCLKT_3/RPLL2_T_FB1/RPLL3_T_FB1	True_of_IOR80B	True	J19
IOR80B/MGCLKC_3/RPLL2_C_FB1/RPLL3_C_FB1	I/O	3	DQ18	MGCLKC_3/RPLL2_C_FB1/RPLL3_C_FB1	Comp_of_IOR80A	True	H19
IOR83A/MGCLKT_2/RPLL2_T_IN1/RPLL3_T_IN1	I/O	3	DQ17	MGCLKT_2/RPLL2_T_IN1/RPLL3_T_IN1	True_of_IOR83B	True	K18
IOR83B/MGCLKC_2/RPLL2_C_IN1/RPLL3_C_IN1	I/O	3	DQ17	MGCLKC_2/RPLL2_C_IN1/RPLL3_C_IN1	Comp_of_IOR83A	True	K19
IOR85A/SGCLKT_2/RPLL2_T_IN0/RPLL3_T_IN0	I/O	3	DQ17	SGCLKT_2/RPLL2_T_IN0/RPLL3_T_IN0	True_of_IOR85B	True	L19
IOR85B/SGCLKC_2/RPLL2_C_IN0/RPLL3_C_IN0	I/O	3	DQ17	SGCLKC_2/RPLL2_C_IN0/RPLL3_C_IN0	Comp_of_IOR85A	True	L20
IOR87A	I/O	3	DQ17		True_of_IOR87B	True	M18
IOR87B	I/O	3	DQ17		Comp_of_IOR87A	True	L18
IOR89A	I/O	3	DQS17		True_of_IOR89B	True	N22
IOR89B	I/O	3	DQS17		Comp_of_IOR89A	True	M22
IOR92A	I/O	3	DQ17		True_of_IOR92B	True	N18
IOR92B	I/O	3	DQ17		Comp_of_IOR92A	True	N19
IOR94A	I/O	3	DQ17		True_of_IOR94B	True	N20
IOR94B	I/O	3	DQ17		Comp_of_IOR94A	True	M20
IOR96A	I/O	3	DQ16		True_of_IOR96B	True	H17
IOR96B	I/O	3	DQ16		Comp_of_IOR96A	True	H18
IOR98A	I/O	3	DQ16		True_of_IOR98B	True	H13
IOR98B	I/O	3	DQ16		Comp_of_IOR98A	True	G13

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
BANK2 True LVDS Pair							
IOR11A	I/O	2	DQ23		True_of_IOR11B	True	F13
IOR11B	I/O	2	DQ23		Comp_of_IOR11A	True	F14
IOR13A	I/O	2	DQ23		True_of_IOR13B	True	F16
IOR13B	I/O	2	DQ23		Comp_of_IOR13A	True	E17
IOR15A	I/O	2	DQ22		True_of_IOR15B	True	A13
IOR15B	I/O	2	DQ22		Comp_of_IOR15A	True	A14
IOR17A	I/O	2	DQ22		True_of_IOR17B	True	C13
IOR17B	I/O	2	DQ22		Comp_of_IOR17A	True	B13
IOR20A	I/O	2	DQS22		True_of_IOR20B	True	A15
IOR20B	I/O	2	DQS22		Comp_of_IOR20A	True	A16
IOR22A	I/O	2	DQ22		True_of_IOR22B	True	B15
IOR22B	I/O	2	DQ22		Comp_of_IOR22A	True	B16
IOR24A/SGCLKT_1/RPLL0_T_FB0/RPLL1_T_FB0	I/O	2	DQ22	SGCLKT_1/RPLL0_T_FB0/RPLL1_T_FB0	True_of_IOR24B	True	B17
IOR24B/SGCLKC_1/RPLL0_C_FB0/RPLL1_C_FB0	I/O	2	DQ22	SGCLKC_1/RPLL0_C_FB0/RPLL1_C_FB0	Comp_of_IOR24A	True	B18
IOR26A/MGCLKT_1/RPLL0_T_FB1/RPLL1_T_FB1	I/O	2	DQ22	MGCLKT_1/RPLL0_T_FB1/RPLL1_T_FB1	True_of_IOR26B	True	D17
IOR26B/MGCLKC_1/RPLL0_C_FB1/RPLL1_C_FB1	I/O	2	DQ22	MGCLKC_1/RPLL0_C_FB1/RPLL1_C_FB1	Comp_of_IOR26A	True	C17
IOR29A/SGCLKT_0/RPLL0_T_IN0/RPLL1_T_IN0	I/O	2	DQ21	SGCLKT_0/RPLL0_T_IN0/RPLL1_T_IN0	True_of_IOR29B	True	E19
IOR29B/SGCLKC_0/RPLL0_C_IN0/RPLL1_C_IN0	I/O	2	DQ21	SGCLKC_0/RPLL0_C_IN0/RPLL1_C_IN0	Comp_of_IOR29A	True	D19
IOR2A	I/O	2	DQ23		True_of_IOR2B	True	E13
IOR2B	I/O	2	DQ23		Comp_of_IOR2A	True	E14
IOR31A/MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	I/O	2	DQ21	MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	True_of_IOR31B	True	C18

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484 ^[1]
IOR31B/MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	I/O	2	DQ21	MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	Comp_of_IOR31A	True	C19
IOR33A	I/O	2	DQ21		True_of_IOR33B	True	F19
IOR33B	I/O	2	DQ21		Comp_of_IOR33A	True	F20
IOR35A	I/O	2	DQS21		True_of_IOR35B	True	F18
IOR35B	I/O	2	DQS21		Comp_of_IOR35A	True	E18
IOR38A	I/O	2	DQ21		True_of_IOR38B	True	A18
IOR38B	I/O	2	DQ21		Comp_of_IOR38A	True	A19
IOR40A	I/O	2	DQ21		True_of_IOR40B	True	B20
IOR40B	I/O	2	DQ21		Comp_of_IOR40A	True	A20
IOR42A	I/O	2	DQ20		True_of_IOR42B	True	C22
IOR42B	I/O	2	DQ20		Comp_of_IOR42A	True	B22
IOR44A	I/O	2	DQS20		True_of_IOR44B	True	B21
IOR44B	I/O	2	DQS20		Comp_of_IOR44A	True	A21
IOR47A	I/O	2	DQ20		True_of_IOR47B	True	D20
IOR47B	I/O	2	DQ20		Comp_of_IOR47A	True	C20
IOR49A	I/O	2	DQ20		True_of_IOR49B	True	E22
IOR49B	I/O	2	DQ20		Comp_of_IOR49A	True	D22
IOR4A	I/O	2	DQ23		True_of_IOR4B	True	D14
IOR4B	I/O	2	DQ23		Comp_of_IOR4A	True	D15
IOR51A	I/O	2	DQ20		True_of_IOR51B	True	E21
IOR51B	I/O	2	DQ20		Comp_of_IOR51A	True	D21
IOR53A	I/O	2	DQ20		True_of_IOR53B	True	G21
IOR53B	I/O	2	DQ20		Comp_of_IOR53A	True	G22
IOR6A	I/O	2	DQ23		True_of_IOR6B	True	E16
IOR6B	I/O	2	DQ23		Comp_of_IOR6A	True	D16
IOR8A	I/O	2	DQS23		True_of_IOR8B	True	C14
IOR8B	I/O	2	DQS23		Comp_of_IOR8A	True	C15

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
BANK10 True LVDS Pair							
IOB169A/TDO	I/O	10	none	TDO	True_of_IOB169B	True	U13
IOB169B/TMS	I/O	10	none	TMS	Comp_of_IOB169A	True	T13
IOB171A/READY	I/O	10	none	READY	True_of_IOB171B	True	U12
IOB171B/DONE	I/O	10	none	DONE	Comp_of_IOB171A	True	G11
IOB173A/TCK	I/O	10	none	TCK	True_of_IOB173B	True	V12
IOB173B/TDI	I/O	10	none	TDI	Comp_of_IOB173A	True	R13
IOB175A/MODE0	I/O	10	none	MODE0	True_of_IOB175B	True	U11
IOB175B/CCLK	I/O	10	none	CCLK	Comp_of_IOB175A	True	L12
IOB177A/MODE1	I/O	10	none	MODE1	True_of_IOB177B	True	U10
IOB177B/MODE2	I/O	10	none	MODE2	Comp_of_IOB177A	True	U9
IOB179A/CFGBVS	I/O	10	none	CFGBVS	True_of_IOB179B	True	U8
IOB179B/RECONFIG_N	I/O	10	none	RECONFIG_N	Comp_of_IOB179A	True	N12
BANK7 True LVDS Pair							
IOL11A	I/O	7	DQ0		True_of_IOL11B	True	G1
IOL11B	I/O	7	DQ0		Comp_of_IOL11A	True	F1
IOL13A	I/O	7	DQ0		True_of_IOL13B	True	F3
IOL13B	I/O	7	DQ0		Comp_of_IOL13A	True	E3
IOL15A	I/O	7	DQ1		True_of_IOL15B	True	K1
IOL15B	I/O	7	DQ1		Comp_of_IOL15A	True	J1
IOL17A	I/O	7	DQ1		True_of_IOL17B	True	H2
IOL17B	I/O	7	DQ1		Comp_of_IOL17A	True	G2
IOL20A	I/O	7	DQ1		True_of_IOL20B	True	J5
IOL20B	I/O	7	DQ1		Comp_of_IOL20A	True	H5
IOL22A	I/O	7	DQS1		True_of_IOL22B	True	K2
IOL22B	I/O	7	DQS1		Comp_of_IOL22A	True	J2
IOL24A/MGCLKT_11/LPLL0_T_IN1/LPLL1_T_IN1	I/O	7	DQ1	MGCLKT_11/LPLL0_T_IN1/LPLL1_T_IN1	True_of_IOL24B	True	H4
IOL24B/MGCLKC_11/LPLL0_C_IN1/LPLL1_C_IN1	I/O	7	DQ1	MGCLKC_11/LPLL0_C_IN1/LPLL1_C_IN1	Comp_of_IOL24A	True	G4

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOL26A/SGCLKT_11/LPLL0_T_IN0/LPLL1_T_IN0	I/O	7	DQ1	SGCLKT_11/LPLL0_T_IN0/LPLL1_T_IN0	True_of_IOL26B	True	H3
IOL26B/SGCLKC_11/LPLL0_C_IN0/LPLL1_C_IN0	I/O	7	DQ1	SGCLKC_11/LPLL0_C_IN0/LPLL1_C_IN0	Comp_of_IOL26A	True	G3
IOL29A/MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	I/O	7	DQ2	MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	True_of_IOL29B	True	K4
IOL29B/MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	I/O	7	DQ2	MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	Comp_of_IOL29A	True	J4
IOL2A/ADCINCK0	I/O	7	DQ0	ADCINCK0	True_of_IOL2B	True	B1
IOL2B	I/O	7	DQ0		Comp_of_IOL2A	True	A1
IOL31A/SGCLKT_10/LPLL0_T_FB0/LPLL1_T_FB0	I/O	7	DQ2	SGCLKT_10/LPLL0_T_FB0/LPLL1_T_FB0	True_of_IOL31B	True	L3
IOL31B/SGCLKC_10/LPLL0_C_FB0/LPLL1_C_FB0	I/O	7	DQ2	SGCLKC_10/LPLL0_C_FB0/LPLL1_C_FB0	Comp_of_IOL31A	True	K3
IOL33A	I/O	7	DQ2		True_of_IOL33B	True	M3
IOL33B	I/O	7	DQ2		Comp_of_IOL33A	True	M2
IOL35A	I/O	7	DQS2		True_of_IOL35B	True	M1
IOL35B	I/O	7	DQS2		Comp_of_IOL35A	True	L1
IOL38A	I/O	7	DQ2		True_of_IOL38B	True	K6
IOL38B	I/O	7	DQ2		Comp_of_IOL38A	True	J6
IOL40A	I/O	7	DQ2		True_of_IOL40B	True	L5
IOL40B	I/O	7	DQ2		Comp_of_IOL40A	True	L4
IOL42A	I/O	7	DQ3		True_of_IOL42B	True	N4
IOL42B	I/O	7	DQ3		Comp_of_IOL42A	True	N3
IOL44A	I/O	7	DQ3		True_of_IOL44B	True	R1
IOL44B	I/O	7	DQ3		Comp_of_IOL44A	True	P1
IOL47A	I/O	7	DQ3		True_of_IOL47B	True	P2
IOL47B	I/O	7	DQ3		Comp_of_IOL47A	True	N2
IOL49A	I/O	7	DQS3		True_of_IOL49B	True	P5
IOL49B	I/O	7	DQS3		Comp_of_IOL49A	True	P4

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOL4A	I/O	7	DQS0		True_of_IOL4B	True	E1
IOL4B	I/O	7	DQS0		Comp_of_IOL4A	True	D1
IOL51A	I/O	7	DQ3		True_of_IOL51B	True	M6
IOL51B	I/O	7	DQ3		Comp_of_IOL51A	True	M5
IOL53A	I/O	7	DQ3		True_of_IOL53B	True	P6
IOL53B	I/O	7	DQ3		Comp_of_IOL53A	True	N5
IOL6A	I/O	7	DQ0		True_of_IOL6B	True	C2
IOL6B	I/O	7	DQ0		Comp_of_IOL6A	True	B2
IOL8A	I/O	7	DQ0		True_of_IOL8B	True	E2
IOL8B	I/O	7	DQ0		Comp_of_IOL8A	True	D2
BANK6 True LVDS Pair							
IOL101A	I/O	6	DQ7		True_of_IOL101B	True	AA8
IOL101B	I/O	6	DQ7		Comp_of_IOL101A	True	AB8
IOL103A	I/O	6	DQ7		True_of_IOL103B	True	Y8
IOL103B	I/O	6	DQ7		Comp_of_IOL103A	True	Y7
IOL105A	I/O	6	DQ7		True_of_IOL105B	True	W9
IOL105B	I/O	6	DQ7		Comp_of_IOL105A	True	Y9
IOL107A	I/O	6	DQ7		True_of_IOL107B	True	V7
IOL107B	I/O	6	DQ7		Comp_of_IOL107A	True	W7
IOL56A	I/O	6	DQ4		True_of_IOL56B	True	T1
IOL56B	I/O	6	DQ4		Comp_of_IOL56A	True	U1
IOL58A	I/O	6	DQ4		True_of_IOL58B	True	U2
IOL58B	I/O	6	DQ4		Comp_of_IOL58A	True	V2
IOL60A	I/O	6	DQ4		True_of_IOL60B	True	U3
IOL60B	I/O	6	DQ4		Comp_of_IOL60A	True	V3
IOL62A	I/O	6	DQ4		True_of_IOL62B	True	W2
IOL62B	I/O	6	DQ4		Comp_of_IOL62A	True	Y2
IOL65A	I/O	6	DQS4		True_of_IOL65B	True	R3
IOL65B	I/O	6	DQS4		Comp_of_IOL65A	True	R2
IOL67A	I/O	6	DQ4		True_of_IOL67B	True	W1

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOL67B	I/O	6	DQ4		Comp_of_IOL67A	True	Y1
IOL69A	I/O	6	DQS5		True_of_IOL69B	True	Y3
IOL69B	I/O	6	DQS5		Comp_of_IOL69A	True	AA3
IOL71A	I/O	6	DQ5		True_of_IOL71B	True	AA1
IOL71B	I/O	6	DQ5		Comp_of_IOL71A	True	AB1
IOL74A	I/O	6	DQ5		True_of_IOL74B	True	AB3
IOL74B	I/O	6	DQ5		Comp_of_IOL74A	True	AB2
IOL76A	I/O	6	DQ5		True_of_IOL76B	True	AA5
IOL76B	I/O	6	DQ5		Comp_of_IOL76A	True	AB5
IOL78A/SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	I/O	6	DQ5	SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	True_of_IOL78B	True	Y4
IOL78B/SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	I/O	6	DQ5	SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	Comp_of_IOL78A	True	AA4
IOL80A/MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	I/O	6	DQ5	MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	True_of_IOL80B	True	V4
IOL80B/MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	I/O	6	DQ5	MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	Comp_of_IOL80A	True	W4
IOL83A/MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	I/O	6	DQ6	MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	True_of_IOL83B	True	R4
IOL83B/MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	I/O	6	DQ6	MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	Comp_of_IOL83A	True	T4
IOL85A/SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	I/O	6	DQ6	SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	True_of_IOL85B	True	T5
IOL85B/SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	I/O	6	DQ6	SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	Comp_of_IOL85A	True	U5
IOL87A	I/O	6	DQ6		True_of_IOL87B	True	U6
IOL87B	I/O	6	DQ6		Comp_of_IOL87A	True	V5
IOL89A	I/O	6	DQ6		True_of_IOL89B	True	R6
IOL89B	I/O	6	DQ6		Comp_of_IOL89A	True	T6
IOL92A	I/O	6	DQ6		True_of_IOL92B	True	Y6

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOL92B	I/O	6	DQ6		Comp_of_IOL92A	True	AA6
IOL94A	I/O	6	DQS6		True_of_IOL94B	True	W6
IOL94B	I/O	6	DQS6		Comp_of_IOL94A	True	W5
IOL96A	I/O	6	DQS7		True_of_IOL96B	True	V9
IOL96B	I/O	6	DQS7		Comp_of_IOL96A	True	V8
IOL98A	I/O	6	DQ7		True_of_IOL98B	True	AB7
IOL98B	I/O	6	DQ7		Comp_of_IOL98A	True	AB6
BANK5 True LVDS Pair							
IOB51A	I/O	5	DQ9		True_of_IOB51B	True	AB11
IOB51B	I/O	5	DQ9		Comp_of_IOB51A	True	AB12
IOB53A	I/O	5	DQ9		True_of_IOB53B	True	AA9
IOB53B	I/O	5	DQ9		Comp_of_IOB53A	True	AB10
IOB56A	I/O	5	DQS9		True_of_IOB56B	True	AA10
IOB56B	I/O	5	DQS9		Comp_of_IOB56A	True	AA11
IOB58A	I/O	5	DQ9		True_of_IOB58B	True	V10
IOB58B	I/O	5	DQ9		Comp_of_IOB58A	True	W10
IOB60A/SGCLKT_6/BPLL0_T_IN0/BPL L1_T_IN0	I/O	5	DQ9	SGCLKT_6/BPLL0_T_IN0/BPL L1_T_IN0	True_of_IOB60B	True	Y11
IOB60B/SGCLKC_6/BPLL0_C_IN0/BPL L1_C_IN0	I/O	5	DQ9	SGCLKC_6/BPLL0_C_IN0/BPL L1_C_IN0	Comp_of_IOB60A	True	Y12
IOB62A/MGCLKT_6/BPLL0_T_IN1/BPL L1_T_IN1	I/O	5	DQ9	MGCLKT_6/BPLL0_T_IN1/BPL L1_T_IN1	True_of_IOB62B	True	W11
IOB62B/MGCLKC_6/BPLL0_C_IN1/BPL L1_C_IN1	I/O	5	DQ9	MGCLKC_6/BPLL0_C_IN1/BP LL1_C_IN1	Comp_of_IOB62A	True	W12
IOB66A/MGCLKT_7/BPLL0_T_FB0/BP LL1_T_FB0	I/O	5	DQ10	MGCLKT_7/BPLL0_T_FB0/BP LL1_T_FB0	True_of_IOB66B	True	V13
IOB66B/MGCLKC_7/BPLL0_C_FB0/BP LL1_C_FB0	I/O	5	DQ10	MGCLKC_7/BPLL0_C_FB0/BP LL1_C_FB0	Comp_of_IOB66A	True	V14
IOB68A/SGCLKT_7/BPLL0_T_FB1/BPL L1_T_FB1	I/O	5	DQ10	SGCLKT_7/BPLL0_T_FB1/BPL L1_T_FB1	True_of_IOB68B	True	U15

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOB68B/SGCLKC_7/BPLL0_C_FB1/BP LL1_C_FB1	I/O	5	DQ10	SGCLKC_7/BPLL0_C_FB1/BP LL1_C_FB1	Comp_of_IOB68A	True	V15
IOB70A	I/O	5	DQS10		True_of_IOB70B	True	T14
IOB70B	I/O	5	DQS10		Comp_of_IOB70A	True	T15
IOB72A	I/O	5	DQ10		True_of_IOB72B	True	W15
IOB72B	I/O	5	DQ10		Comp_of_IOB72A	True	W16
IOB76A	I/O	5	DQ10		True_of_IOB76B	True	T16
IOB76B	I/O	5	DQ10		Comp_of_IOB76A	True	U16
IOB78A	I/O	5	DQ11		True_of_IOB78B	True	Y16
IOB78B	I/O	5	DQ11		Comp_of_IOB78A	True	AA16
IOB80A	I/O	5	DQ11		True_of_IOB80B	True	AB16
IOB80B	I/O	5	DQ11		Comp_of_IOB80A	True	AB17
IOB83A	I/O	5	DQ11		True_of_IOB83B	True	AA15
IOB83B	I/O	5	DQ11		Comp_of_IOB83A	True	AB15
IOB85A	I/O	5	DQ11		True_of_IOB85B	True	W14
IOB85B	I/O	5	DQ11		Comp_of_IOB85A	True	Y14
IOB87A	I/O	5	DQ11		True_of_IOB87B	True	Y13
IOB87B	I/O	5	DQ11		Comp_of_IOB87A	True	AA14
IOB89A	I/O	5	DQS11		True_of_IOB89B	True	AA13
IOB89B	I/O	5	DQS11		Comp_of_IOB89A	True	AB13
BANK4 True LVDS Pair							
IOB102A/MCS_N	I/O	4	DQ12	MCS_N	True_of_IOB102B	True	T19
IOB102B/D08/SO	I/O	4	DQ12	D08/SO	Comp_of_IOB102A	True	T20
IOB104A/PUDC_B	I/O	4	DQS12	PUDC_B	True_of_IOB104B	True	U22
IOB104B/EMCCLK	I/O	4	DQS12	EMCCLK	Comp_of_IOB104A	True	V22
IOB106A/CSI_B	I/O	4	DQ13	CSI_B	True_of_IOB106B	True	V17
IOB106B/D31	I/O	4	DQ13	D31	Comp_of_IOB106A	True	W17
IOB108A/D30	I/O	4	DQ13	D30	True_of_IOB108B	True	AA18
IOB108B/D29	I/O	4	DQ13	D29	Comp_of_IOB108A	True	AB18
IOB110A/RDWR	I/O	4	DQS13	RDWR	True_of_IOB110B	True	AA19

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOB110B/DOUT_CSO_B	I/O	4	DQS13	DOUT_CSO_B	Comp_of_IOB110A	True	AB20
IOB112A/D28	I/O	4	DQ13	D28	True_of_IOB112B	True	U17
IOB112B/D27	I/O	4	DQ13	D27	Comp_of_IOB112A	True	U18
IOB114A/SGCLKT_4/BPLL2_T_FB1/BPLL3_T_FB1	I/O	4	DQ13	SGCLKT_4/BPLL2_T_FB1/BPLL3_T_FB1	True_of_IOB114B	True	V18
IOB114B/SGCLKC_4/BPLL2_C_FB1/BPLL3_C_FB1	I/O	4	DQ13	SGCLKC_4/BPLL2_C_FB1/BPLL3_C_FB1	Comp_of_IOB114A	True	V19
IOB116A/MGCLKT_4/BPLL2_T_FB0/BPLL3_T_FB0	I/O	4	DQ13	MGCLKT_4/BPLL2_T_FB0/BPLL3_T_FB0	True_of_IOB116B	True	Y18
IOB116B/MGCLKC_4/BPLL2_C_FB0/BPLL3_C_FB0	I/O	4	DQ13	MGCLKC_4/BPLL2_C_FB0/BPLL3_C_FB0	Comp_of_IOB116A	True	Y19
IOB120A/SGCLKT_5/BPLL2_T_IN0/BPLL3_T_IN0	I/O	4	DQ14	SGCLKT_5/BPLL2_T_IN0/BPLL3_T_IN0	True_of_IOB120B	True	U20
IOB120B/SGCLKC_5/BPLL2_C_IN0/BPLL3_C_IN0	I/O	4	DQ14	SGCLKC_5/BPLL2_C_IN0/BPLL3_C_IN0	Comp_of_IOB120A	True	V20
IOB122A/MGCLKT_5/BPLL2_T_IN1/BPLL3_T_IN1	I/O	4	DQ14	MGCLKT_5/BPLL2_T_IN1/BPLL3_T_IN1	True_of_IOB122B	True	W19
IOB122B/MGCLKC_5/BPLL2_C_IN1/BPLL3_C_IN1	I/O	4	DQ14	MGCLKC_5/BPLL2_C_IN1/BPLL3_C_IN1	Comp_of_IOB122A	True	W20
IOB124A/D09	I/O	4	DQ14	D09	True_of_IOB124B	True	W21
IOB124B/D10	I/O	4	DQ14	D10	Comp_of_IOB124A	True	W22
IOB126A/D11	I/O	4	DQ14	D11	True_of_IOB126B	True	AA20
IOB126B/D12	I/O	4	DQ14	D12	Comp_of_IOB126A	True	AA21
IOB129A/D14	I/O	4	DQ14	D14	True_of_IOB129B	True	AB21
IOB129B/D15	I/O	4	DQ14	D15	Comp_of_IOB129A	True	AB22
IOB131A/SSPI_CS_N	I/O	4	DQS14	SSPI_CS_N	True_of_IOB131B	True	Y21
IOB131B/D13	I/O	4	DQS14	D13	Comp_of_IOB131A	True	Y22
IOB133A/D26	I/O	4	DQ15	D26	True_of_IOB133B	True	P14
IOB133B/D25	I/O	4	DQ15	D25	Comp_of_IOB133A	True	R14
IOB135A/CLKHOLD_N	I/O	4	DQS15	CLKHOLD_N	True_of_IOB135B	True	N17

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOB135B/D22	I/O	4	DQS15	D22	Comp_of_IOB135A	True	P17
IOB138A/D24	I/O	4	DQ15	D24	True_of_IOB138B	True	R18
IOB138B/D23	I/O	4	DQ15	D23	Comp_of_IOB138A	True	T18
IOB140A/D21	I/O	4	DQ15	D21	True_of_IOB140B	True	P15
IOB140B/D20	I/O	4	DQ15	D20	Comp_of_IOB140A	True	R16
IOB142A/D19	I/O	4	DQ15	D19	True_of_IOB142B	True	N13
IOB142B/D18	I/O	4	DQ15	D18	Comp_of_IOB142A	True	N14
IOB144A/D17	I/O	4	DQ15	D17	True_of_IOB144B	True	P16
IOB144B/D16	I/O	4	DQ15	D16	Comp_of_IOB144A	True	R17
IOB93A/D00/MOSI	I/O	4	DQ12	D00/MOSI	True_of_IOB93B	True	P22
IOB93B/D01/DIN	I/O	4	DQ12	D01/DIN	Comp_of_IOB93A	True	R22
IOB95A/D02	I/O	4	DQ12	D02	True_of_IOB95B	True	P21
IOB95B/D03	I/O	4	DQ12	D03	Comp_of_IOB95A	True	R21
IOB97A/D04	I/O	4	DQ12	D04	True_of_IOB97B	True	T21
IOB97B/D05/SI	I/O	4	DQ12	D05/SI	Comp_of_IOB97A	True	U21
IOB99A/D06/SSPI_CLK	I/O	4	DQ12	D06/SSPI_CLK	True_of_IOB99B	True	P19
IOB99B/D07/SSPI_WPN	I/O	4	DQ12	D07/SSPI_WPN	Comp_of_IOB99A	True	R19
BANK3 True LVDS Pair							
IOR101A	I/O	3	DQ16		True_of_IOR101B	True	J15
IOR101B	I/O	3	DQ16		Comp_of_IOR101A	True	H15
IOR103A	I/O	3	DQS16		True_of_IOR103B	True	J14
IOR103B	I/O	3	DQS16		Comp_of_IOR103A	True	H14
IOR105A	I/O	3	DQ16		True_of_IOR105B	True	G15
IOR105B	I/O	3	DQ16		Comp_of_IOR105A	True	G16
IOR107A/ADCINCK1	I/O	3	DQ16	ADCINCK1	True_of_IOR107B	True	G17
IOR107B	I/O	3	DQ16		Comp_of_IOR107A	True	G18
IOR56A	I/O	3	DQ19		True_of_IOR56B	True	M13
IOR56B	I/O	3	DQ19		Comp_of_IOR56A	True	L13
IOR58A	I/O	3	DQS19		True_of_IOR58B	True	K17
IOR58B	I/O	3	DQS19		Comp_of_IOR58A	True	J17

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOR60A	I/O	3	DQ19		True_of_IOR60B	True	K13
IOR60B	I/O	3	DQ19		Comp_of_IOR60A	True	K14
IOR62A	I/O	3	DQ19		True_of_IOR62B	True	L14
IOR62B	I/O	3	DQ19		Comp_of_IOR62A	True	L15
IOR65A	I/O	3	DQ19		True_of_IOR65B	True	L16
IOR65B	I/O	3	DQ19		Comp_of_IOR65A	True	K16
IOR67A	I/O	3	DQ19		True_of_IOR67B	True	M15
IOR67B	I/O	3	DQ19		Comp_of_IOR67A	True	M16
IOR69A	I/O	3	DQ18		True_of_IOR69B	True	H20
IOR69B	I/O	3	DQ18		Comp_of_IOR69A	True	G20
IOR71A	I/O	3	DQ18		True_of_IOR71B	True	M21
IOR71B	I/O	3	DQ18		Comp_of_IOR71A	True	L21
IOR74A	I/O	3	DQ18		True_of_IOR74B	True	J22
IOR74B	I/O	3	DQ18		Comp_of_IOR74A	True	H22
IOR76A	I/O	3	DQS18		True_of_IOR76B	True	K21
IOR76B	I/O	3	DQS18		Comp_of_IOR76A	True	K22
IOR78A/SGCLKT_3/RPLL2_T_FB0/RPLL3_T_FB0	I/O	3	DQ18	SGCLKT_3/RPLL2_T_FB0/RPLL3_T_FB0	True_of_IOR78B	True	J20
IOR78B/SGCLKC_3/RPLL2_C_FB0/RPLL3_C_FB0	I/O	3	DQ18	SGCLKC_3/RPLL2_C_FB0/RPLL3_C_FB0	Comp_of_IOR78A	True	J21
IOR80A/MGCLKT_3/RPLL2_T_FB1/RPLL3_T_FB1	I/O	3	DQ18	MGCLKT_3/RPLL2_T_FB1/RPLL3_T_FB1	True_of_IOR80B	True	J19
IOR80B/MGCLKC_3/RPLL2_C_FB1/RPLL3_C_FB1	I/O	3	DQ18	MGCLKC_3/RPLL2_C_FB1/RPLL3_C_FB1	Comp_of_IOR80A	True	H19
IOR83A/MGCLKT_2/RPLL2_T_IN1/RPLL3_T_IN1	I/O	3	DQ17	MGCLKT_2/RPLL2_T_IN1/RPLL3_T_IN1	True_of_IOR83B	True	K18
IOR83B/MGCLKC_2/RPLL2_C_IN1/RPLL3_C_IN1	I/O	3	DQ17	MGCLKC_2/RPLL2_C_IN1/RPLL3_C_IN1	Comp_of_IOR83A	True	K19
IOR85A/SGCLKT_2/RPLL2_T_IN0/RPLL3_T_IN0	I/O	3	DQ17	SGCLKT_2/RPLL2_T_IN0/RPLL3_T_IN0	True_of_IOR85B	True	L19

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOR85B/SGCLKC_2/RPLL2_C_IN0/RPL L3_C_IN0	I/O	3	DQ17	SGCLKC_2/RPLL2_C_IN0/RP LL3_C_IN0	Comp_of_IOR85A	True	L20
IOR87A	I/O	3	DQ17		True_of_IOR87B	True	M18
IOR87B	I/O	3	DQ17		Comp_of_IOR87A	True	L18
IOR89A	I/O	3	DQS17		True_of_IOR89B	True	N22
IOR89B	I/O	3	DQS17		Comp_of_IOR89A	True	M22
IOR92A	I/O	3	DQ17		True_of_IOR92B	True	N18
IOR92B	I/O	3	DQ17		Comp_of_IOR92A	True	N19
IOR94A	I/O	3	DQ17		True_of_IOR94B	True	N20
IOR94B	I/O	3	DQ17		Comp_of_IOR94A	True	M20
IOR96A	I/O	3	DQ16		True_of_IOR96B	True	H17
IOR96B	I/O	3	DQ16		Comp_of_IOR96A	True	H18
IOR98A	I/O	3	DQ16		True_of_IOR98B	True	H13
IOR98B	I/O	3	DQ16		Comp_of_IOR98A	True	G13
BANK2 True LVDS Pair							
IOR11A	I/O	2	DQ23		True_of_IOR11B	True	F13
IOR11B	I/O	2	DQ23		Comp_of_IOR11A	True	F14
IOR13A	I/O	2	DQ23		True_of_IOR13B	True	F16
IOR13B	I/O	2	DQ23		Comp_of_IOR13A	True	E17
IOR15A	I/O	2	DQ22		True_of_IOR15B	True	A13
IOR15B	I/O	2	DQ22		Comp_of_IOR15A	True	A14
IOR17A	I/O	2	DQ22		True_of_IOR17B	True	C13
IOR17B	I/O	2	DQ22		Comp_of_IOR17A	True	B13
IOR20A	I/O	2	DQS22		True_of_IOR20B	True	A15
IOR20B	I/O	2	DQS22		Comp_of_IOR20A	True	A16
IOR22A	I/O	2	DQ22		True_of_IOR22B	True	B15
IOR22B	I/O	2	DQ22		Comp_of_IOR22A	True	B16
IOR24A/SGCLKT_1/RPLL0_T_FB0/RP LL1_T_FB0	I/O	2	DQ22	SGCLKT_1/RPLL0_T_FB0/RP LL1_T_FB0	True_of_IOR24B	True	B17

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOR24B/SGCLKC_1/RPLL0_C_FB0/RPLL1_C_FB0	I/O	2	DQ22	SGCLKC_1/RPLL0_C_FB0/RPLL1_C_FB0	Comp_of_IOR24A	True	B18
IOR26A/MGCLKT_1/RPLL0_T_FB1/RPLL1_T_FB1	I/O	2	DQ22	MGCLKT_1/RPLL0_T_FB1/RPLL1_T_FB1	True_of_IOR26B	True	D17
IOR26B/MGCLKC_1/RPLL0_C_FB1/RPLL1_C_FB1	I/O	2	DQ22	MGCLKC_1/RPLL0_C_FB1/RPLL1_C_FB1	Comp_of_IOR26A	True	C17
IOR29A/SGCLKT_0/RPLL0_T_IN0/RPLL1_T_IN0	I/O	2	DQ21	SGCLKT_0/RPLL0_T_IN0/RPLL1_T_IN0	True_of_IOR29B	True	E19
IOR29B/SGCLKC_0/RPLL0_C_IN0/RPLL1_C_IN0	I/O	2	DQ21	SGCLKC_0/RPLL0_C_IN0/RPLL1_C_IN0	Comp_of_IOR29A	True	D19
IOR2A	I/O	2	DQ23		True_of_IOR2B	True	E13
IOR2B	I/O	2	DQ23		Comp_of_IOR2A	True	E14
IOR31A/MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	I/O	2	DQ21	MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	True_of_IOR31B	True	C18
IOR31B/MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	I/O	2	DQ21	MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	Comp_of_IOR31A	True	C19
IOR33A	I/O	2	DQ21		True_of_IOR33B	True	F19
IOR33B	I/O	2	DQ21		Comp_of_IOR33A	True	F20
IOR35A	I/O	2	DQS21		True_of_IOR35B	True	F18
IOR35B	I/O	2	DQS21		Comp_of_IOR35A	True	E18
IOR38A	I/O	2	DQ21		True_of_IOR38B	True	A18
IOR38B	I/O	2	DQ21		Comp_of_IOR38A	True	A19
IOR40A	I/O	2	DQ21		True_of_IOR40B	True	B20
IOR40B	I/O	2	DQ21		Comp_of_IOR40A	True	A20
IOR42A	I/O	2	DQ20		True_of_IOR42B	True	C22
IOR42B	I/O	2	DQ20		Comp_of_IOR42A	True	B22
IOR44A	I/O	2	DQS20		True_of_IOR44B	True	B21
IOR44B	I/O	2	DQS20		Comp_of_IOR44A	True	A21
IOR47A	I/O	2	DQ20		True_of_IOR47B	True	D20
IOR47B	I/O	2	DQ20		Comp_of_IOR47A	True	C20

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484A ^[1]
IOR49A	I/O	2	DQ20		True_of_IOR49B	True	E22
IOR49B	I/O	2	DQ20		Comp_of_IOR49A	True	D22
IOR4A	I/O	2	DQ23		True_of_IOR4B	True	D14
IOR4B	I/O	2	DQ23		Comp_of_IOR4A	True	D15
IOR51A	I/O	2	DQ20		True_of_IOR51B	True	E21
IOR51B	I/O	2	DQ20		Comp_of_IOR51A	True	D21
IOR53A	I/O	2	DQ20		True_of_IOR53B	True	G21
IOR53B	I/O	2	DQ20		Comp_of_IOR53A	True	G22
IOR6A	I/O	2	DQ23		True_of_IOR6B	True	E16
IOR6B	I/O	2	DQ23		Comp_of_IOR6A	True	D16
IOR8A	I/O	2	DQS23		True_of_IOR8B	True	C14
IOR8B	I/O	2	DQS23		Comp_of_IOR8A	True	C15

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
BANK10 True LVDS Pair							
IOB169A/TDO	I/O	10	none	TDO	True_of_IOB169B	True	U13
IOB169B/TMS	I/O	10	none	TMS	Comp_of_IOB169A	True	T13
IOB171A/READY	I/O	10	none	READY	True_of_IOB171B	True	U12
IOB171B/DONE	I/O	10	none	DONE	Comp_of_IOB171A	True	G11
IOB173A/TCK	I/O	10	none	TCK	True_of_IOB173B	True	V12
IOB173B/TDI	I/O	10	none	TDI	Comp_of_IOB173A	True	R13
IOB175A/MODE0	I/O	10	none	MODE0	True_of_IOB175B	True	U11
IOB175B/CCLK	I/O	10	none	CCLK	Comp_of_IOB175A	True	L12
IOB177A/MODE1	I/O	10	none	MODE1	True_of_IOB177B	True	U10
IOB177B/MODE2	I/O	10	none	MODE2	Comp_of_IOB177A	True	U9
IOB179A/CFGBVS	I/O	10	none	CFGBVS	True_of_IOB179B	True	U8
IOB179B/RECONFIG_N	I/O	10	none	RECONFIG_N	Comp_of_IOB179A	True	N12
BANK7 True LVDS Pair							
IOL20A	I/O	7	DQ1		True_of_IOL20B	True	J5
IOL20B	I/O	7	DQ1		Comp_of_IOL20A	True	H5
IOL24A/MGCLKT_11/LPLL0_T_IN1/LPLL1_T_IN1	I/O	7	DQ1	MGCLKT_11/LPLL0_T_IN1/LPLL1_T_IN1	True_of_IOL24B	True	H4
IOL24B/MGCLKC_11/LPLL0_C_IN1/LPLL1_C_IN1	I/O	7	DQ1	MGCLKC_11/LPLL0_C_IN1/LPLL1_C_IN1	Comp_of_IOL24A	True	G4
IOL29A/MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	I/O	7	DQ2	MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	True_of_IOL29B	True	K4
IOL29B/MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	I/O	7	DQ2	MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	Comp_of_IOL29A	True	J4
IOL31A/SGCLKT_10/LPLL0_T_FB0/LPLL1_T_FB0	I/O	7	DQ2	SGCLKT_10/LPLL0_T_FB0/LPLL1_T_FB0	True_of_IOL31B	True	L3
IOL31B/SGCLKC_10/LPLL0_C_FB0/LPLL1_C_FB0	I/O	7	DQ2	SGCLKC_10/LPLL0_C_FB0/LPLL1_C_FB0	Comp_of_IOL31A	True	K3
IOL33A	I/O	7	DQ2		True_of_IOL33B	True	M3
IOL33B	I/O	7	DQ2		Comp_of_IOL33A	True	M2

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOL35A	I/O	7	DQS2		True_of_IOL35B	True	M1
IOL35B	I/O	7	DQS2		Comp_of_IOL35A	True	L1
IOL38A	I/O	7	DQ2		True_of_IOL38B	True	K6
IOL38B	I/O	7	DQ2		Comp_of_IOL38A	True	J6
IOL40A	I/O	7	DQ2		True_of_IOL40B	True	L5
IOL40B	I/O	7	DQ2		Comp_of_IOL40A	True	L4
IOL42A	I/O	7	DQ3		True_of_IOL42B	True	N4
IOL42B	I/O	7	DQ3		Comp_of_IOL42A	True	N3
IOL44A	I/O	7	DQ3		True_of_IOL44B	True	R1
IOL44B	I/O	7	DQ3		Comp_of_IOL44A	True	P1
IOL47A	I/O	7	DQ3		True_of_IOL47B	True	P2
IOL47B	I/O	7	DQ3		Comp_of_IOL47A	True	N2
IOL49A	I/O	7	DQS3		True_of_IOL49B	True	P5
IOL49B	I/O	7	DQS3		Comp_of_IOL49A	True	P4
IOL51A	I/O	7	DQ3		True_of_IOL51B	True	M6
IOL51B	I/O	7	DQ3		Comp_of_IOL51A	True	M5
IOL53A	I/O	7	DQ3		True_of_IOL53B	True	P6
IOL53B	I/O	7	DQ3		Comp_of_IOL53A	True	N5
BANK6 True LVDS Pair							
IOL101A	I/O	6	DQ7		True_of_IOL101B	True	AA8
IOL101B	I/O	6	DQ7		Comp_of_IOL101A	True	AB8
IOL103A	I/O	6	DQ7		True_of_IOL103B	True	Y8
IOL103B	I/O	6	DQ7		Comp_of_IOL103A	True	Y7
IOL105A	I/O	6	DQ7		True_of_IOL105B	True	W9
IOL105B	I/O	6	DQ7		Comp_of_IOL105A	True	Y9
IOL107A	I/O	6	DQ7		True_of_IOL107B	True	V7
IOL107B	I/O	6	DQ7		Comp_of_IOL107A	True	W7
IOL56A	I/O	6	DQ4		True_of_IOL56B	True	T1
IOL56B	I/O	6	DQ4		Comp_of_IOL56A	True	U1
IOL58A	I/O	6	DQ4		True_of_IOL58B	True	U2

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOL58B	I/O	6	DQ4		Comp_of_IOL58A	True	V2
IOL60A	I/O	6	DQ4		True_of_IOL60B	True	U3
IOL60B	I/O	6	DQ4		Comp_of_IOL60A	True	V3
IOL62A	I/O	6	DQ4		True_of_IOL62B	True	W2
IOL62B	I/O	6	DQ4		Comp_of_IOL62A	True	Y2
IOL65A	I/O	6	DQS4		True_of_IOL65B	True	R3
IOL65B	I/O	6	DQS4		Comp_of_IOL65A	True	R2
IOL67A	I/O	6	DQ4		True_of_IOL67B	True	W1
IOL67B	I/O	6	DQ4		Comp_of_IOL67A	True	Y1
IOL69A	I/O	6	DQS5		True_of_IOL69B	True	Y3
IOL69B	I/O	6	DQS5		Comp_of_IOL69A	True	AA3
IOL71A	I/O	6	DQ5		True_of_IOL71B	True	AA1
IOL71B	I/O	6	DQ5		Comp_of_IOL71A	True	AB1
IOL74A	I/O	6	DQ5		True_of_IOL74B	True	AB3
IOL74B	I/O	6	DQ5		Comp_of_IOL74A	True	AB2
IOL76A	I/O	6	DQ5		True_of_IOL76B	True	AA5
IOL76B	I/O	6	DQ5		Comp_of_IOL76A	True	AB5
IOL78A/SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	I/O	6	DQ5	SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	True_of_IOL78B	True	Y4
IOL78B/SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	I/O	6	DQ5	SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	Comp_of_IOL78A	True	AA4
IOL80A/MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	I/O	6	DQ5	MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	True_of_IOL80B	True	V4
IOL80B/MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	I/O	6	DQ5	MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	Comp_of_IOL80A	True	W4
IOL83A/MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	I/O	6	DQ6	MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	True_of_IOL83B	True	R4
IOL83B/MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	I/O	6	DQ6	MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	Comp_of_IOL83A	True	T4

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOL85A/SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	I/O	6	DQ6	SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	True_of_IOL85B	True	T5
IOL85B/SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	I/O	6	DQ6	SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	Comp_of_IOL85A	True	U5
IOL87A	I/O	6	DQ6		True_of_IOL87B	True	U6
IOL87B	I/O	6	DQ6		Comp_of_IOL87A	True	V5
IOL89A	I/O	6	DQ6		True_of_IOL89B	True	R6
IOL89B	I/O	6	DQ6		Comp_of_IOL89A	True	T6
IOL92A	I/O	6	DQ6		True_of_IOL92B	True	Y6
IOL92B	I/O	6	DQ6		Comp_of_IOL92A	True	AA6
IOL94A	I/O	6	DQS6		True_of_IOL94B	True	W6
IOL94B	I/O	6	DQS6		Comp_of_IOL94A	True	W5
IOL96A	I/O	6	DQS7		True_of_IOL96B	True	V9
IOL96B	I/O	6	DQS7		Comp_of_IOL96A	True	V8
IOL98A	I/O	6	DQ7		True_of_IOL98B	True	AB7
IOL98B	I/O	6	DQ7		Comp_of_IOL98A	True	AB6
BANK5 True LVDS Pair							
IOB51A	I/O	5	DQ9		True_of_IOB51B	True	AB11
IOB51B	I/O	5	DQ9		Comp_of_IOB51A	True	AB12
IOB53A	I/O	5	DQ9		True_of_IOB53B	True	AA9
IOB53B	I/O	5	DQ9		Comp_of_IOB53A	True	AB10
IOB56A	I/O	5	DQS9		True_of_IOB56B	True	AA10
IOB56B	I/O	5	DQS9		Comp_of_IOB56A	True	AA11
IOB58A	I/O	5	DQ9		True_of_IOB58B	True	V10
IOB58B	I/O	5	DQ9		Comp_of_IOB58A	True	W10
IOB60A/SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	I/O	5	DQ9	SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	True_of_IOB60B	True	Y11
IOB60B/SGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	I/O	5	DQ9	SGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	Comp_of_IOB60A	True	Y12

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOB62A/MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	I/O	5	DQ9	MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	True_of_IOB62B	True	W11
IOB62B/MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	I/O	5	DQ9	MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	Comp_of_IOB62A	True	W12
IOB66A/MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	I/O	5	DQ10	MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	True_of_IOB66B	True	V13
IOB66B/MGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	I/O	5	DQ10	MGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	Comp_of_IOB66A	True	V14
IOB68A/SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	I/O	5	DQ10	SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	True_of_IOB68B	True	U15
IOB68B/SGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	I/O	5	DQ10	SGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	Comp_of_IOB68A	True	V15
IOB70A	I/O	5	DQS10		True_of_IOB70B	True	T14
IOB70B	I/O	5	DQS10		Comp_of_IOB70A	True	T15
IOB72A	I/O	5	DQ10		True_of_IOB72B	True	W15
IOB72B	I/O	5	DQ10		Comp_of_IOB72A	True	W16
IOB76A	I/O	5	DQ10		True_of_IOB76B	True	T16
IOB76B	I/O	5	DQ10		Comp_of_IOB76A	True	U16
IOB78A	I/O	5	DQ11		True_of_IOB78B	True	Y16
IOB78B	I/O	5	DQ11		Comp_of_IOB78A	True	AA16
IOB80A	I/O	5	DQ11		True_of_IOB80B	True	AB16
IOB80B	I/O	5	DQ11		Comp_of_IOB80A	True	AB17
IOB83A	I/O	5	DQ11		True_of_IOB83B	True	AA15
IOB83B	I/O	5	DQ11		Comp_of_IOB83A	True	AB15
IOB85A	I/O	5	DQ11		True_of_IOB85B	True	W14
IOB85B	I/O	5	DQ11		Comp_of_IOB85A	True	Y14
IOB87A	I/O	5	DQ11		True_of_IOB87B	True	Y13
IOB87B	I/O	5	DQ11		Comp_of_IOB87A	True	AA14
IOB89A	I/O	5	DQS11		True_of_IOB89B	True	AA13
IOB89B	I/O	5	DQS11		Comp_of_IOB89A	True	AB13

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
BANK4 True LVDS Pair							
IOB102A/MCS_N	I/O	4	DQ12	MCS_N	True_of_IOB102B	True	T19
IOB102B/D08/SO	I/O	4	DQ12	D08/SO	Comp_of_IOB102A	True	T20
IOB104A/PUDC_B	I/O	4	DQS12	PUDC_B	True_of_IOB104B	True	U22
IOB104B/EMCCLK	I/O	4	DQS12	EMCCLK	Comp_of_IOB104A	True	V22
IOB106A/CSI_B	I/O	4	DQ13	CSI_B	True_of_IOB106B	True	V17
IOB106B/D31	I/O	4	DQ13	D31	Comp_of_IOB106A	True	W17
IOB108A/D30	I/O	4	DQ13	D30	True_of_IOB108B	True	AA18
IOB108B/D29	I/O	4	DQ13	D29	Comp_of_IOB108A	True	AB18
IOB110A/RDWR	I/O	4	DQS13	RDWR	True_of_IOB110B	True	AA19
IOB110B/DOOUT_CSO_B	I/O	4	DQS13	DOOUT_CSO_B	Comp_of_IOB110A	True	AB20
IOB112A/D28	I/O	4	DQ13	D28	True_of_IOB112B	True	U17
IOB112B/D27	I/O	4	DQ13	D27	Comp_of_IOB112A	True	U18
IOB114A/SGCLKT_4/BPLL2_T_FB1/BPLL3_T_FB1	I/O	4	DQ13	SGCLKT_4/BPLL2_T_FB1/BPLL3_T_FB1	True_of_IOB114B	True	V18
IOB114B/SGCLKC_4/BPLL2_C_FB1/BPLL3_C_FB1	I/O	4	DQ13	SGCLKC_4/BPLL2_C_FB1/BPLL3_C_FB1	Comp_of_IOB114A	True	V19
IOB116A/MGCLKT_4/BPLL2_T_FB0/BPLL3_T_FB0	I/O	4	DQ13	MGCLKT_4/BPLL2_T_FB0/BPLL3_T_FB0	True_of_IOB116B	True	Y18
IOB116B/MGCLKC_4/BPLL2_C_FB0/BPLL3_C_FB0	I/O	4	DQ13	MGCLKC_4/BPLL2_C_FB0/BPLL3_C_FB0	Comp_of_IOB116A	True	Y19
IOB120A/SGCLKT_5/BPLL2_T_IN0/BPLL3_T_IN0	I/O	4	DQ14	SGCLKT_5/BPLL2_T_IN0/BPLL3_T_IN0	True_of_IOB120B	True	U20
IOB120B/SGCLKC_5/BPLL2_C_IN0/BPLL3_C_IN0	I/O	4	DQ14	SGCLKC_5/BPLL2_C_IN0/BPLL3_C_IN0	Comp_of_IOB120A	True	V20
IOB122A/MGCLKT_5/BPLL2_T_IN1/BPLL3_T_IN1	I/O	4	DQ14	MGCLKT_5/BPLL2_T_IN1/BPLL3_T_IN1	True_of_IOB122B	True	W19
IOB122B/MGCLKC_5/BPLL2_C_IN1/BPLL3_C_IN1	I/O	4	DQ14	MGCLKC_5/BPLL2_C_IN1/BPLL3_C_IN1	Comp_of_IOB122A	True	W20
IOB124A/D09	I/O	4	DQ14	D09	True_of_IOB124B	True	W21

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOB124B/D10	I/O	4	DQ14	D10	Comp_of_IOB124A	True	W22
IOB126A/D11	I/O	4	DQ14	D11	True_of_IOB126B	True	AA20
IOB126B/D12	I/O	4	DQ14	D12	Comp_of_IOB126A	True	AA21
IOB129A/D14	I/O	4	DQ14	D14	True_of_IOB129B	True	AB21
IOB129B/D15	I/O	4	DQ14	D15	Comp_of_IOB129A	True	AB22
IOB131A/SSPI_CS_N	I/O	4	DQS14	SSPI_CS_N	True_of_IOB131B	True	Y21
IOB131B/D13	I/O	4	DQS14	D13	Comp_of_IOB131A	True	Y22
IOB133A/D26	I/O	4	DQ15	D26	True_of_IOB133B	True	P14
IOB133B/D25	I/O	4	DQ15	D25	Comp_of_IOB133A	True	R14
IOB135A/CLKHOLD_N	I/O	4	DQS15	CLKHOLD_N	True_of_IOB135B	True	N17
IOB135B/D22	I/O	4	DQS15	D22	Comp_of_IOB135A	True	P17
IOB138A/D24	I/O	4	DQ15	D24	True_of_IOB138B	True	R18
IOB138B/D23	I/O	4	DQ15	D23	Comp_of_IOB138A	True	T18
IOB140A/D21	I/O	4	DQ15	D21	True_of_IOB140B	True	P15
IOB140B/D20	I/O	4	DQ15	D20	Comp_of_IOB140A	True	R16
IOB142A/D19	I/O	4	DQ15	D19	True_of_IOB142B	True	N13
IOB142B/D18	I/O	4	DQ15	D18	Comp_of_IOB142A	True	N14
IOB144A/D17	I/O	4	DQ15	D17	True_of_IOB144B	True	P16
IOB144B/D16	I/O	4	DQ15	D16	Comp_of_IOB144A	True	R17
IOB93A/D00/MOSI	I/O	4	DQ12	D00/MOSI	True_of_IOB93B	True	P22
IOB93B/D01/DIN	I/O	4	DQ12	D01/DIN	Comp_of_IOB93A	True	R22
IOB95A/D02	I/O	4	DQ12	D02	True_of_IOB95B	True	P21
IOB95B/D03	I/O	4	DQ12	D03	Comp_of_IOB95A	True	R21
IOB97A/D04	I/O	4	DQ12	D04	True_of_IOB97B	True	T21
IOB97B/D05/SI	I/O	4	DQ12	D05/SI	Comp_of_IOB97A	True	U21
IOB99A/D06/SSPI_CLK	I/O	4	DQ12	D06/SSPI_CLK	True_of_IOB99B	True	P19
IOB99B/D07/SSPI_WPN	I/O	4	DQ12	D07/SSPI_WPN	Comp_of_IOB99A	True	R19

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
BANK3 True LVDS Pair							
IOR101A	I/O	3	DQ16		True_of_IOR101B	True	J15
IOR101B	I/O	3	DQ16		Comp_of_IOR101A	True	H15
IOR103A	I/O	3	DQS16		True_of_IOR103B	True	J14
IOR103B	I/O	3	DQS16		Comp_of_IOR103A	True	H14
IOR105A	I/O	3	DQ16		True_of_IOR105B	True	G15
IOR105B	I/O	3	DQ16		Comp_of_IOR105A	True	G16
IOR107A/ADCINCK1	I/O	3	DQ16	ADCINCK1	True_of_IOR107B	True	G17
IOR107B	I/O	3	DQ16		Comp_of_IOR107A	True	G18
IOR56A	I/O	3	DQ19		True_of_IOR56B	True	M13
IOR56B	I/O	3	DQ19		Comp_of_IOR56A	True	L13
IOR58A	I/O	3	DQS19		True_of_IOR58B	True	K17
IOR58B	I/O	3	DQS19		Comp_of_IOR58A	True	J17
IOR60A	I/O	3	DQ19		True_of_IOR60B	True	K13
IOR60B	I/O	3	DQ19		Comp_of_IOR60A	True	K14
IOR62A	I/O	3	DQ19		True_of_IOR62B	True	L14
IOR62B	I/O	3	DQ19		Comp_of_IOR62A	True	L15
IOR65A	I/O	3	DQ19		True_of_IOR65B	True	L16
IOR65B	I/O	3	DQ19		Comp_of_IOR65A	True	K16
IOR67A	I/O	3	DQ19		True_of_IOR67B	True	M15
IOR67B	I/O	3	DQ19		Comp_of_IOR67A	True	M16
IOR69A	I/O	3	DQ18		True_of_IOR69B	True	H20
IOR69B	I/O	3	DQ18		Comp_of_IOR69A	True	G20
IOR71A	I/O	3	DQ18		True_of_IOR71B	True	M21
IOR71B	I/O	3	DQ18		Comp_of_IOR71A	True	L21
IOR74A	I/O	3	DQ18		True_of_IOR74B	True	J22
IOR74B	I/O	3	DQ18		Comp_of_IOR74A	True	H22
IOR76A	I/O	3	DQS18		True_of_IOR76B	True	K21
IOR76B	I/O	3	DQS18		Comp_of_IOR76A	True	K22

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOR78A/SGCLKT_3/RPLL2_T_FB0/RPLL3_T_FB0	I/O	3	DQ18	SGCLKT_3/RPLL2_T_FB0/RPLL3_T_FB0	True_of_IOR78B	True	J20
IOR78B/SGCLKC_3/RPLL2_C_FB0/RPLL3_C_FB0	I/O	3	DQ18	SGCLKC_3/RPLL2_C_FB0/RPLL3_C_FB0	Comp_of_IOR78A	True	J21
IOR80A/MGCLKT_3/RPLL2_T_FB1/RPLL3_T_FB1	I/O	3	DQ18	MGCLKT_3/RPLL2_T_FB1/RPLL3_T_FB1	True_of_IOR80B	True	J19
IOR80B/MGCLKC_3/RPLL2_C_FB1/RPLL3_C_FB1	I/O	3	DQ18	MGCLKC_3/RPLL2_C_FB1/RPLL3_C_FB1	Comp_of_IOR80A	True	H19
IOR83A/MGCLKT_2/RPLL2_T_IN1/RPLL3_T_IN1	I/O	3	DQ17	MGCLKT_2/RPLL2_T_IN1/RPLL3_T_IN1	True_of_IOR83B	True	K18
IOR83B/MGCLKC_2/RPLL2_C_IN1/RPLL3_C_IN1	I/O	3	DQ17	MGCLKC_2/RPLL2_C_IN1/RPLL3_C_IN1	Comp_of_IOR83A	True	K19
IOR85A/SGCLKT_2/RPLL2_T_IN0/RPLL3_T_IN0	I/O	3	DQ17	SGCLKT_2/RPLL2_T_IN0/RPLL3_T_IN0	True_of_IOR85B	True	L19
IOR85B/SGCLKC_2/RPLL2_C_IN0/RPLL3_C_IN0	I/O	3	DQ17	SGCLKC_2/RPLL2_C_IN0/RPLL3_C_IN0	Comp_of_IOR85A	True	L20
IOR87A	I/O	3	DQ17		True_of_IOR87B	True	M18
IOR87B	I/O	3	DQ17		Comp_of_IOR87A	True	L18
IOR89A	I/O	3	DQS17		True_of_IOR89B	True	N22
IOR89B	I/O	3	DQS17		Comp_of_IOR89A	True	M22
IOR92A	I/O	3	DQ17		True_of_IOR92B	True	N18
IOR92B	I/O	3	DQ17		Comp_of_IOR92A	True	N19
IOR94A	I/O	3	DQ17		True_of_IOR94B	True	N20
IOR94B	I/O	3	DQ17		Comp_of_IOR94A	True	M20
IOR96A	I/O	3	DQ16		True_of_IOR96B	True	H17
IOR96B	I/O	3	DQ16		Comp_of_IOR96A	True	H18
IOR98A	I/O	3	DQ16		True_of_IOR98B	True	H13
IOR98B	I/O	3	DQ16		Comp_of_IOR98A	True	G13

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
BANK2 True LVDS Pair							
IOR11A	I/O	2	DQ23		True_of_IOR11B	True	F13
IOR11B	I/O	2	DQ23		Comp_of_IOR11A	True	F14
IOR13A	I/O	2	DQ23		True_of_IOR13B	True	F16
IOR13B	I/O	2	DQ23		Comp_of_IOR13A	True	E17
IOR15A	I/O	2	DQ22		True_of_IOR15B	True	A13
IOR15B	I/O	2	DQ22		Comp_of_IOR15A	True	A14
IOR17A	I/O	2	DQ22		True_of_IOR17B	True	C13
IOR17B	I/O	2	DQ22		Comp_of_IOR17A	True	B13
IOR20A	I/O	2	DQS22		True_of_IOR20B	True	A15
IOR20B	I/O	2	DQS22		Comp_of_IOR20A	True	A16
IOR22A	I/O	2	DQ22		True_of_IOR22B	True	B15
IOR22B	I/O	2	DQ22		Comp_of_IOR22A	True	B16
IOR24A/SGCLKT_1/RPLL0_T_FB0/RPLL1_T_FB0	I/O	2	DQ22	SGCLKT_1/RPLL0_T_FB0/RPLL1_T_FB0	True_of_IOR24B	True	B17
IOR24B/SGCLKC_1/RPLL0_C_FB0/RPLL1_C_FB0	I/O	2	DQ22	SGCLKC_1/RPLL0_C_FB0/RPLL1_C_FB0	Comp_of_IOR24A	True	B18
IOR26A/MGCLKT_1/RPLL0_T_FB1/RPLL1_T_FB1	I/O	2	DQ22	MGCLKT_1/RPLL0_T_FB1/RPLL1_T_FB1	True_of_IOR26B	True	D17
IOR26B/MGCLKC_1/RPLL0_C_FB1/RPLL1_C_FB1	I/O	2	DQ22	MGCLKC_1/RPLL0_C_FB1/RPLL1_C_FB1	Comp_of_IOR26A	True	C17
IOR29A/SGCLKT_0/RPLL0_T_IN0/RPLL1_T_IN0	I/O	2	DQ21	SGCLKT_0/RPLL0_T_IN0/RPLL1_T_IN0	True_of_IOR29B	True	E19
IOR29B/SGCLKC_0/RPLL0_C_IN0/RPLL1_C_IN0	I/O	2	DQ21	SGCLKC_0/RPLL0_C_IN0/RPLL1_C_IN0	Comp_of_IOR29A	True	D19
IOR2A	I/O	2	DQ23		True_of_IOR2B	True	E13
IOR2B	I/O	2	DQ23		Comp_of_IOR2A	True	E14
IOR31A/MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	I/O	2	DQ21	MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	True_of_IOR31B	True	C18

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG484F ^[1]
IOR31B/MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	I/O	2	DQ21	MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	Comp_of_IOR31A	True	C19
IOR33A	I/O	2	DQ21		True_of_IOR33B	True	F19
IOR33B	I/O	2	DQ21		Comp_of_IOR33A	True	F20
IOR35A	I/O	2	DQS21		True_of_IOR35B	True	F18
IOR35B	I/O	2	DQS21		Comp_of_IOR35A	True	E18
IOR38A	I/O	2	DQ21		True_of_IOR38B	True	A18
IOR38B	I/O	2	DQ21		Comp_of_IOR38A	True	A19
IOR40A	I/O	2	DQ21		True_of_IOR40B	True	B20
IOR40B	I/O	2	DQ21		Comp_of_IOR40A	True	A20
IOR42A	I/O	2	DQ20		True_of_IOR42B	True	C22
IOR42B	I/O	2	DQ20		Comp_of_IOR42A	True	B22
IOR44A	I/O	2	DQS20		True_of_IOR44B	True	B21
IOR44B	I/O	2	DQS20		Comp_of_IOR44A	True	A21
IOR47A	I/O	2	DQ20		True_of_IOR47B	True	D20
IOR47B	I/O	2	DQ20		Comp_of_IOR47A	True	C20
IOR49A	I/O	2	DQ20		True_of_IOR49B	True	E22
IOR49B	I/O	2	DQ20		Comp_of_IOR49A	True	D22
IOR4A	I/O	2	DQ23		True_of_IOR4B	True	D14
IOR4B	I/O	2	DQ23		Comp_of_IOR4A	True	D15
IOR51A	I/O	2	DQ20		True_of_IOR51B	True	E21
IOR51B	I/O	2	DQ20		Comp_of_IOR51A	True	D21
IOR53A	I/O	2	DQ20		True_of_IOR53B	True	G21
IOR53B	I/O	2	DQ20		Comp_of_IOR53A	True	G22
IOR6A	I/O	2	DQ23		True_of_IOR6B	True	E16
IOR6B	I/O	2	DQ23		Comp_of_IOR6A	True	D16
IOR8A	I/O	2	DQS23		True_of_IOR8B	True	C14
IOR8B	I/O	2	DQS23		Comp_of_IOR8A	True	C15

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
BANK10 True LVDS Pair							
IOB169A/TDO	I/O	10	none	TDO	True_of_IOB169B	True	J10
IOB169B/TMS	I/O	10	none	TMS	Comp_of_IOB169A	True	H11
IOB171A/READY	I/O	10	none	READY	True_of_IOB171B	True	V11
IOB171B/DONE	I/O	10	none	DONE	Comp_of_IOB171A	True	W10
IOB173A/TCK	I/O	10	none	TCK	True_of_IOB173B	True	H12
IOB173B/TDI	I/O	10	none	TDI	Comp_of_IOB173A	True	H10
IOB175A/MODE0	I/O	10	none	MODE0	True_of_IOB175B	True	AB7
IOB175B/CCLK	I/O	10	none	CCLK	Comp_of_IOB175A	True	H13
IOB177A/MODE1	I/O	10	none	MODE1	True_of_IOB177B	True	Y9
IOB177B/MODE2	I/O	10	none	MODE2	Comp_of_IOB177A	True	W9
IOB179A/CFGBVS	I/O	10	none	CFGBVS	True_of_IOB179B	True	AB15
IOB179B/RECONFIG_N	I/O	10	none	RECONFIG_N	Comp_of_IOB179A	True	AE16
BANK7 True LVDS Pair							
IOL11A	I/O	7	DQ0		True_of_IOL11B	True	H9
IOL11B	I/O	7	DQ0		Comp_of_IOL11A	True	G9
IOL13A	I/O	7	DQ0		True_of_IOL13B	True	H6
IOL13B	I/O	7	DQ0		Comp_of_IOL13A	True	G6
IOL15A	I/O	7	DQ1		True_of_IOL15B	True	B4
IOL15B	I/O	7	DQ1		Comp_of_IOL15A	True	A4
IOL17A	I/O	7	DQ1		True_of_IOL17B	True	D3
IOL17B	I/O	7	DQ1		Comp_of_IOL17A	True	C3
IOL20A	I/O	7	DQ1		True_of_IOL20B	True	F3
IOL20B	I/O	7	DQ1		Comp_of_IOL20A	True	E3
IOL22A	I/O	7	DQS1		True_of_IOL22B	True	B5
IOL22B	I/O	7	DQS1		Comp_of_IOL22A	True	A5
IOL24A/MGCLKT_11/LPLL0_T_IN1/LPLL1_T_IN1	I/O	7	DQ1	MGCLKT_11/LPLL0_T_IN1/LPLL1_T_IN1	True_of_IOL24B	True	E5
IOL24B/MGCLKC_11/LPLL0_C_IN1/LPLL1_C_IN1	I/O	7	DQ1	MGCLKC_11/LPLL0_C_IN1/LPLL1_C_IN1	Comp_of_IOL24A	True	D5

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOL26A/SGCLKT_11/LPLL0_T_IN0/LPLL1_T_IN0	I/O	7	DQ1	SGCLKT_11/LPLL0_T_IN0/LPLL1_T_IN0	True_of_IOL26B	True	D4
IOL26B/SGCLKC_11/LPLL0_C_IN0/LPLL1_C_IN0	I/O	7	DQ1	SGCLKC_11/LPLL0_C_IN0/LPLL1_C_IN0	Comp_of_IOL26A	True	C4
IOL29A/MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	I/O	7	DQ2	MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	True_of_IOL29B	True	G5
IOL29B/MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	I/O	7	DQ2	MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	Comp_of_IOL29A	True	F5
IOL2A/ADCINCK0	I/O	7	DQ0	ADCINCK0	True_of_IOL2B	True	E6
IOL2B	I/O	7	DQ0		Comp_of_IOL2A	True	D6
IOL31A/SGCLKT_10/LPLL0_T_FB0/LPLL1_T_FB0	I/O	7	DQ2	SGCLKT_10/LPLL0_T_FB0/LPLL1_T_FB0	True_of_IOL31B	True	G4
IOL31B/SGCLKC_10/LPLL0_C_FB0/LPLL1_C_FB0	I/O	7	DQ2	SGCLKC_10/LPLL0_C_FB0/LPLL1_C_FB0	Comp_of_IOL31A	True	F4
IOL33A	I/O	7	DQ2		True_of_IOL33B	True	J6
IOL33B	I/O	7	DQ2		Comp_of_IOL33A	True	J5
IOL35A	I/O	7	DQS2		True_of_IOL35B	True	J4
IOL35B	I/O	7	DQS2		Comp_of_IOL35A	True	H4
IOL38A	I/O	7	DQ2		True_of_IOL38B	True	L8
IOL38B	I/O	7	DQ2		Comp_of_IOL38A	True	K8
IOL40A	I/O	7	DQ2		True_of_IOL40B	True	K7
IOL40B	I/O	7	DQ2		Comp_of_IOL40A	True	K6
IOL42A	I/O	7	DQ3		True_of_IOL42B	True	A3
IOL42B	I/O	7	DQ3		Comp_of_IOL42A	True	A2
IOL44A	I/O	7	DQ3		True_of_IOL44B	True	C2
IOL44B	I/O	7	DQ3		Comp_of_IOL44A	True	B2
IOL47A	I/O	7	DQ3		True_of_IOL47B	True	F2
IOL47B	I/O	7	DQ3		Comp_of_IOL47A	True	E2
IOL49A	I/O	7	DQS3		True_of_IOL49B	True	C1
IOL49B	I/O	7	DQS3		Comp_of_IOL49A	True	B1

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOL4A	I/O	7	DQS0		True_of_IOL4B	True	H7
IOL4B	I/O	7	DQS0		Comp_of_IOL4A	True	G7
IOL51A	I/O	7	DQ3		True_of_IOL51B	True	E1
IOL51B	I/O	7	DQ3		Comp_of_IOL51A	True	D1
IOL53A	I/O	7	DQ3		True_of_IOL53B	True	G2
IOL53B	I/O	7	DQ3		Comp_of_IOL53A	True	G1
IOL6A	I/O	7	DQ0		True_of_IOL6B	True	H8
IOL6B	I/O	7	DQ0		Comp_of_IOL6A	True	G8
IOL8A	I/O	7	DQ0		True_of_IOL8B	True	F8
IOL8B	I/O	7	DQ0		Comp_of_IOL8A	True	F7
BANK6 True LVDS Pair							
IOL101A	I/O	6	DQ7		True_of_IOL101B	True	R8
IOL101B	I/O	6	DQ7		Comp_of_IOL101A	True	P8
IOL103A	I/O	6	DQ7		True_of_IOL103B	True	R7
IOL103B	I/O	6	DQ7		Comp_of_IOL103A	True	R6
IOL105A	I/O	6	DQ7		True_of_IOL105B	True	T8
IOL105B	I/O	6	DQ7		Comp_of_IOL105A	True	T7
IOL107A	I/O	6	DQ7		True_of_IOL107B	True	P6
IOL107B	I/O	6	DQ7		Comp_of_IOL107A	True	P5
IOL56A	I/O	6	DQ4		True_of_IOL56B	True	K3
IOL56B	I/O	6	DQ4		Comp_of_IOL56A	True	J3
IOL58A	I/O	6	DQ4		True_of_IOL58B	True	M7
IOL58B	I/O	6	DQ4		Comp_of_IOL58A	True	L7
IOL60A	I/O	6	DQ4		True_of_IOL60B	True	M6
IOL60B	I/O	6	DQ4		Comp_of_IOL60A	True	M5
IOL62A	I/O	6	DQ4		True_of_IOL62B	True	L5
IOL62B	I/O	6	DQ4		Comp_of_IOL62A	True	K5
IOL65A	I/O	6	DQS4		True_of_IOL65B	True	M4
IOL65B	I/O	6	DQS4		Comp_of_IOL65A	True	L4
IOL67A	I/O	6	DQ4		True_of_IOL67B	True	N7

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOL67B	I/O	6	DQ4		Comp_of_IOL67A	True	N6
IOL69A	I/O	6	DQS5		True_of_IOL69B	True	N1
IOL69B	I/O	6	DQS5		Comp_of_IOL69A	True	M1
IOL71A	I/O	6	DQ5		True_of_IOL71B	True	K1
IOL71B	I/O	6	DQ5		Comp_of_IOL71A	True	J1
IOL74A	I/O	6	DQ5		True_of_IOL74B	True	L3
IOL74B	I/O	6	DQ5		Comp_of_IOL74A	True	K2
IOL76A	I/O	6	DQ5		True_of_IOL76B	True	H2
IOL76B	I/O	6	DQ5		Comp_of_IOL76A	True	H1
IOL78A/SGCLKT_8/LPLL2_T_IN0/LPL L3_T_IN0	I/O	6	DQ5	SGCLKT_8/LPLL2_T_IN0/LPLL 3_T_IN0	True_of_IOL78B	True	M2
IOL78B/SGCLKC_8/LPLL2_C_IN0/LP LL3_C_IN0	I/O	6	DQ5	SGCLKC_8/LPLL2_C_IN0/LPLL 3_C_IN0	Comp_of_IOL78A	True	L2
IOL80A/MGCLKT_8/LPLL2_T_IN1/LPL L3_T_IN1	I/O	6	DQ5	MGCLKT_8/LPLL2_T_IN1/LPLL 3_T_IN1	True_of_IOL80B	True	N3
IOL80B/MGCLKC_8/LPLL2_C_IN1/LP LL3_C_IN1	I/O	6	DQ5	MGCLKC_8/LPLL2_C_IN1/LPLL 3_C_IN1	Comp_of_IOL80A	True	N2
IOL83A/MGCLKT_9/LPLL2_T_FB1/LP LL3_T_FB1	I/O	6	DQ6	MGCLKT_9/LPLL2_T_FB1/LPL L3_T_FB1	True_of_IOL83B	True	R3
IOL83B/MGCLKC_9/LPLL2_C_FB1/LP LL3_C_FB1	I/O	6	DQ6	MGCLKC_9/LPLL2_C_FB1/LPL L3_C_FB1	Comp_of_IOL83A	True	P3
IOL85A/SGCLKT_9/LPLL2_T_FB0/LP LL3_T_FB0	I/O	6	DQ6	SGCLKT_9/LPLL2_T_FB0/LPLL 3_T_FB0	True_of_IOL85B	True	P4
IOL85B/SGCLKC_9/LPLL2_C_FB0/LP LL3_C_FB0	I/O	6	DQ6	SGCLKC_9/LPLL2_C_FB0/LPL L3_C_FB0	Comp_of_IOL85A	True	N4
IOL87A	I/O	6	DQ6		True_of_IOL87B	True	T4
IOL87B	I/O	6	DQ6		Comp_of_IOL87A	True	T3
IOL89A	I/O	6	DQ6		True_of_IOL89B	True	T2
IOL89B	I/O	6	DQ6		Comp_of_IOL89A	True	R2
IOL92A	I/O	6	DQ6		True_of_IOL92B	True	U2

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOL92B	I/O	6	DQ6		Comp_of_IOL92A	True	U1
IOL94A	I/O	6	DQS6		True_of_IOL94B	True	R1
IOL94B	I/O	6	DQS6		Comp_of_IOL94A	True	P1
IOL96A	I/O	6	DQS7		True_of_IOL96B	True	U6
IOL96B	I/O	6	DQS7		Comp_of_IOL96A	True	U5
IOL98A	I/O	6	DQ7		True_of_IOL98B	True	T5
IOL98B	I/O	6	DQ7		Comp_of_IOL98A	True	R5
BANK5 True LVDS Pair							
IOB38A	I/O	5	DQ8		True_of_IOB38B	True	V16
IOB38B	I/O	5	DQ8		Comp_of_IOB38A	True	V17
IOB40A	I/O	5	DQ8		True_of_IOB40B	True	T14
IOB40B	I/O	5	DQ8		Comp_of_IOB40A	True	T15
IOB42A	I/O	5	DQS8		True_of_IOB42B	True	T17
IOB42B	I/O	5	DQS8		Comp_of_IOB42A	True	T18
IOB44A	I/O	5	DQ8		True_of_IOB44B	True	U15
IOB44B	I/O	5	DQ8		Comp_of_IOB44A	True	U16
IOB47A	I/O	5	DQ8		True_of_IOB47B	True	U14
IOB47B	I/O	5	DQ8		Comp_of_IOB47A	True	V14
IOB49A	I/O	5	DQ8		True_of_IOB49B	True	V18
IOB49B	I/O	5	DQ8		Comp_of_IOB49A	True	W18
IOB51A	I/O	5	DQ9		True_of_IOB51B	True	AA24
IOB51B	I/O	5	DQ9		Comp_of_IOB51A	True	AB25
IOB53A	I/O	5	DQ9		True_of_IOB53B	True	AA22
IOB53B	I/O	5	DQ9		Comp_of_IOB53A	True	AA23
IOB56A	I/O	5	DQS9		True_of_IOB56B	True	AB24
IOB56B	I/O	5	DQS9		Comp_of_IOB56A	True	AC24
IOB58A	I/O	5	DQ9		True_of_IOB58B	True	V23
IOB58B	I/O	5	DQ9		Comp_of_IOB58A	True	W23
IOB60A/SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	I/O	5	DQ9	SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	True_of_IOB60B	True	Y22

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOB60B/SGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	I/O	5	DQ9	SGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	Comp_of_IOB60A	True	Y23
IOB62A/MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	I/O	5	DQ9	MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	True_of_IOB62B	True	U22
IOB62B/MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	I/O	5	DQ9	MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	Comp_of_IOB62A	True	V22
IOB66A/MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	I/O	5	DQ10	MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	True_of_IOB66B	True	U21
IOB66B/MGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	I/O	5	DQ10	MGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	Comp_of_IOB66A	True	V21
IOB68A/SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	I/O	5	DQ10	SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	True_of_IOB68B	True	W21
IOB68B/SGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	I/O	5	DQ10	SGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	Comp_of_IOB68A	True	Y21
IOB70A	I/O	5	DQS10		True_of_IOB70B	True	T20
IOB70B	I/O	5	DQS10		Comp_of_IOB70A	True	U20
IOB72A	I/O	5	DQ10		True_of_IOB72B	True	W20
IOB72B	I/O	5	DQ10		Comp_of_IOB72A	True	Y20
IOB74A	I/O	5	DQ10		True_of_IOB74B	True	V19
IOB74B	I/O	5	DQ10		Comp_of_IOB74A	True	W19
IOB76A	I/O	5	DQ10		True_of_IOB76B	True	T19
IOB76B	I/O	5	DQ10		Comp_of_IOB76A	True	U19
IOB78A	I/O	5	DQ11		True_of_IOB78B	True	U25
IOB78B	I/O	5	DQ11		Comp_of_IOB78A	True	U26
IOB80A	I/O	5	DQ11		True_of_IOB80B	True	V26
IOB80B	I/O	5	DQ11		Comp_of_IOB80A	True	W26
IOB83A	I/O	5	DQ11		True_of_IOB83B	True	W25
IOB83B	I/O	5	DQ11		Comp_of_IOB83A	True	Y26
IOB85A	I/O	5	DQ11		True_of_IOB85B	True	V24
IOB85B	I/O	5	DQ11		Comp_of_IOB85A	True	W24

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOB87A	I/O	5	DQ11		True_of_IOB87B	True	Y25
IOB87B	I/O	5	DQ11		Comp_of_IOB87A	True	AA25
IOB89A	I/O	5	DQS11		True_of_IOB89B	True	AB26
IOB89B	I/O	5	DQS11		Comp_of_IOB89A	True	AC26
BANK4 True LVDS Pair							
IOB102A/MCS_N	I/O	4	DQ12	MCS_N	True_of_IOB102B	True	P18
IOB102B/D08/SO	I/O	4	DQ12	D08/SO	Comp_of_IOB102A	True	N18
IOB104A/PUDC_B	I/O	4	DQS12	PUDC_B	True_of_IOB104B	True	P15
IOB104B/EMCCLK	I/O	4	DQS12	EMCCLK	Comp_of_IOB104A	True	P16
IOB106A/CSI_B	I/O	4	DQ13	CSI_B	True_of_IOB106B	True	P19
IOB106B/D31	I/O	4	DQ13	D31	Comp_of_IOB106A	True	N19
IOB108A/D30	I/O	4	DQ13	D30	True_of_IOB108B	True	P23
IOB108B/D29	I/O	4	DQ13	D29	Comp_of_IOB108A	True	P24
IOB110A/RDWR	I/O	4	DQS13	RDWR	True_of_IOB110B	True	N23
IOB110B/DOUT_CSO_B	I/O	4	DQS13	DOUT_CSO_B	Comp_of_IOB110A	True	N24
IOB112A/D28	I/O	4	DQ13	D28	True_of_IOB112B	True	R20
IOB112B/D27	I/O	4	DQ13	D27	Comp_of_IOB112A	True	R21
IOB114A/SGCLKT_4/BPLL2_T_FB1/BPLL3_T_FB1	I/O	4	DQ13	SGCLKT_4/BPLL2_T_FB1/BPLL3_T_FB1	True_of_IOB114B	True	P20
IOB114B/SGCLKC_4/BPLL2_C_FB1/BPLL3_C_FB1	I/O	4	DQ13	SGCLKC_4/BPLL2_C_FB1/BPLL3_C_FB1	Comp_of_IOB114A	True	P21
IOB116A/MGCLKT_4/BPLL2_T_FB0/BPLL3_T_FB0	I/O	4	DQ13	MGCLKT_4/BPLL2_T_FB0/BPLL3_T_FB0	True_of_IOB116B	True	N21
IOB116B/MGCLKC_4/BPLL2_C_FB0/BPLL3_C_FB0	I/O	4	DQ13	MGCLKC_4/BPLL2_C_FB0/BPLL3_C_FB0	Comp_of_IOB116A	True	N22
IOB120A/SGCLKT_5/BPLL2_T_IN0/BPLL3_T_IN0	I/O	4	DQ14	SGCLKT_5/BPLL2_T_IN0/BPLL3_T_IN0	True_of_IOB120B	True	L22
IOB120B/SGCLKC_5/BPLL2_C_IN0/BPLL3_C_IN0	I/O	4	DQ14	SGCLKC_5/BPLL2_C_IN0/BPLL3_C_IN0	Comp_of_IOB120A	True	L23

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOB122A/MGCLKT_5/BPLL2_T_IN1/BPLL3_T_IN1	I/O	4	DQ14	MGCLKT_5/BPLL2_T_IN1/BPLL3_T_IN1	True_of_IOB122B	True	M21
IOB122B/MGCLKC_5/BPLL2_C_IN1/BPLL3_C_IN1	I/O	4	DQ14	MGCLKC_5/BPLL2_C_IN1/BPLL3_C_IN1	Comp_of_IOB122A	True	M22
IOB124A/D09	I/O	4	DQ14	D09	True_of_IOB124B	True	K25
IOB124B/D10	I/O	4	DQ14	D10	Comp_of_IOB124A	True	K26
IOB126A/D11	I/O	4	DQ14	D11	True_of_IOB126B	True	M20
IOB126B/D12	I/O	4	DQ14	D12	Comp_of_IOB126A	True	L20
IOB129A/D14	I/O	4	DQ14	D14	True_of_IOB129B	True	M24
IOB129B/D15	I/O	4	DQ14	D15	Comp_of_IOB129A	True	M25
IOB131A/SSPI_CS_N	I/O	4	DQS14	SSPI_CS_N	True_of_IOB131B	True	L24
IOB131B/D13	I/O	4	DQS14	D13	Comp_of_IOB131A	True	L25
IOB133A/D26	I/O	4	DQ15	D26	True_of_IOB133B	True	R25
IOB133B/D25	I/O	4	DQ15	D25	Comp_of_IOB133A	True	P25
IOB135A/CLKHOLD_N	I/O	4	DQS15	CLKHOLD_N	True_of_IOB135B	True	T24
IOB135B/D22	I/O	4	DQS15	D22	Comp_of_IOB135A	True	T25
IOB138A/D24	I/O	4	DQ15	D24	True_of_IOB138B	True	N26
IOB138B/D23	I/O	4	DQ15	D23	Comp_of_IOB138A	True	M26
IOB140A/D21	I/O	4	DQ15	D21	True_of_IOB140B	True	R26
IOB140B/D20	I/O	4	DQ15	D20	Comp_of_IOB140A	True	P26
IOB142A/D19	I/O	4	DQ15	D19	True_of_IOB142B	True	T22
IOB142B/D18	I/O	4	DQ15	D18	Comp_of_IOB142A	True	R22
IOB144A/D17	I/O	4	DQ15	D17	True_of_IOB144B	True	T23
IOB144B/D16	I/O	4	DQ15	D16	Comp_of_IOB144A	True	R23
IOB93A/D00/MOSI	I/O	4	DQ12	D00/MOSI	True_of_IOB93B	True	R14
IOB93B/D01/DIN	I/O	4	DQ12	D01/DIN	Comp_of_IOB93A	True	R15
IOB95A/D02	I/O	4	DQ12	D02	True_of_IOB95B	True	P14
IOB95B/D03	I/O	4	DQ12	D03	Comp_of_IOB95A	True	N14
IOB97A/D04	I/O	4	DQ12	D04	True_of_IOB97B	True	N16
IOB97B/D05/SI	I/O	4	DQ12	D05/SI	Comp_of_IOB97A	True	N17

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOB99A/D06/SSPI_CLK	I/O	4	DQ12	D06/SSPI_CLK	True_of_IOB99B	True	R16
IOB99B/D07/SSPI_WPN	I/O	4	DQ12	D07/SSPI_WPN	Comp_of_IOB99A	True	R17
BANK3 True LVDS Pair							
IOR101A	I/O	3	DQ16		True_of_IOR101B	True	M14
IOR101B	I/O	3	DQ16		Comp_of_IOR101A	True	L14
IOR103A	I/O	3	DQS16		True_of_IOR103B	True	K16
IOR103B	I/O	3	DQS16		Comp_of_IOR103A	True	K17
IOR105A	I/O	3	DQ16		True_of_IOR105B	True	J14
IOR105B	I/O	3	DQ16		Comp_of_IOR105A	True	J15
IOR107A/ADCINCK1	I/O	3	DQ16	ADCINCK1	True_of_IOR107B	True	K15
IOR107B	I/O	3	DQ16		Comp_of_IOR107A	True	J16
IOR56A	I/O	3	DQ19		True_of_IOR56B	True	J25
IOR56B	I/O	3	DQ19		Comp_of_IOR56A	True	J26
IOR58A	I/O	3	DQS19		True_of_IOR58B	True	E26
IOR58B	I/O	3	DQS19		Comp_of_IOR58A	True	D26
IOR60A	I/O	3	DQ19		True_of_IOR60B	True	G25
IOR60B	I/O	3	DQ19		Comp_of_IOR60A	True	F25
IOR62A	I/O	3	DQ19		True_of_IOR62B	True	H26
IOR62B	I/O	3	DQ19		Comp_of_IOR62A	True	G26
IOR65A	I/O	3	DQ19		True_of_IOR65B	True	E25
IOR65B	I/O	3	DQ19		Comp_of_IOR65A	True	D25
IOR67A	I/O	3	DQ19		True_of_IOR67B	True	G24
IOR67B	I/O	3	DQ19		Comp_of_IOR67A	True	F24
IOR69A	I/O	3	DQ18		True_of_IOR69B	True	K22
IOR69B	I/O	3	DQ18		Comp_of_IOR69A	True	K23
IOR71A	I/O	3	DQ18		True_of_IOR71B	True	F23
IOR71B	I/O	3	DQ18		Comp_of_IOR71A	True	E23
IOR74A	I/O	3	DQ18		True_of_IOR74B	True	J24
IOR74B	I/O	3	DQ18		Comp_of_IOR74A	True	H24
IOR76A	I/O	3	DQS18		True_of_IOR76B	True	G22

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOR76B	I/O	3	DQS18		Comp_of_IOR76A	True	F22
IOR78A/SGCLKT_3/RPLL2_T_FB0/RPLL3_T_FB0	I/O	3	DQ18	SGCLKT_3/RPLL2_T_FB0/RPLL3_T_FB0	True_of_IOR78B	True	J23
IOR78B/SGCLKC_3/RPLL2_C_FB0/RPLL3_C_FB0	I/O	3	DQ18	SGCLKC_3/RPLL2_C_FB0/RPLL3_C_FB0	Comp_of_IOR78A	True	H23
IOR80A/MGCLKT_3/RPLL2_T_FB1/RPLL3_T_FB1	I/O	3	DQ18	MGCLKT_3/RPLL2_T_FB1/RPLL3_T_FB1	True_of_IOR80B	True	H21
IOR80B/MGCLKC_3/RPLL2_C_FB1/RPLL3_C_FB1	I/O	3	DQ18	MGCLKC_3/RPLL2_C_FB1/RPLL3_C_FB1	Comp_of_IOR80A	True	H22
IOR83A/MGCLKT_2/RPLL2_T_IN1/RPLL3_T_IN1	I/O	3	DQ17	MGCLKT_2/RPLL2_T_IN1/RPLL3_T_IN1	True_of_IOR83B	True	K21
IOR83B/MGCLKC_2/RPLL2_C_IN1/RPLL3_C_IN1	I/O	3	DQ17	MGCLKC_2/RPLL2_C_IN1/RPLL3_C_IN1	Comp_of_IOR83A	True	J21
IOR85A/SGCLKT_2/RPLL2_T_IN0/RPLL3_T_IN0	I/O	3	DQ17	SGCLKT_2/RPLL2_T_IN0/RPLL3_T_IN0	True_of_IOR85B	True	G20
IOR85B/SGCLKC_2/RPLL2_C_IN0/RPLL3_C_IN0	I/O	3	DQ17	SGCLKC_2/RPLL2_C_IN0/RPLL3_C_IN0	Comp_of_IOR85A	True	G21
IOR87A	I/O	3	DQ17		True_of_IOR87B	True	J18
IOR87B	I/O	3	DQ17		Comp_of_IOR87A	True	H18
IOR89A	I/O	3	DQS17		True_of_IOR89B	True	K20
IOR89B	I/O	3	DQS17		Comp_of_IOR89A	True	J20
IOR92A	I/O	3	DQ17		True_of_IOR92B	True	L17
IOR92B	I/O	3	DQ17		Comp_of_IOR92A	True	L18
IOR94A	I/O	3	DQ17		True_of_IOR94B	True	J19
IOR94B	I/O	3	DQ17		Comp_of_IOR94A	True	H19
IOR96A	I/O	3	DQ16		True_of_IOR96B	True	M16
IOR96B	I/O	3	DQ16		Comp_of_IOR96A	True	M17
IOR98A	I/O	3	DQ16		True_of_IOR98B	True	M15
IOR98B	I/O	3	DQ16		Comp_of_IOR98A	True	L15

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
BANK2 True LVDS Pair							
IOR11A	I/O	2	DQ23		True_of_IOR11B	True	G19
IOR11B	I/O	2	DQ23		Comp_of_IOR11A	True	F20
IOR13A	I/O	2	DQ23		True_of_IOR13B	True	H16
IOR13B	I/O	2	DQ23		Comp_of_IOR13A	True	G16
IOR15A	I/O	2	DQ22		True_of_IOR15B	True	C17
IOR15B	I/O	2	DQ22		Comp_of_IOR15A	True	B17
IOR17A	I/O	2	DQ22		True_of_IOR17B	True	E16
IOR17B	I/O	2	DQ22		Comp_of_IOR17A	True	D16
IOR20A	I/O	2	DQS22		True_of_IOR20B	True	A17
IOR20B	I/O	2	DQS22		Comp_of_IOR20A	True	A18
IOR22A	I/O	2	DQ22		True_of_IOR22B	True	B19
IOR22B	I/O	2	DQ22		Comp_of_IOR22A	True	A19
IOR24A/SGCLKT_1/RPLL0_T_FB0/RPLL1_T_FB0	I/O	2	DQ22	SGCLKT_1/RPLL0_T_FB0/RPLL1_T_FB0	True_of_IOR24B	True	E17
IOR24B/SGCLKC_1/RPLL0_C_FB0/RPLL1_C_FB0	I/O	2	DQ22	SGCLKC_1/RPLL0_C_FB0/RPLL1_C_FB0	Comp_of_IOR24A	True	E18
IOR26A/MGCLKT_1/RPLL0_T_FB1/RPLL1_T_FB1	I/O	2	DQ22	MGCLKT_1/RPLL0_T_FB1/RPLL1_T_FB1	True_of_IOR26B	True	D18
IOR26B/MGCLKC_1/RPLL0_C_FB1/RPLL1_C_FB1	I/O	2	DQ22	MGCLKC_1/RPLL0_C_FB1/RPLL1_C_FB1	Comp_of_IOR26A	True	C18
IOR29A/SGCLKT_0/RPLL0_T_IN0/RPLL1_T_IN0	I/O	2	DQ21	SGCLKT_0/RPLL0_T_IN0/RPLL1_T_IN0	True_of_IOR29B	True	E20
IOR29B/SGCLKC_0/RPLL0_C_IN0/RPLL1_C_IN0	I/O	2	DQ21	SGCLKC_0/RPLL0_C_IN0/RPLL1_C_IN0	Comp_of_IOR29A	True	D20
IOR2A	I/O	2	DQ23		True_of_IOR2B	True	H14
IOR2B	I/O	2	DQ23		Comp_of_IOR2A	True	H15
IOR31A/MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	I/O	2	DQ21	MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	True_of_IOR31B	True	D19

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	PG676A ^[1]
IOR31B/MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	I/O	2	DQ21	MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	Comp_of_IOR31A	True	C19
IOR33A	I/O	2	DQ21		True_of_IOR33B	True	C21
IOR33B	I/O	2	DQ21		Comp_of_IOR33A	True	B21
IOR35A	I/O	2	DQS21		True_of_IOR35B	True	B20
IOR35B	I/O	2	DQS21		Comp_of_IOR35A	True	A20
IOR38A	I/O	2	DQ21		True_of_IOR38B	True	B22
IOR38B	I/O	2	DQ21		Comp_of_IOR38A	True	A22
IOR40A	I/O	2	DQ21		True_of_IOR40B	True	E21
IOR40B	I/O	2	DQ21		Comp_of_IOR40A	True	D21
IOR42A	I/O	2	DQ20		True_of_IOR42B	True	C22
IOR42B	I/O	2	DQ20		Comp_of_IOR42A	True	C23
IOR44A	I/O	2	DQS20		True_of_IOR44B	True	A23
IOR44B	I/O	2	DQS20		Comp_of_IOR44A	True	A24
IOR47A	I/O	2	DQ20		True_of_IOR47B	True	B25
IOR47B	I/O	2	DQ20		Comp_of_IOR47A	True	A25
IOR49A	I/O	2	DQ20		True_of_IOR49B	True	C26
IOR49B	I/O	2	DQ20		Comp_of_IOR49A	True	B26
IOR4A	I/O	2	DQ23		True_of_IOR4B	True	G17
IOR4B	I/O	2	DQ23		Comp_of_IOR4A	True	F17
IOR51A	I/O	2	DQ20		True_of_IOR51B	True	C24
IOR51B	I/O	2	DQ20		Comp_of_IOR51A	True	B24
IOR53A	I/O	2	DQ20		True_of_IOR53B	True	D23
IOR53B	I/O	2	DQ20		Comp_of_IOR53A	True	D24
IOR6A	I/O	2	DQ23		True_of_IOR6B	True	G15
IOR6B	I/O	2	DQ23		Comp_of_IOR6A	True	F15
IOR8A	I/O	2	DQS23		True_of_IOR8B	True	F18
IOR8B	I/O	2	DQS23		Comp_of_IOR8A	True	F19

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
BANK10 True LVDS Pair							
IOB169A/TDO	I/O	10	none	TDO	True_of_IOB169B	True	T8
IOB169B/TMS	I/O	10	none	TMS	Comp_of_IOB169A	True	R8
IOB171A/READY	I/O	10	none	READY	True_of_IOB171B	True	T10
IOB171B/DONE	I/O	10	none	DONE	Comp_of_IOB171A	True	F12
IOB173A/TCK	I/O	10	none	TCK	True_of_IOB173B	True	F8
IOB173B/TDI	I/O	10	none	TDI	Comp_of_IOB173A	True	T9
IOB175A/MODE0	I/O	10	none	MODE0	True_of_IOB175B	True	R12
IOB175B/CCLK	I/O	10	none	CCLK	Comp_of_IOB175A	True	E8
IOB177A/MODE1	I/O	10	none	MODE1	True_of_IOB177B	True	R11
IOB177B/MODE2	I/O	10	none	MODE2	Comp_of_IOB177A	True	F13
IOB179A/CFGBVS	I/O	10	none	CFGBVS	True_of_IOB179B	True	E12
IOB179B/RECONFIG_N	I/O	10	none	RECONFIG_N	Comp_of_IOB179A	True	P10
BANK7 True LVDS Pair							
IOL29A/MGCLKT_10/LPLL0_T_FB1/ LPLL1_T_FB1	I/O	7	DQ2	MGCLKT_10/LPLL0_T_FB1/LP LL1_T_FB1	True_of_IOL29B	True	P4
IOL29B/MGCLKC_10/LPLL0_C_FB1/ LPLL1_C_FB1	I/O	7	DQ2	MGCLKC_10/LPLL0_C_FB1/LP LL1_C_FB1	Comp_of_IOL29A	True	P3
IOL35A	I/O	7	DQS2		True_of_IOL35B	True	N1
IOL35B	I/O	7	DQS2		Comp_of_IOL35A	True	P1
BANK6 True LVDS Pair							
IOL83A/MGCLKT_9/LPLL2_T_FB1/L PLL3_T_FB1	I/O	6	DQ6	MGCLKT_9/LPLL2_T_FB1/LPL L3_T_FB1	True_of_IOL83B	True	R2
IOL83B/MGCLKC_9/LPLL2_C_FB1/L PLL3_C_FB1	I/O	6	DQ6	MGCLKC_9/LPLL2_C_FB1/LPL L3_C_FB1	Comp_of_IOL83A	True	R1
IOL85A/SGCLKT_9/LPLL2_T_FB0/L PLL3_T_FB0	I/O	6	DQ6	SGCLKT_9/LPLL2_T_FB0/LPL L3_T_FB0	True_of_IOL85B	True	R3
IOL85B/SGCLKC_9/LPLL2_C_FB0/L PLL3_C_FB0	I/O	6	DQ6	SGCLKC_9/LPLL2_C_FB0/LPL L3_C_FB0	Comp_of_IOL85A	True	T2
IOL87A	I/O	6	DQ6		True_of_IOL87B	True	V3

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
IOL87B	I/O	6	DQ6		Comp_of_IOL87A	True	V2
IOL89A	I/O	6	DQ6		True_of_IOL89B	True	P6
IOL89B	I/O	6	DQ6		Comp_of_IOL89A	True	P5
IOL92A	I/O	6	DQ6		True_of_IOL92B	True	R5
IOL92B	I/O	6	DQ6		Comp_of_IOL92A	True	T5
IOL94A	I/O	6	DQS6		True_of_IOL94B	True	U2
IOL94B	I/O	6	DQS6		Comp_of_IOL94A	True	U1
IOL96A	I/O	6	DQS7		True_of_IOL96B	True	
IOL96B	I/O	6	DQS7		Comp_of_IOL96A	True	
IOL98A	I/O	6	DQ7		True_of_IOL98B	True	
IOL98B	I/O	6	DQ7		Comp_of_IOL98A	True	
BANK5 True LVDS Pair							
IOB38A	I/O	5	DQ8		True_of_IOB38B	True	U4
IOB38B	I/O	5	DQ8		Comp_of_IOB38A	True	V4
IOB40A	I/O	5	DQ8		True_of_IOB40B	True	U6
IOB40B	I/O	5	DQ8		Comp_of_IOB40A	True	U5
IOB42A	I/O	5	DQS8		True_of_IOB42B	True	T4
IOB42B	I/O	5	DQS8		Comp_of_IOB42A	True	T3
IOB44A	I/O	5	DQ8		True_of_IOB44B	True	R7
IOB44B	I/O	5	DQ8		Comp_of_IOB44A	True	T7
IOB47A	I/O	5	DQ8		True_of_IOB47B	True	U7
IOB47B	I/O	5	DQ8		Comp_of_IOB47A	True	V6
IOB49A	I/O	5	DQ8		True_of_IOB49B	True	V8
IOB49B	I/O	5	DQ8		Comp_of_IOB49A	True	V7
BANK4 True LVDS Pair							
IOB102A/MCS_N	I/O	4	DQ12	MCS_N	True_of_IOB102B	True	L15
IOB102B/D08/SO	I/O	4	DQ12	D08/SO	Comp_of_IOB102A	True	M15
IOB104A/PUDC_B	I/O	4	DQS12	PUDC_B	True_of_IOB104B	True	J18
IOB104B/EMCCLK	I/O	4	DQS12	EMCCLK	Comp_of_IOB104A	True	K18
IOB106A/CSI_B	I/O	4	DQ13	CSI_B	True_of_IOB106B	True	T17

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
IOB106B/D31	I/O	4	DQ13	D31	Comp_of_IOB106A	True	U17
IOB108A/D30	I/O	4	DQ13	D30	True_of_IOB108B	True	U15
IOB108B/D29	I/O	4	DQ13	D29	Comp_of_IOB108A	True	U16
IOB110A/RDWR	I/O	4	DQS13	RDWR	True_of_IOB110B	True	R18
IOB110B/DOUT_CSO_B	I/O	4	DQS13	DOUT_CSO_B	Comp_of_IOB110A	True	T18
IOB112A/D28	I/O	4	DQ13	D28	True_of_IOB112B	True	V16
IOB112B/D27	I/O	4	DQ13	D27	Comp_of_IOB112A	True	V17
IOB114A/SGCLKT_4/BPLL2_T_FB1/ BPLL3_T_FB1	I/O	4	DQ13	SGCLKT_4/BPLL2_T_FB1/BPL L3_T_FB1	True_of_IOB114B	True	R16
IOB114B/SGCLKC_4/BPLL2_C_FB1/ BPLL3_C_FB1	I/O	4	DQ13	SGCLKC_4/BPLL2_C_FB1/BPL L3_C_FB1	Comp_of_IOB114A	True	R17
IOB116A/MGCLKT_4/BPLL2_T_FB0/ BPLL3_T_FB0	I/O	4	DQ13	MGCLKT_4/BPLL2_T_FB0/BPL L3_T_FB0	True_of_IOB116B	True	T14
IOB116B/MGCLKC_4/BPLL2_C_FB0/ BPLL3_C_FB0	I/O	4	DQ13	MGCLKC_4/BPLL2_C_FB0/BP LL3_C_FB0	Comp_of_IOB116A	True	T15
IOB120A/SGCLKT_5/BPLL2_T_IN0/ BPLL3_T_IN0	I/O	4	DQ14	SGCLKT_5/BPLL2_T_IN0/BPL L3_T_IN0	True_of_IOB120B	True	P15
IOB120B/SGCLKC_5/BPLL2_C_IN0/ BPLL3_C_IN0	I/O	4	DQ14	SGCLKC_5/BPLL2_C_IN0/BPL L3_C_IN0	Comp_of_IOB120A	True	P16
IOB122A/MGCLKT_5/BPLL2_T_IN1/ BPLL3_T_IN1	I/O	4	DQ14	MGCLKT_5/BPLL2_T_IN1/BPL L3_T_IN1	True_of_IOB122B	True	P14
IOB122B/MGCLKC_5/BPLL2_C_IN1/ BPLL3_C_IN1	I/O	4	DQ14	MGCLKC_5/BPLL2_C_IN1/BPL L3_C_IN1	Comp_of_IOB122A	True	R15
IOB124A/D09	I/O	4	DQ14	D09	True_of_IOB124B	True	M16
IOB124B/D10	I/O	4	DQ14	D10	Comp_of_IOB124A	True	M17
IOB126A/D11	I/O	4	DQ14	D11	True_of_IOB126B	True	M14
IOB126B/D12	I/O	4	DQ14	D12	Comp_of_IOB126A	True	N14
IOB129A/D14	I/O	4	DQ14	D14	True_of_IOB129B	True	N18
IOB129B/D15	I/O	4	DQ14	D15	Comp_of_IOB129A	True	P18
IOB131A/SSPI_CS_N	I/O	4	DQS14	SSPI_CS_N	True_of_IOB131B	True	N16

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
IOB131B/D13	I/O	4	DQS14	D13	Comp_of_IOB131A	True	N17
IOB133A/D26	I/O	4	DQ15	D26	True_of_IOB133B	True	R13
IOB133B/D25	I/O	4	DQ15	D25	Comp_of_IOB133A	True	T13
IOB135A/CLKHOLD_N	I/O	4	DQS15	CLKHOLD_N	True_of_IOB135B	True	V12
IOB135B/D22	I/O	4	DQS15	D22	Comp_of_IOB135A	True	V13
IOB138A/D24	I/O	4	DQ15	D24	True_of_IOB138B	True	U14
IOB138B/D23	I/O	4	DQ15	D23	Comp_of_IOB138A	True	V14
IOB140A/D21	I/O	4	DQ15	D21	True_of_IOB140B	True	T12
IOB140B/D20	I/O	4	DQ15	D20	Comp_of_IOB140A	True	U12
IOB142A/D19	I/O	4	DQ15	D19	True_of_IOB142B	True	U11
IOB142B/D18	I/O	4	DQ15	D18	Comp_of_IOB142A	True	V11
IOB144A/D17	I/O	4	DQ15	D17	True_of_IOB144B	True	U9
IOB144B/D16	I/O	4	DQ15	D16	Comp_of_IOB144A	True	V9
IOB93A/D00/MOSI	I/O	4	DQ12	D00/MOSI	True_of_IOB93B	True	K16
IOB93B/D01/DIN	I/O	4	DQ12	D01/DIN	Comp_of_IOB93A	True	L17
IOB95A/D02	I/O	4	DQ12	D02	True_of_IOB95B	True	J15
IOB95B/D03	I/O	4	DQ12	D03	Comp_of_IOB95A	True	J16
IOB97A/D04	I/O	4	DQ12	D04	True_of_IOB97B	True	K17
IOB97B/D05/SI	I/O	4	DQ12	D05/SI	Comp_of_IOB97A	True	L18
IOB99A/D06/SSPI_CLK	I/O	4	DQ12	D06/SSPI_CLK	True_of_IOB99B	True	J14
IOB99B/D07/SSPI_WPN	I/O	4	DQ12	D07/SSPI_WPN	Comp_of_IOB99A	True	K15
BANK3 True LVDS Pair							
IOR101A	I/O	3	DQ16		True_of_IOR101B	True	G14
IOR101B	I/O	3	DQ16		Comp_of_IOR101A	True	F14
IOR103A	I/O	3	DQS16		True_of_IOR103B	True	G15
IOR103B	I/O	3	DQS16		Comp_of_IOR103A	True	F15
IOR105A	I/O	3	DQ16		True_of_IOR105B	True	H17
IOR105B	I/O	3	DQ16		Comp_of_IOR105A	True	H18
IOR107A/ADCINCK1	I/O	3	DQ16	ADCINCK1	True_of_IOR107B	True	F17
IOR107B	I/O	3	DQ16		Comp_of_IOR107A	True	E18

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
IOR69A	I/O	3	DQ18		True_of_IOR69B	True	C16
IOR69B	I/O	3	DQ18		Comp_of_IOR69A	True	B17
IOR71A	I/O	3	DQ18		True_of_IOR71B	True	E17
IOR71B	I/O	3	DQ18		Comp_of_IOR71A	True	D18
IOR74A	I/O	3	DQ18		True_of_IOR74B	True	C17
IOR74B	I/O	3	DQ18		Comp_of_IOR74A	True	C18
IOR76A	I/O	3	DQS18		True_of_IOR76B	True	B16
IOR76B	I/O	3	DQS18		Comp_of_IOR76A	True	A17
IOR78A/SGCLKT_3/RPLL2_T_FB0/ RPLL3_T_FB0	I/O	3	DQ18	SGCLKT_3/RPLL2_T_FB0/RPL L3_T_FB0	True_of_IOR78B	True	E16
IOR78B/SGCLKC_3/RPLL2_C_FB0/ RPLL3_C_FB0	I/O	3	DQ18	SGCLKC_3/RPLL2_C_FB0/RP LL3_C_FB0	Comp_of_IOR78A	True	D16
IOR80A/MGCLKT_3/RPLL2_T_FB1/ RPLL3_T_FB1	I/O	3	DQ18	MGCLKT_3/RPLL2_T_FB1/RP LL3_T_FB1	True_of_IOR80B	True	E15
IOR80B/MGCLKC_3/RPLL2_C_FB1/ RPLL3_C_FB1	I/O	3	DQ18	MGCLKC_3/RPLL2_C_FB1/RP LL3_C_FB1	Comp_of_IOR80A	True	D15
IOR96A	I/O	3	DQ16		True_of_IOR96B	True	G17
IOR96B	I/O	3	DQ16		Comp_of_IOR96A	True	F18
IOR98A	I/O	3	DQ16		True_of_IOR98B	True	H16
IOR98B	I/O	3	DQ16		Comp_of_IOR98A	True	G16
BANK2 True LVDS Pair							
IOR11A	I/O	2	DQ23		True_of_IOR11B	True	B10
IOR11B	I/O	2	DQ23		Comp_of_IOR11A	True	A10
IOR13A	I/O	2	DQ23		True_of_IOR13B	True	D11
IOR13B	I/O	2	DQ23		Comp_of_IOR13A	True	C12
IOR29A/SGCLKT_0/RPLL0_T_IN0/R PLL1_T_IN0	I/O	2	DQ21	SGCLKT_0/RPLL0_T_IN0/RPL L1_T_IN0	True_of_IOR29B	True	D13
IOR29B/SGCLKC_0/RPLL0_C_IN0/ RPLL1_C_IN0	I/O	2	DQ21	SGCLKC_0/RPLL0_C_IN0/RPL L1_C_IN0	Comp_of_IOR29A	True	C13
IOR2A	I/O	2	DQ23		True_of_IOR2B	True	D8

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	UG324A ^[1]
IOR2B	I/O	2	DQ23		Comp_of_IOR2A	True	C8
IOR31A/MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	I/O	2	DQ21	MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	True_of_IOR31B	True	E13
IOR31B/MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	I/O	2	DQ21	MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	Comp_of_IOR31A	True	D14
IOR33A	I/O	2	DQ21		True_of_IOR33B	True	B12
IOR33B	I/O	2	DQ21		Comp_of_IOR33A	True	A12
IOR35A	I/O	2	DQS21		True_of_IOR35B	True	C14
IOR35B	I/O	2	DQS21		Comp_of_IOR35A	True	B15
IOR38A	I/O	2	DQ21		True_of_IOR38B	True	A13
IOR38B	I/O	2	DQ21		Comp_of_IOR38A	True	A14
IOR40A	I/O	2	DQ21		True_of_IOR40B	True	B14
IOR40B	I/O	2	DQ21		Comp_of_IOR40A	True	A15
IOR4A	I/O	2	DQ23		True_of_IOR4B	True	D9
IOR4B	I/O	2	DQ23		Comp_of_IOR4A	True	C9
IOR6A	I/O	2	DQ23		True_of_IOR6B	True	C11
IOR6B	I/O	2	DQ23		Comp_of_IOR6A	True	B11
IOR8A	I/O	2	DQS23		True_of_IOR8B	True	B9
IOR8B	I/O	2	DQS23		Comp_of_IOR8A	True	A9

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	FPG676A (FC) ^[1]
BANK10 True LVDS Pair							
IOB169A/TDO	I/O	10	none	TDO	True_of_IOB169B	True	J10
IOB169B/TMS	I/O	10	none	TMS	Comp_of_IOB169A	True	H11
IOB171A/READY	I/O	10	none	READY	True_of_IOB171B	True	V11
IOB171B/DONE	I/O	10	none	DONE	Comp_of_IOB171A	True	W10
IOB173A/TCK	I/O	10	none	TCK	True_of_IOB173B	True	H12
IOB173B/TDI	I/O	10	none	TDI	Comp_of_IOB173A	True	H10
IOB175A/MODE0	I/O	10	none	MODE0	True_of_IOB175B	True	AB7
IOB175B/CCLK	I/O	10	none	CCLK	Comp_of_IOB175A	True	H13
IOB177A/MODE1	I/O	10	none	MODE1	True_of_IOB177B	True	Y9
IOB177B/MODE2	I/O	10	none	MODE2	Comp_of_IOB177A	True	W9
IOB179A/CFGBVS	I/O	10	none	CFGBVS	True_of_IOB179B	True	AB15
IOB179B/RECONFIG_N	I/O	10	none	RECONFIG_N	Comp_of_IOB179A	True	AE16
BANK7 True LVDS Pair							
IOL11A	I/O	7	DQ0		True_of_IOL11B	True	P6
IOL11B/VREF	I/O	7	DQ0	VREF	Comp_of_IOL11A	True	P5
IOL13A	I/O	7	DQ0		True_of_IOL13B	True	R8
IOL13B	I/O	7	DQ0		Comp_of_IOL13A	True	P8
IOL15A	I/O	7	DQ1		True_of_IOL15B	True	U2
IOL15B	I/O	7	DQ1		Comp_of_IOL15A	True	U1
IOL17A	I/O	7	DQ1		True_of_IOL17B	True	T2
IOL17B	I/O	7	DQ1		Comp_of_IOL17A	True	R2
IOL20A	I/O	7	DQ1		True_of_IOL20B	True	T4
IOL20B	I/O	7	DQ1		Comp_of_IOL20A	True	T3
IOL22A	I/O	7	DQS1		True_of_IOL22B	True	R1
IOL22B	I/O	7	DQS1		Comp_of_IOL22A	True	P1

Note!							
[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	FPG676A (FC) ^[1]
IOL24A/MGCLKT_11/LPLL0_T_IN1/LPLL1_T_IN1	I/O	7	DQ1	MGCLKT_11/LPLL0_T_IN1/LPLL1_T_IN1	True_of_IOL24B	True	R3
IOL24B/MGCLKC_11/LPLL0_C_IN1/LPLL1_C_IN1	I/O	7	DQ1	MGCLKC_11/LPLL0_C_IN1/LPLL1_C_IN1	Comp_of_IOL24A	True	P3
IOL26A/SGCLKT_11/LPLL0_T_IN0/LPLL1_T_IN0	I/O	7	DQ1	SGCLKT_11/LPLL0_T_IN0/LPLL1_T_IN0	True_of_IOL26B	True	P4
IOL26B/SGCLKC_11/LPLL0_C_IN0/LPLL1_C_IN0	I/O	7	DQ1	SGCLKC_11/LPLL0_C_IN0/LPLL1_C_IN0	Comp_of_IOL26A	True	N4
IOL29A/MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	I/O	7	DQ2	MGCLKT_10/LPLL0_T_FB1/LPLL1_T_FB1	True_of_IOL29B	True	N3
IOL29B/MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	I/O	7	DQ2	MGCLKC_10/LPLL0_C_FB1/LPLL1_C_FB1	Comp_of_IOL29A	True	N2
IOL2A/ADCINCK0	I/O	7	DQ0	ADCINCK0	True_of_IOL2B	True	T8
IOL2B	I/O	7	DQ0		Comp_of_IOL2A	True	T7
IOL31A/SGCLKT_10/LPLL0_T_FB0/LPLL1_T_FB0	I/O	7	DQ2	SGCLKT_10/LPLL0_T_FB0/LPLL1_T_FB0	True_of_IOL31B	True	M2
IOL31B/SGCLKC_10/LPLL0_C_FB0/LPLL1_C_FB0	I/O	7	DQ2	SGCLKC_10/LPLL0_C_FB0/LPLL1_C_FB0	Comp_of_IOL31A	True	L2
IOL33A	I/O	7	DQ2		True_of_IOL33B	True	K1
IOL33B	I/O	7	DQ2		Comp_of_IOL33A	True	J1
IOL35A	I/O	7	DQS2		True_of_IOL35B	True	N1
IOL35B	I/O	7	DQS2		Comp_of_IOL35A	True	M1
IOL38A	I/O	7	DQ2		True_of_IOL38B	True	L3
IOL38B	I/O	7	DQ2		Comp_of_IOL38A	True	K2
IOL40A	I/O	7	DQ2		True_of_IOL40B	True	H2
IOL40B	I/O	7	DQ2		Comp_of_IOL40A	True	H1
IOL42A	I/O	7	DQ3		True_of_IOL42B	True	N7
IOL42B	I/O	7	DQ3		Comp_of_IOL42A	True	N6

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	FPG676A (FC) ^[1]
IOL44A	I/O	7	DQ3		True_of_IOL44B	True	M6
IOL44B/VREF	I/O	7	DQ3	VREF	Comp_of_IOL44A	True	M5
IOL47A	I/O	7	DQ3		True_of_IOL47B	True	L5
IOL47B	I/O	7	DQ3		Comp_of_IOL47A	True	K5
IOL49A	I/O	7	DQS3		True_of_IOL49B	True	M4
IOL49B	I/O	7	DQS3		Comp_of_IOL49A	True	L4
IOL4A	I/O	7	DQS0		True_of_IOL4B	True	U6
IOL4B	I/O	7	DQS0		Comp_of_IOL4A	True	U5
IOL51A	I/O	7	DQ3		True_of_IOL51B	True	K3
IOL51B	I/O	7	DQ3		Comp_of_IOL51A	True	J3
IOL53A	I/O	7	DQ3		True_of_IOL53B	True	M7
IOL53B	I/O	7	DQ3		Comp_of_IOL53A	True	L7
IOL6A	I/O	7	DQ0		True_of_IOL6B	True	T5
IOL6B	I/O	7	DQ0		Comp_of_IOL6A	True	R5
IOL8A	I/O	7	DQ0		True_of_IOL8B	True	R7
IOL8B	I/O	7	DQ0		Comp_of_IOL8A	True	R6
BANK6 True LVDS Pair							
IOL101A	I/O	6	DQ7		True_of_IOL101B	True	E6
IOL101B	I/O	6	DQ7		Comp_of_IOL101A	True	D6
IOL103A	I/O	6	DQ7		True_of_IOL103B	True	H8
IOL103B	I/O	6	DQ7		Comp_of_IOL103A	True	G8
IOL105A	I/O	6	DQ7		True_of_IOL105B	True	F8
IOL105B	I/O	6	DQ7		Comp_of_IOL105A	True	F7
IOL107A	I/O	6	DQ7		True_of_IOL107B	True	H9
IOL107B/VREF	I/O	6	DQ7	VREF	Comp_of_IOL107A	True	G9
IOL56A	I/O	6	DQ4		True_of_IOL56B	True	G2

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	FPG676A (FC) ^[1]
IOL56B	I/O	6	DQ4		Comp_of_IOL56A	True	G1
IOL58A	I/O	6	DQ4		True_of_IOL58B	True	F2
IOL58B	I/O	6	DQ4		Comp_of_IOL58A	True	E2
IOL60A	I/O	6	DQ4		True_of_IOL60B	True	C2
IOL60B/VREF	I/O	6	DQ4	VREF	Comp_of_IOL60A	True	B2
IOL62A	I/O	6	DQ4		True_of_IOL62B	True	E1
IOL62B	I/O	6	DQ4		Comp_of_IOL62A	True	D1
IOL65A	I/O	6	DQS4		True_of_IOL65B	True	C1
IOL65B	I/O	6	DQS4		Comp_of_IOL65A	True	B1
IOL67A	I/O	6	DQ4		True_of_IOL67B	True	A3
IOL67B	I/O	6	DQ4		Comp_of_IOL67A	True	A2
IOL69A	I/O	6	DQS5		True_of_IOL69B	True	J4
IOL69B	I/O	6	DQS5		Comp_of_IOL69A	True	H4
IOL71A	I/O	6	DQ5		True_of_IOL71B	True	K7
IOL71B	I/O	6	DQ5		Comp_of_IOL71A	True	K6
IOL74A	I/O	6	DQ5		True_of_IOL74B	True	J6
IOL74B	I/O	6	DQ5		Comp_of_IOL74A	True	J5
IOL76A	I/O	6	DQ5		True_of_IOL76B	True	L8
IOL76B	I/O	6	DQ5		Comp_of_IOL76A	True	K8
IOL78A/SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	I/O	6	DQ5	SGCLKT_8/LPLL2_T_IN0/LPLL3_T_IN0	True_of_IOL78B	True	G4
IOL78B/SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	I/O	6	DQ5	SGCLKC_8/LPLL2_C_IN0/LPLL3_C_IN0	Comp_of_IOL78A	True	F4
IOL80A/MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	I/O	6	DQ5	MGCLKT_8/LPLL2_T_IN1/LPLL3_T_IN1	True_of_IOL80B	True	G5
IOL80B/MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	I/O	6	DQ5	MGCLKC_8/LPLL2_C_IN1/LPLL3_C_IN1	Comp_of_IOL80A	True	F5

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	FPG676A (FC) ^[1]
IOL83A/MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	I/O	6	DQ6	MGCLKT_9/LPLL2_T_FB1/LPLL3_T_FB1	True_of_IOL83B	True	E5
IOL83B/MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	I/O	6	DQ6	MGCLKC_9/LPLL2_C_FB1/LPLL3_C_FB1	Comp_of_IOL83A	True	D5
IOL85A/SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	I/O	6	DQ6	SGCLKT_9/LPLL2_T_FB0/LPLL3_T_FB0	True_of_IOL85B	True	D4
IOL85B/SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	I/O	6	DQ6	SGCLKC_9/LPLL2_C_FB0/LPLL3_C_FB0	Comp_of_IOL85A	True	C4
IOL87A	I/O	6	DQ6		True_of_IOL87B	True	B4
IOL87B	I/O	6	DQ6		Comp_of_IOL87A	True	A4
IOL89A	I/O	6	DQ6		True_of_IOL89B	True	F3
IOL89B	I/O	6	DQ6		Comp_of_IOL89A	True	E3
IOL92A	I/O	6	DQ6		True_of_IOL92B	True	D3
IOL92B	I/O	6	DQ6		Comp_of_IOL92A	True	C3
IOL94A	I/O	6	DQS6		True_of_IOL94B	True	B5
IOL94B	I/O	6	DQS6		Comp_of_IOL94A	True	A5
IOL96A	I/O	6	DQS7		True_of_IOL96B	True	H7
IOL96B	I/O	6	DQS7		Comp_of_IOL96A	True	G7
IOL98A	I/O	6	DQ7		True_of_IOL98B	True	H6
IOL98B	I/O	6	DQ7		Comp_of_IOL98A	True	G6
BANK5 True LVDS Pair							
IOB38A	I/O	5	DQ8		True_of_IOB38B	True	D23
IOB38B	I/O	5	DQ8		Comp_of_IOB38A	True	D24
IOB40A	I/O	5	DQ8		True_of_IOB40B	True	C24
IOB40B	I/O	5	DQ8		Comp_of_IOB40A	True	B24
IOB42A	I/O	5	DQS8		True_of_IOB42B	True	A23
IOB42B	I/O	5	DQS8		Comp_of_IOB42A	True	A24

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	FPG676A (FC) ^[1]
IOB44A	I/O	5	DQ8		True_of_IOB44B	True	B25
IOB44B	I/O	5	DQ8		Comp_of_IOB44A	True	A25
IOB47A	I/O	5	DQ8		True_of_IOB47B	True	C26
IOB47B	I/O	5	DQ8		Comp_of_IOB47A	True	B26
IOB49A	I/O	5	DQ8		True_of_IOB49B	True	C22
IOB49B/VREF	I/O	5	DQ8	VREF	Comp_of_IOB49A	True	C23
IOB51A	I/O	5	DQ9		True_of_IOB51B	True	C17
IOB51B	I/O	5	DQ9		Comp_of_IOB51A	True	B17
IOB53A	I/O	5	DQ9		True_of_IOB53B	True	E16
IOB53B	I/O	5	DQ9		Comp_of_IOB53A	True	D16
IOB56A	I/O	5	DQS9		True_of_IOB56B	True	A17
IOB56B	I/O	5	DQS9		Comp_of_IOB56A	True	A18
IOB58A	I/O	5	DQ9		True_of_IOB58B	True	B19
IOB58B	I/O	5	DQ9		Comp_of_IOB58A	True	A19
IOB60A/SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	I/O	5	DQ9	SGCLKT_6/BPLL0_T_IN0/BPLL1_T_IN0	True_of_IOB60B	True	E17
IOB60BSGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	I/O	5	DQ9	SGCLKC_6/BPLL0_C_IN0/BPLL1_C_IN0	Comp_of_IOB60A	True	E18
IOB62A/MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	I/O	5	DQ9	MGCLKT_6/BPLL0_T_IN1/BPLL1_T_IN1	True_of_IOB62B	True	D18
IOB62B/MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	I/O	5	DQ9	MGCLKC_6/BPLL0_C_IN1/BPLL1_C_IN1	Comp_of_IOB62A	True	C18
IOB66A/MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	I/O	5	DQ10	MGCLKT_7/BPLL0_T_FB0/BPLL1_T_FB0	True_of_IOB66B	True	D19
IOB66B/MGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	I/O	5	DQ10	MGCLKC_7/BPLL0_C_FB0/BPLL1_C_FB0	Comp_of_IOB66A	True	C19
IOB68A/SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	I/O	5	DQ10	SGCLKT_7/BPLL0_T_FB1/BPLL1_T_FB1	True_of_IOB68B	True	E20

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	FPG676A (FC) ^[1]
IOB68B/SGCLKC_7/BPLL0_C_FB1/B1/BPLL1_C_FB1	I/O	5	DQ10	SGCLKC_7/BPLL0_C_FB1/BPLL1_C_FB1	Comp_of_IOB68A	True	D20
IOB70A	I/O	5	DQS10		True_of_IOB70B	True	B20
IOB70B	I/O	5	DQS10		Comp_of_IOB70A	True	A20
IOB72A	I/O	5	DQ10		True_of_IOB72B	True	C21
IOB72B	I/O	5	DQ10		Comp_of_IOB72A	True	B21
IOB74A	I/O	5	DQ10		True_of_IOB74B	True	B22
IOB74B	I/O	5	DQ10		Comp_of_IOB74A	True	A22
IOB76A	I/O	5	DQ10		True_of_IOB76B	True	E21
IOB76B	I/O	5	DQ10		Comp_of_IOB76A	True	D21
IOB78A	I/O	5	DQ11		True_of_IOB78B	True	G15
IOB78B	I/O	5	DQ11		Comp_of_IOB78A	True	F15
IOB80A	I/O	5	DQ11		True_of_IOB80B	True	G17
IOB80B	I/O	5	DQ11		Comp_of_IOB80A	True	F17
IOB83A	I/O	5	DQ11		True_of_IOB83B	True	H14
IOB83B	I/O	5	DQ11		Comp_of_IOB83A	True	H15
IOB85A	I/O	5	DQ11		True_of_IOB85B	True	H16
IOB85B/VREF	I/O	5	DQ11	VREF	Comp_of_IOB85A	True	G16
IOB87A	I/O	5	DQ11		True_of_IOB87B	True	G19
IOB87B	I/O	5	DQ11		Comp_of_IOB87A	True	F20
IOB89A	I/O	5	DQS11		True_of_IOB89B	True	F18
IOB89B	I/O	5	DQS11		Comp_of_IOB89A	True	F19
BANK4 True LVDS Pair							
IOB102A	I/O	4	DQ12		True_of_IOB102B	True	M16
IOB102B/VREF	I/O	4	DQ12	VREF	Comp_of_IOB102A	True	M17
IOB104A	I/O	4	DQS12		True_of_IOB104B	True	K16

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	FPG676A (FC) ^[1]
IOB104B	I/O	4	DQS12		Comp_of_IOB104A	True	K17
IOB106A	I/O	4	DQ13		True_of_IOB106B	True	K22
IOB106B	I/O	4	DQ13		Comp_of_IOB106A	True	K23
IOB108A	I/O	4	DQ13		True_of_IOB108B	True	F23
IOB108B	I/O	4	DQ13		Comp_of_IOB108A	True	E23
IOB110A	I/O	4	DQS13		True_of_IOB110B	True	G22
IOB110B	I/O	4	DQS13		Comp_of_IOB110A	True	F22
IOB112A	I/O	4	DQ13		True_of_IOB112B	True	J24
IOB112B	I/O	4	DQ13		Comp_of_IOB112A	True	H24
IOB114A/SGCLKT_4/BPLL2_T_FB1/BPLL3_T_FB1	I/O	4	DQ13	SGCLKT_4/BPLL2_T_FB1/BPLL3_T_FB1	True_of_IOB114B	True	J23
IOB114B/SGCLKC_4/BPLL2_C_FB1/BPLL3_C_FB1	I/O	4	DQ13	SGCLKC_4/BPLL2_C_FB1/BPLL3_C_FB1	Comp_of_IOB114A	True	H23
IOB116A/MGCLKT_4/BPLL2_T_FB0/BPLL3_T_FB0	I/O	4	DQ13	MGCLKT_4/BPLL2_T_FB0/BPLL3_T_FB0	True_of_IOB116B	True	H21
IOB116B/MGCLKC_4/BPLL2_C_FB0/BPLL3_C_FB0	I/O	4	DQ13	MGCLKC_4/BPLL2_C_FB0/BPLL3_C_FB0	Comp_of_IOB116A	True	H22
IOB120A/SGCLKT_5/BPLL2_T_IN0/BPLL3_T_IN0	I/O	4	DQ14	SGCLKT_5/BPLL2_T_IN0/BPLL3_T_IN0	True_of_IOB120B	True	G20
IOB120B/SGCLKC_5/BPLL2_C_IN0/BPLL3_C_IN0	I/O	4	DQ14	SGCLKC_5/BPLL2_C_IN0/BPLL3_C_IN0	Comp_of_IOB120A	True	G21
IOB122A/MGCLKT_5/BPLL2_T_IN1/BPLL3_T_IN1	I/O	4	DQ14	MGCLKT_5/BPLL2_T_IN1/BPLL3_T_IN1	True_of_IOB122B	True	K21
IOB122B/MGCLKC_5/BPLL2_C_IN1/BPLL3_C_IN1	I/O	4	DQ14	MGCLKC_5/BPLL2_C_IN1/BPLL3_C_IN1	Comp_of_IOB122A	True	J21
IOB124A	I/O	4	DQ14		True_of_IOB124B	True	L17
IOB124B	I/O	4	DQ14		Comp_of_IOB124A	True	L18
IOB126A	I/O	4	DQ14		True_of_IOB126B	True	J19

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	FPG676A (FC) ^[1]
IOB126B	I/O	4	DQ14		Comp_of_IOB126A	True	H19
IOB129A	I/O	4	DQ14		True_of_IOB129B	True	J18
IOB129B	I/O	4	DQ14		Comp_of_IOB129A	True	H18
IOB131A	I/O	4	DQS14		True_of_IOB131B	True	K20
IOB131B	I/O	4	DQS14		Comp_of_IOB131A	True	J20
IOB133A	I/O	4	DQ15		True_of_IOB133B	True	E25
IOB133B/VREF	I/O	4	DQ15	VREF	Comp_of_IOB133A	True	D25
IOB135A	I/O	4	DQS15		True_of_IOB135B	True	E26
IOB135B	I/O	4	DQS15		Comp_of_IOB135A	True	D26
IOB138A	I/O	4	DQ15		True_of_IOB138B	True	H26
IOB138B	I/O	4	DQ15		Comp_of_IOB138A	True	G26
IOB140A	I/O	4	DQ15		True_of_IOB140B	True	G24
IOB140B	I/O	4	DQ15		Comp_of_IOB140A	True	F24
IOB142A	I/O	4	DQ15		True_of_IOB142B	True	J25
IOB142B	I/O	4	DQ15		Comp_of_IOB142A	True	J26
IOB144A	I/O	4	DQ15		True_of_IOB144B	True	G25
IOB144B	I/O	4	DQ15		Comp_of_IOB144A	True	F25
IOB93A	I/O	4	DQ12		True_of_IOB93B	True	M14
IOB93B	I/O	4	DQ12		Comp_of_IOB93A	True	L14
IOB95A/ADCINCK1	I/O	4	DQ12	ADCINCK1	True_of_IOB95B	True	M15
IOB95B	I/O	4	DQ12		Comp_of_IOB95A	True	L15
IOB97A	I/O	4	DQ12		True_of_IOB97B	True	J14
IOB97B	I/O	4	DQ12		Comp_of_IOB97A	True	J15
IOB99A	I/O	4	DQ12		True_of_IOB99B	True	K15
IOB99B	I/O	4	DQ12		Comp_of_IOB99A	True	J16

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	FPG676A (FC) ^[1]
BANK3 True LVDS Pair							
IOR101A/D04	I/O	3	DQ16	D04	True_of_IOR101B	True	N16
IOR101B/D05/SI	I/O	3	DQ16	D05/SI	Comp_of_IOR101A	True	N17
IOR103A/PUDC_B	I/O	3	DQS16	PUDC_B	True_of_IOR103B	True	P15
IOR103B/EMCCLK	I/O	3	DQS16	EMCCLK	Comp_of_IOR103A	True	P16
IOR105A/D00/MOSI	I/O	3	DQ16	D00/MOSI	True_of_IOR105B	True	R14
IOR105B/D01/DIN	I/O	3	DQ16	D01/DIN	Comp_of_IOR105A	True	R15
IOR107A/D02/ADCINCK1	I/O	3	DQ16	D02/ADCINCK1	True_of_IOR107B	True	P14
IOR107B/D03	I/O	3	DQ16	D03	Comp_of_IOR107A	True	N14
IOR56A/D19	I/O	3	DQ19	D19	True_of_IOR56B	True	T22
IOR56B/D18	I/O	3	DQ19	D18	Comp_of_IOR56A	True	R22
IOR58A/CLKHOLD_N	I/O	3	DQS19	CLKHOLD_N	True_of_IOR58B	True	T24
IOR58B/D22	I/O	3	DQS19	D22	Comp_of_IOR58A	True	T25
IOR60A/D26	I/O	3	DQ19	D26	True_of_IOR60B	True	R25
IOR60B/D25/VREF	I/O	3	DQ19	D25/VREF	Comp_of_IOR60A	True	P25
IOR62A/D17	I/O	3	DQ19	D17	True_of_IOR62B	True	T23
IOR62B/D16	I/O	3	DQ19	D16	Comp_of_IOR62A	True	R23
IOR65A/D21	I/O	3	DQ19	D21	True_of_IOR65B	True	R26
IOR65B/D20	I/O	3	DQ19	D20	Comp_of_IOR65A	True	P26
IOR67A/D24	I/O	3	DQ19	D24	True_of_IOR67B	True	N26
IOR67B/D23	I/O	3	DQ19	D23	Comp_of_IOR67A	True	M26
IOR69A/D28	I/O	3	DQ18	D28	True_of_IOR69B	True	R20
IOR69B/D27	I/O	3	DQ18	D27	Comp_of_IOR69A	True	R21
IOR71A/CSI_B	I/O	3	DQ18	CSI_B	True_of_IOR71B	True	P19
IOR71B/D31	I/O	3	DQ18	D31	Comp_of_IOR71A	True	N19
IOR74A/D30	I/O	3	DQ18	D30	True_of_IOR74B	True	P23

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	FPG676A (FC) ^[1]
IOR74B/D29	I/O	3	DQ18	D29	Comp_of_IOR74A	True	P24
IOR76A/RDWR	I/O	3	DQS18	RDWR	True_of_IOR76B	True	N23
IOR76B/DOUT_CSO_B	I/O	3	DQS18	DOUT_CSO_B	Comp_of_IOR76A	True	N24
IOR78A/SGCLKT_3/RPLL2_T_FB0/RPLL3_T_FB0	I/O	3	DQ18	SGCLKT_3/RPLL2_T_FB0/RPLL3_T_FB0	True_of_IOR78B	True	P20
IOR78B/SGCLKC_3/RPLL2_C_FB0/RPLL3_C_FB0	I/O	3	DQ18	SGCLKC_3/RPLL2_C_FB0/RPLL3_C_FB0	Comp_of_IOR78A	True	P21
IOR80A/MGCLKT_3/RPLL2_T_FB1/RPLL3_T_FB1	I/O	3	DQ18	MGCLKT_3/RPLL2_T_FB1/RPLL3_T_FB1	True_of_IOR80B	True	N21
IOR80B/MGCLKC_3/RPLL2_C_FB1/RPLL3_C_FB1	I/O	3	DQ18	MGCLKC_3/RPLL2_C_FB1/RPLL3_C_FB1	Comp_of_IOR80A	True	N22
IOR83A/MGCLKT_2/RPLL2_T_IN1/RPLL3_T_IN1	I/O	3	DQ17	MGCLKT_2/RPLL2_T_IN1/RPLL3_T_IN1	True_of_IOR83B	True	M21
IOR83B/MGCLKC_2/RPLL2_C_IN1/RPLL3_C_IN1	I/O	3	DQ17	MGCLKC_2/RPLL2_C_IN1/RPLL3_C_IN1	Comp_of_IOR83A	True	M22
IOR85A/MGCLKC_2/RPLL2_T_IN0/RPLL3_T_IN0	I/O	3	DQ17	SGCLKT_2/RPLL2_T_IN0/RPLL3_T_IN0	True_of_IOR85B	True	L22
IOR85B/SGCLKC_2/RPLL2_C_IN0/RPLL3_C_IN0	I/O	3	DQ17	SGCLKC_2/RPLL2_C_IN0/RPLL3_C_IN0	Comp_of_IOR85A	True	L23
IOR87A/D14	I/O	3	DQ17	D14	True_of_IOR87B	True	M24
IOR87B/D15	I/O	3	DQ17	D15	Comp_of_IOR87A	True	M25
IOR89A/SSPI_CS_N	I/O	3	DQS17	SSPI_CS_N	True_of_IOR89B	True	L24
IOR89B/D13	I/O	3	DQS17	D13	Comp_of_IOR89A	True	L25
IOR92A/D11	I/O	3	DQ17	D11	True_of_IOR92B	True	M20
IOR92B/D12	I/O	3	DQ17	D12	Comp_of_IOR92A	True	L20
IOR94A/D09	I/O	3	DQ17	D09	True_of_IOR94B	True	K25
IOR94B/D10	I/O	3	DQ17	D10	Comp_of_IOR94A	True	K26
IOR96A/MCS_N	I/O	3	DQ16	MCS_N	True_of_IOR96B	True	P18

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	FPG676A (FC) ^[1]
IOR96B/D08/VREF/SO	I/O	3	DQ16	D08/VREF/SO	Comp_of_IOR96A	True	N18
IOR98A/D06/SSPI_CLK	I/O	3	DQ16	D06/SSPI_CLK	True_of_IOR98B	True	R16
IOR98B/D07/SSPI_WPN	I/O	3	DQ16	D07/SSPI_WPN	Comp_of_IOR98A	True	R17
BANK2 True LVDS Pair							
IOR11A	I/O	2	DQ23		True_of_IOR11B	True	U14
IOR11B	I/O	2	DQ23		Comp_of_IOR11A	True	V14
IOR13A	I/O	2	DQ23		True_of_IOR13B	True	T14
IOR13B	I/O	2	DQ23		Comp_of_IOR13A	True	T15
IOR15A	I/O	2	DQ22		True_of_IOR15B	True	V19
IOR15B	I/O	2	DQ22		Comp_of_IOR15A	True	W19
IOR17A	I/O	2	DQ22		True_of_IOR17B	True	W20
IOR17B	I/O	2	DQ22		Comp_of_IOR17A	True	Y20
IOR20A	I/O	2	DQS22		True_of_IOR20B	True	T20
IOR20B	I/O	2	DQS22		Comp_of_IOR20A	True	U20
IOR22A	I/O	2	DQ22		True_of_IOR22B	True	T19
IOR22B	I/O	2	DQ22		Comp_of_IOR22A	True	U19
IOR24A/SGCLKT_1/RPLL0_T_FB0/RPLL1_T_FB0	I/O	2	DQ22	SGCLKT_1/RPLL0_T_FB0/RPLL1_T_FB0	True_of_IOR24B	True	W21
IOR24B/SGCLKC_1/RPLL0_C_FB0/RPLL1_C_FB0	I/O	2	DQ22	SGCLKC_1/RPLL0_C_FB0/RPLL1_C_FB0	Comp_of_IOR24A	True	Y21
IOR26A/MGCLKT_1/RPLL0_T_FB1/RPLL1_T_FB1	I/O	2	DQ22	MGCLKT_1/RPLL0_T_FB1/RPLL1_T_FB1	True_of_IOR26B	True	U21
IOR26B/MGCLKC_1/RPLL0_C_FB1/RPLL1_C_FB1	I/O	2	DQ22	MGCLKC_1/RPLL0_C_FB1/RPLL1_C_FB1	Comp_of_IOR26A	True	V21
IOR29A/SGCLKT_0/RPLL0_T_IN0/RPLL1_T_IN0	I/O	2	DQ21	SGCLKT_0/RPLL0_T_IN0/RPLL1_T_IN0	True_of_IOR29B	True	Y22
IOR29B/SGCLKC_0/RPLL0_C_IN0/RPLL1_C_IN0	I/O	2	DQ21	SGCLKC_0/RPLL0_C_IN0/RPLL1_C_IN0	Comp_of_IOR29A	True	Y23

Note!

^[1] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	FPG676A (FC) ^[1]
IOR2A	I/O	2	DQ23		True_of_IOR2B	True	V16
IOR2B	I/O	2	DQ23		Comp_of_IOR2A	True	V17
IOR31A/MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	I/O	2	DQ21	MGCLKT_0/RPLL0_T_IN1/RPLL1_T_IN1	True_of_IOR31B	True	U22
IOR31B/MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	I/O	2	DQ21	MGCLKC_0/RPLL0_C_IN1/RPLL1_C_IN1	Comp_of_IOR31A	True	V22
IOR33A	I/O	2	DQ21		True_of_IOR33B	True	AA22
IOR33B	I/O	2	DQ21		Comp_of_IOR33A	True	AA23
IOR35A	I/O	2	DQS21		True_of_IOR35B	True	AB24
IOR35B	I/O	2	DQS21		Comp_of_IOR35A	True	AC24
IOR38A	I/O	2	DQ21		True_of_IOR38B	True	V23
IOR38B	I/O	2	DQ21		Comp_of_IOR38A	True	W23
IOR40A	I/O	2	DQ21		True_of_IOR40B	True	AA24
IOR40B	I/O	2	DQ21		Comp_of_IOR40A	True	AB25
IOR42A	I/O	2	DQ20		True_of_IOR42B	True	Y25
IOR42B	I/O	2	DQ20		Comp_of_IOR42A	True	AA25
IOR44A	I/O	2	DQS20		True_of_IOR44B	True	AB26
IOR44B	I/O	2	DQS20		Comp_of_IOR44A	True	AC26
IOR47A	I/O	2	DQ20		True_of_IOR47B	True	V24
IOR47B/VREF	I/O	2	DQ20	VREF	Comp_of_IOR47A	True	W24
IOR49A	I/O	2	DQ20		True_of_IOR49B	True	W25
IOR49B	I/O	2	DQ20		Comp_of_IOR49A	True	Y26
IOR4A	I/O	2	DQ23		True_of_IOR4B	True	V18
IOR4B/VREF	I/O	2	DQ23	VREF	Comp_of_IOR4A	True	W18
IOR51A	I/O	2	DQ20		True_of_IOR51B	True	V26
IOR51B	I/O	2	DQ20		Comp_of_IOR51A	True	W26
IOR53A	I/O	2	DQ20		True_of_IOR53B	True	U25

Note!							
^[1] LV version							
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	FPG676A (FC) ^[1]
IOR53B	I/O	2	DQ20		Comp_of_IOR53A	True	U26
IOR6A	I/O	2	DQ23		True_of_IOR6B	True	U15
IOR6B	I/O	2	DQ23		Comp_of_IOR6A	True	U16
IOR8A	I/O	2	DQS23		True_of_IOR8B	True	T17
IOR8B	I/O	2	DQS23		Comp_of_IOR8A	True	T18