

SCHEMATIC	CONTENT	PAGE
TOP	COVER PAGE, PLACEMENT	1-2
FPGA	FPGA	3
POWER	DC5V to 3.3V, 2.5V, 1.8V, 1.2V	4
Download	FPGA Download, ARM Download	5
INOUT	GPIO, LED, KEY, UART, LVDS/MIPI	6-7









