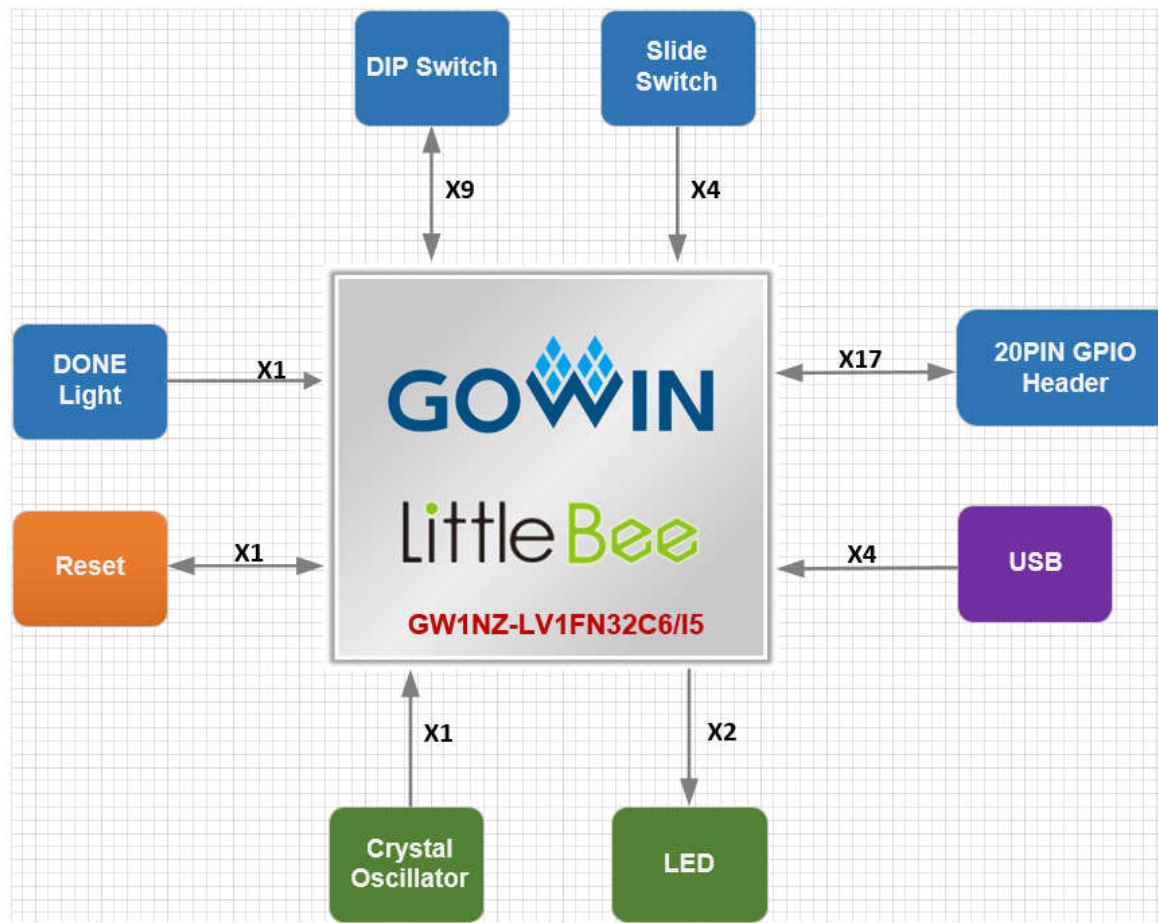
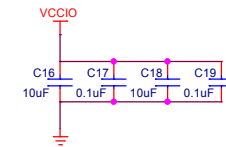
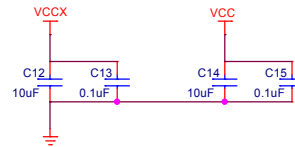
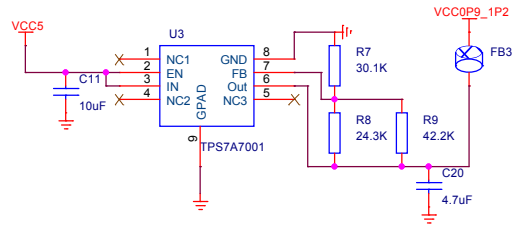
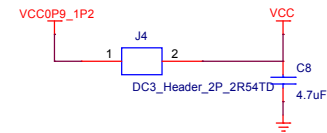
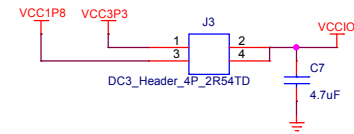
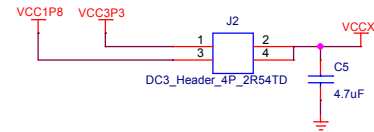
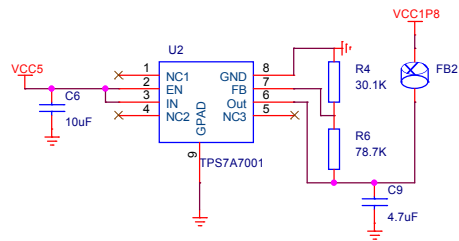
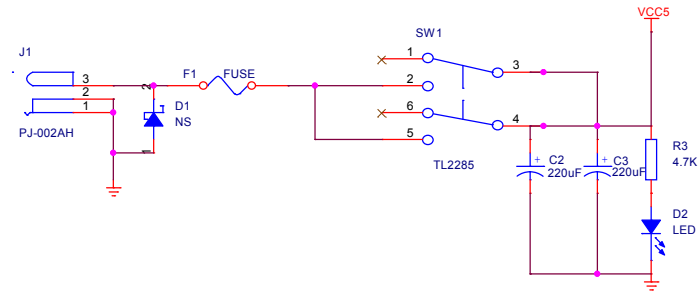
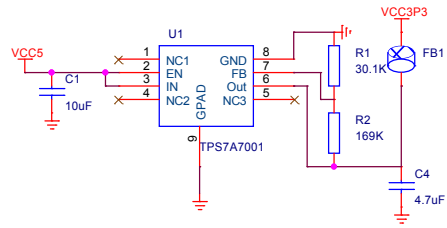


SCHEMATIC	CONTENT	PAGE
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FPGA	FPGA&DOWNLOAD	4
GPIO	GPIO&LED&KEY	5





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