

GW5A-25 Engineering Samples Update

Issue

GW5A-25 UG324S/UG324/PG256C/UG256C/UG225S engineering samples can only support MSPI mode and JTAG mode.

Products Affected

- GW5A-EV25UG324SES
- GW5A-EV25UG256CES
- GW5A-LV25UG324ES
- GW5A-EV25PG256CES
- GW5A-EV25UG225SES

Customers Affected

The mode pins of the engineering samples listed in Products Affected are hardwired to support MSPI, so customers with designs that use these samples need to leave the mode pins unconnected.

Details

The GW5A series of FPGA products are designed to support JTAG as well as GOWINSEMI's proprietary configuration modes: SSPI, MSPI, CPU, and SERIAL.

For early engineering samples whose part numbers end with 'ES', the mode pins are hardwired to support MSPI, limiting their support to MSPI mode and JTAG mode.

For products whose part numbers end with the speed grades CX/IX, they can offer support for various configuration modes based on the mode values.

For the further detailed information about configuration modes, please refer to:

- [UG704E, Arora V FPGA Product Programming and Configuration Guide](#)
- [UG985E, GW5A-25 Pinout](#)
- [UG987E, GW5A series of FPGA Products Schematic Manual](#)

Support and Feedback

Gowin Semiconductor provides customers with comprehensive technical support. If you have any questions, comments, or suggestions, please feel free to contact the local representative office or contact us directly using the information provided below.

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Revision History

Date	Version	Description
09/15/2023	1.0E	Initial release.