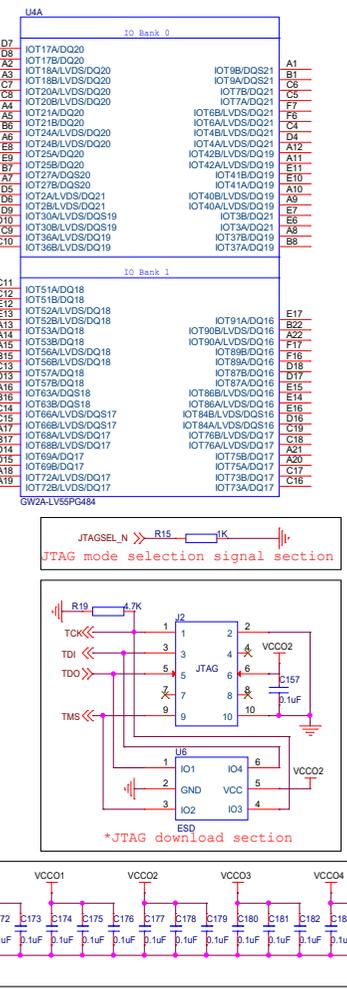
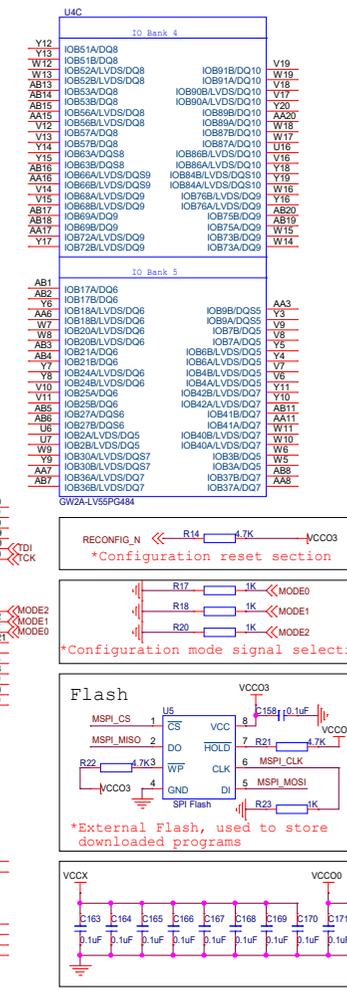
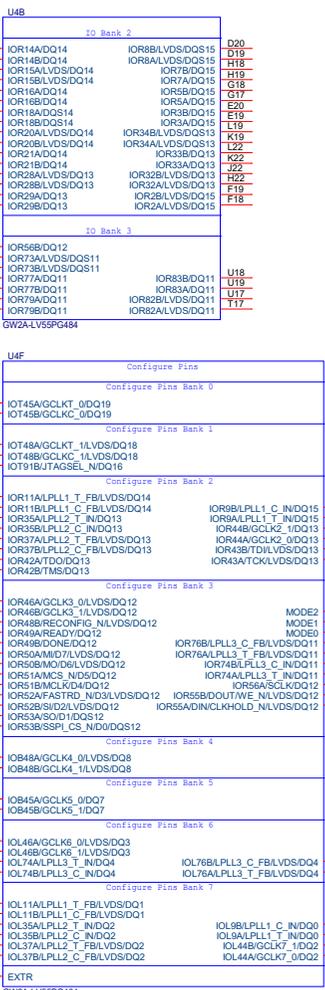
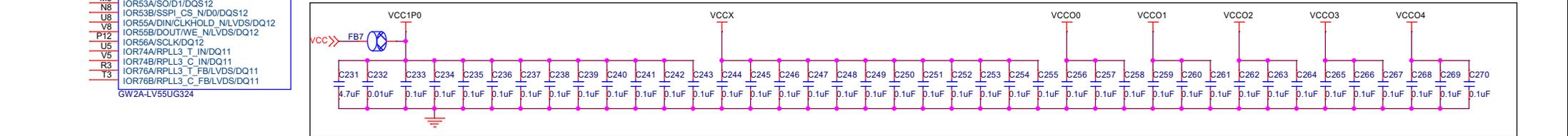
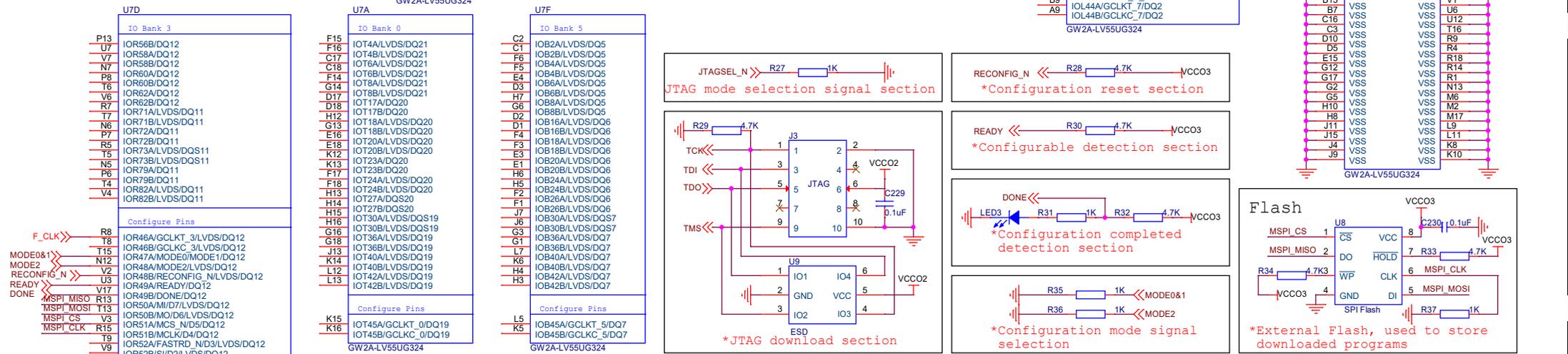
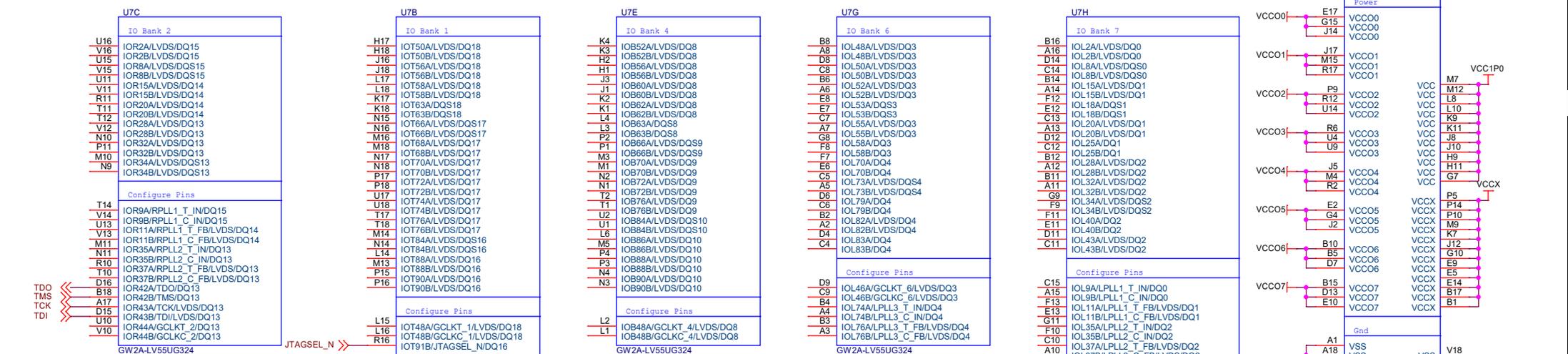


Notes:

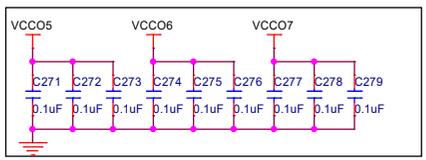
- 1.F CLK signal is an external input clock signal. It is recommended that F CLK signal be provided through an active oscillator crystal.
- 2.External Flash memory is used to store downloaded programs.
- 3.It is recommended that add an ESD protection chip to the JTAG download circuit.



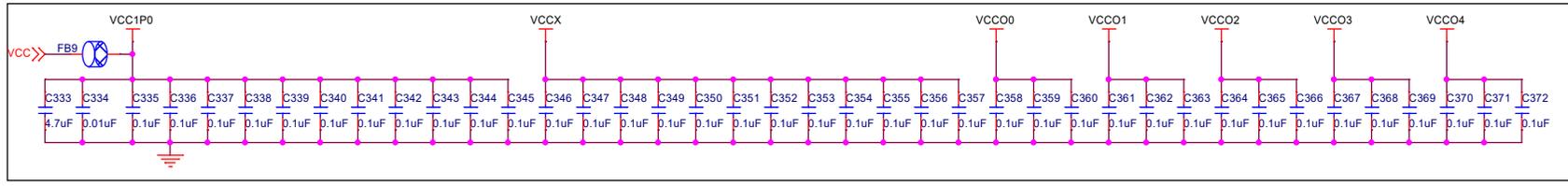
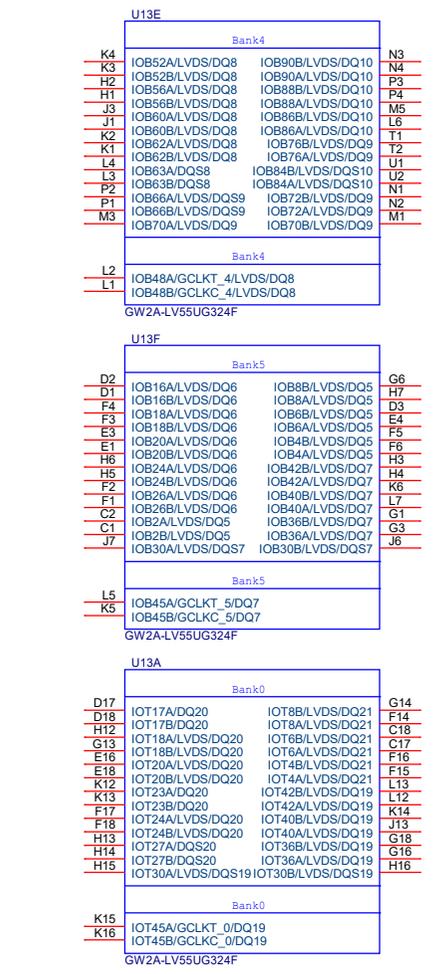
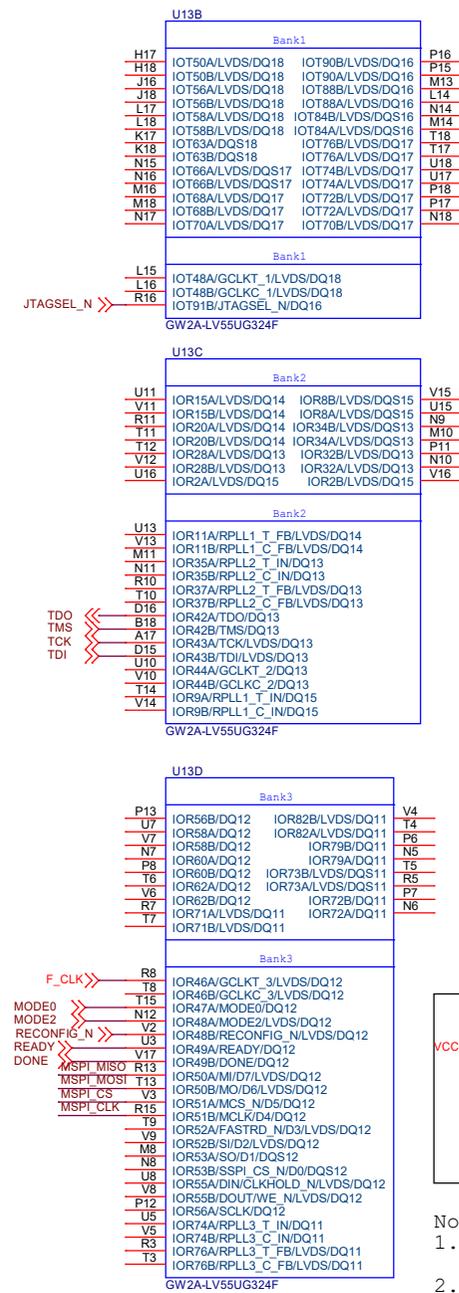
GW2A-LV55UG324



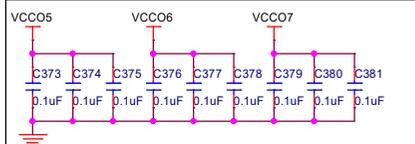
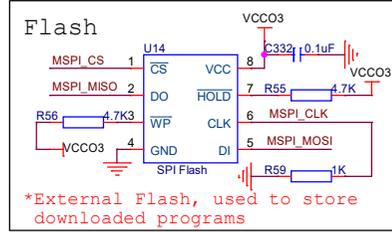
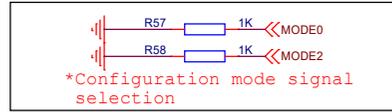
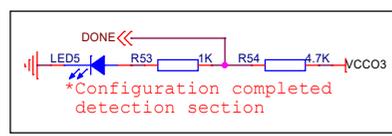
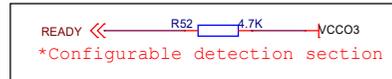
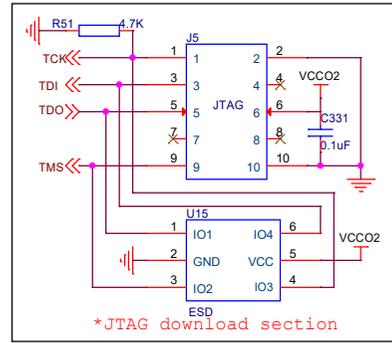
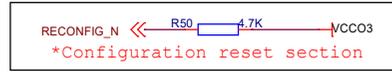
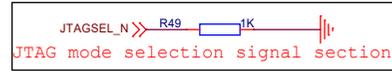
- Notes:**
- 1.F_CLK signal is an external input clock signal. It is recommended that F_CLK signal be provided through an active oscillator crystal.
 - 2.External Flash memory is used to store downloaded programs. For details about SPI Flash model selection, see Chapter 10 SPI Flash Selection in UG290.
 - 3.It is recommended that add an ESD protection chip to the JTAG download circuit.



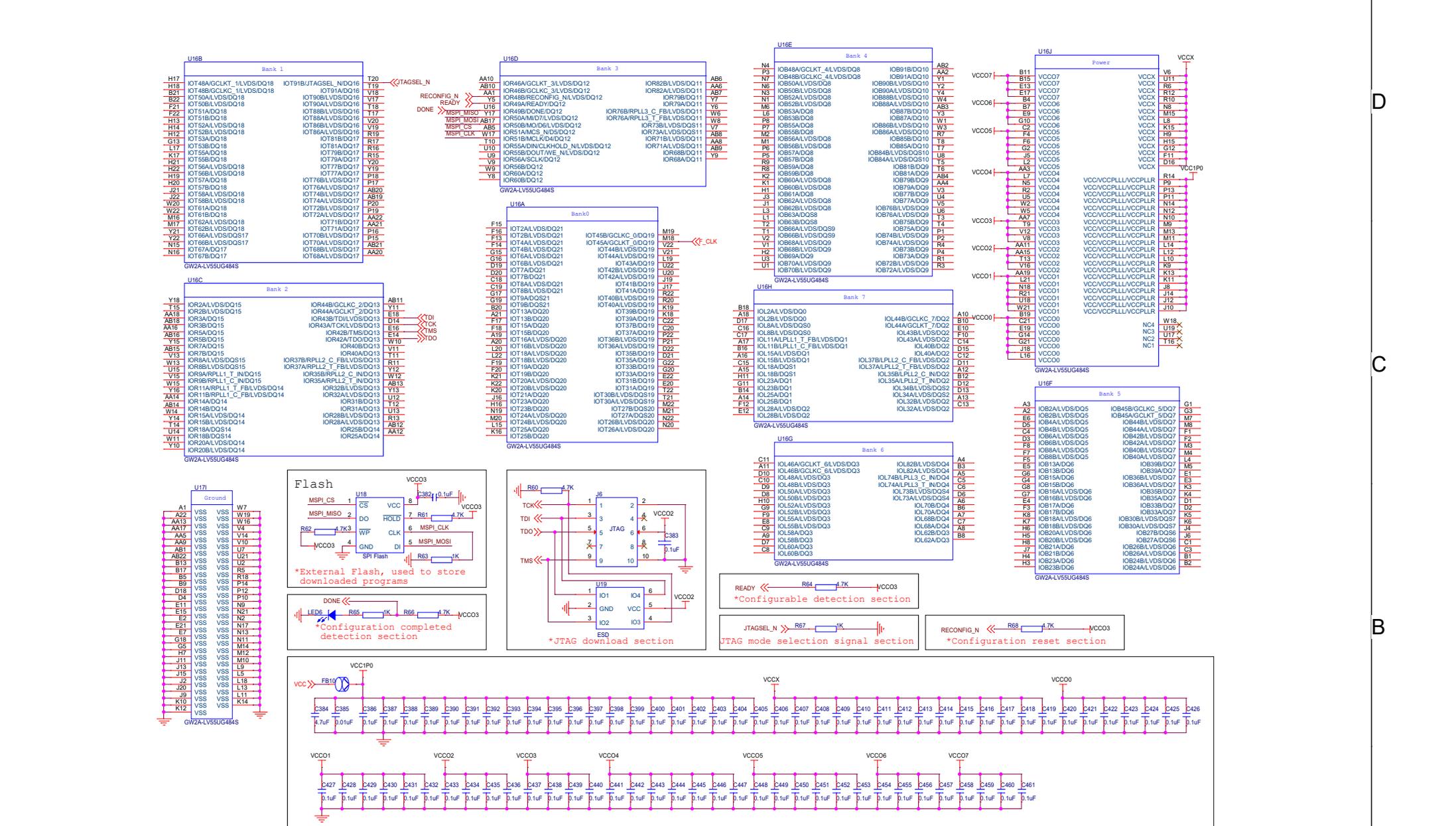
GW2A-LV55UG324F



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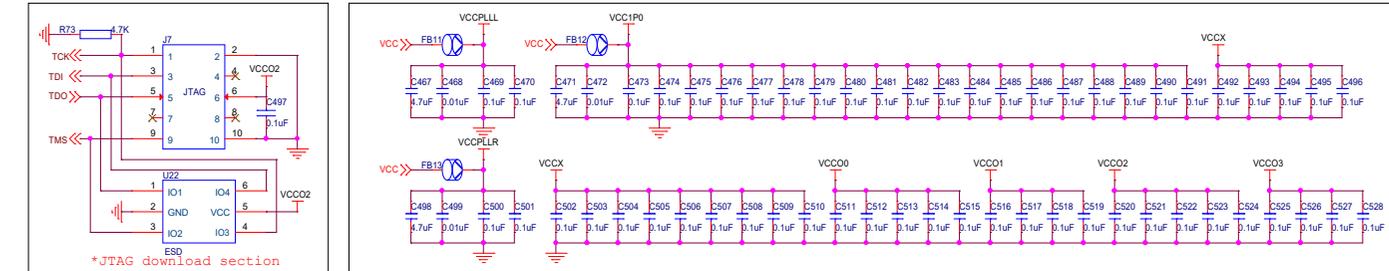
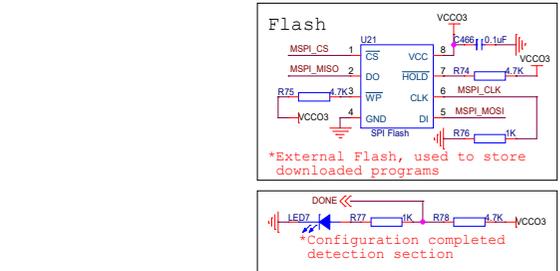
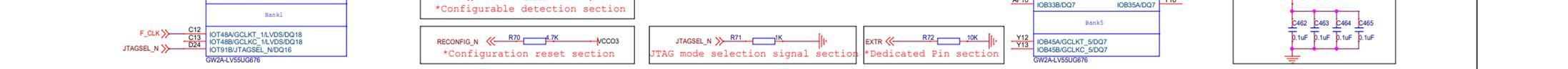
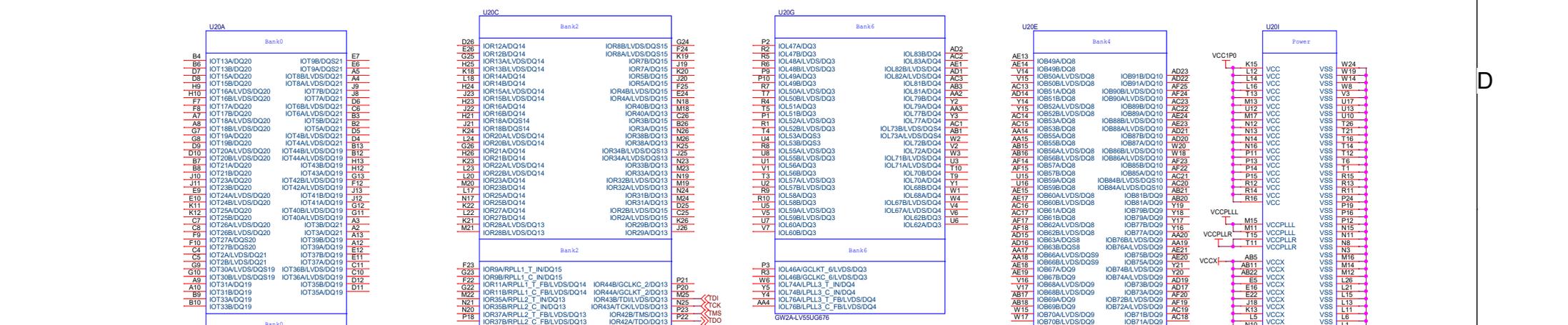


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GOWIN Minimum System Diagram		
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Date:	Tuesday, April 04, 2023	Sheet 5 of 7



Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
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For details about SPI Flash model selection, see Chapter 10 SPI Flash Selection in UG290.
- 3.It is recommended that add an ESD protection chip to the JTAG download circuit.



Notes:

1. F_CLK signal is an external input clock signal.
2. External Flash memory is used to store downloaded programs.
3. It is recommended that the JTAG model selection chip to the JTAG download circuit.

For details about SPI Flash model selection, see Chapter 10 SPI Flash Selection in UG290.

It is recommended that add an ESD protection chip to the JTAG download circuit.