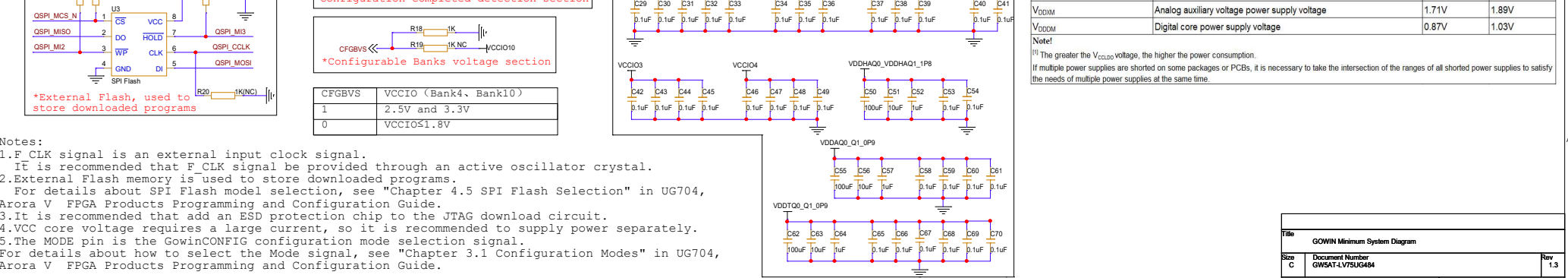
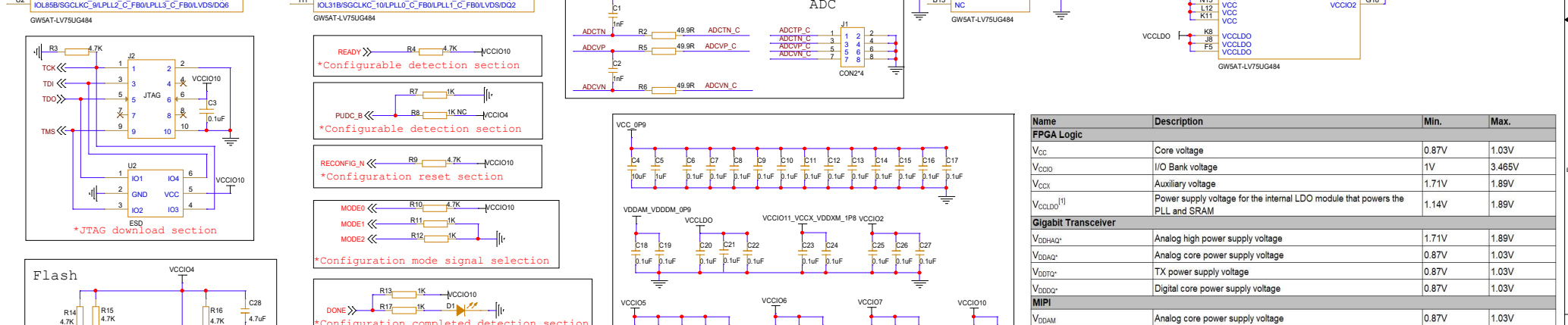
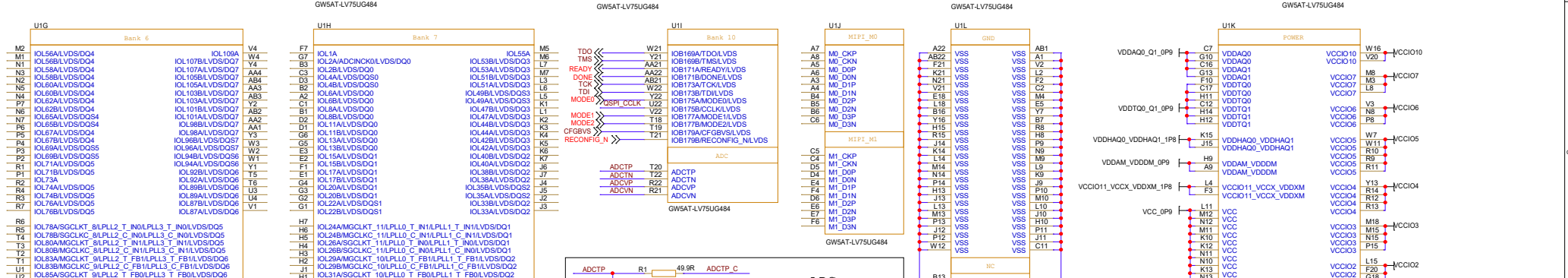
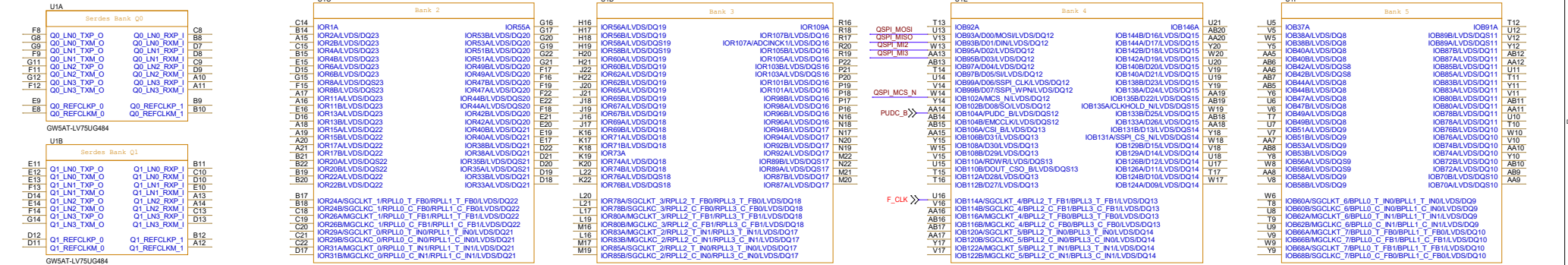


GW5AT-LV75UG484



Name	Description	Min.	Max.
V _{CC}	Core voltage	0.87V	1.03V
V _{CCIO}	I/O Bank voltage	1V	3.465V
V _{CCX}	Auxiliary voltage	1.71V	1.89V
V _{CCDDO} ^[1]	Power supply voltage for the internal LDO module that powers the PLL and SRAM	1.14V	1.89V
Gigabit Transceiver			
V _{DDHAQ} ²	Analog high power supply voltage	1.71V	1.89V
V _{DDAQ} ²	Analog core power supply voltage	0.87V	1.03V
V _{DDT0} ²	TX power supply voltage	0.87V	1.03V
V _{DDDD} ²	Digital core power supply voltage	0.87V	1.03V
MIPI			
V _{DDAM}	Analog core power supply voltage	0.87V	1.03V
V _{DDXM}	Analog auxiliary voltage power supply voltage	1.71V	1.89V
V _{DDDM}	Digital core power supply voltage	0.87V	1.03V

Note!
^[1] The greater the V_{CCDDO} voltage, the higher the power consumption.
 If multiple power supplies are shorted on some packages or PCBs, it is necessary to take the intersection of the ranges of all shorted power supplies to satisfy the needs of multiple power supplies at the same time.