

日期	版本	说明
2024/3/29	1.0	初始版本，支持UG225封装和PG484A封装。
2024/4/18	1.0.1	更新Power页中PG484A封装的VCCX电压值。 更新Power页中VCC的电压最大值。
2024/5/17	1.0.2	更新Pin Definitions页中MIPI D-PHY的管脚定义。
2024/7/5	1.1	优化Power页。 新增UG324S封装。 更新电源管脚名称及说明。 更新UG225的IOB77/IOT89/IOR36A的PLL属性。 更新PG484A的IOR36A/IOB71A/IOB77A/IOT89/IOT113的PLL属性。
2024/9/6	1.1.1	更新Power页中VREFN和VREFP电压值。
2024/11/22	1.1.2	更新Power页中VREFN和VREFP电压的描述。 更新UG225、PG484A、UG324S封装配置过程中的IO上下拉状态。
2024/12/30	1.2	新增UG225H和UG324A封装。
2025/1/17	1.2.1	更新UG324A封装的IOT135管脚信息。

管脚名称	方向	说明
用户I/O管脚		
IO [End][Row/Column Number][A/B]	I/O/LVDS	[End]提供管脚在器件中的位置信息，包括L(left) R(right) B(bottom) T(top)
		[Row/Column Number]提供管脚在器件中的具体行列位置信息，若[End]为T(top)或B(bottom)，则提供列信息，即管脚对应的CFU列数。若[End]为L(left)或R(right)，则提供行信息，即管脚对应的CFU行数
		[A/B]提供差分信号对信息
		方向为LVDS时表示该管脚只支持真LVDS输出
多功能管脚		
IO [End][Row/Column Number][A/B]/MMM		多功能管脚定义，/MMM表示在用户I/O功能的基础上有另外一种或多种功能。当这些功能不使用的時候，这些管脚可以用作用户I/O
D00~D07	I/O	CPU模式：数据输入输出端口D00~D07
D08~D15	I	CPU模式：数据输入端口D08~D15
MI0	I/O	MSPI模式：串行指令和地址输出，以及X2, X4模式下的并行数据bit0的输入管脚，连接外部Flash器件的DQ0/D/SI/IO0管脚
MI1	I/O	MSPI模式：X1模式下串行数据输入，X2, X4模式下并行数据bit1的输入管脚，连接外部Flash器件的DQ1/Q/SO/IO1管脚
MI2	I/O	MSPI模式：X4模式下并行数据bit2的输入管脚，分别连接外部Flash器件的DQ2/W#/WP#/IO2管脚
MI3	I/O	MSPI模式：X4模式下并行数据bit3的输入管脚，分别连接外部Flash器件的DQ3/HOLD#/IO3管脚
MOSI	I/O	MSPI模式：串行指令和地址输出，以及X2, X4模式下的并行数据bit0的输入管脚，连接外部Flash器件的DQ0/D/SI/IO0管脚
MISO	I/O	MSPI模式：X1模式下串行数据输入，X2, X4模式下并行数据bit1的输入管脚，连接外部Flash器件的DQ1/Q/SO/IO1管脚
MCS_N	O	MSPI模式：使能信号MCS_N，低电平有效
CCLK	I/O	配置时钟
		Slave模式：CCLK为输入，需要连接外部时钟源 Master模式：CCLK为输出
EMCCLK	I	外部输入时钟信号
		Master模式：EMCCLK用作FPGA配置逻辑，以及输出CCLK的时钟源 Slave模式：EMCCLK对slave模式没有关联
SDA	I/O	I2C串行数据线；当GowinCONFIG配置模式支持I2C时，需要外上拉
SCL	I	I2C串行时钟线；当GowinCONFIG配置模式支持I2C时，需要外上拉
SO	O	SSPI模式下SO
SI	I	SSPI模式下SI

管脚名称	方向	说明
SSI0	I	QSSPI配置模式：数据输入管脚
SSI1	I	QSSPI配置模式：数据输入管脚
SSI2	I	QSSPI配置模式：数据输入管脚
SSI3	I	QSSPI配置模式：数据输入管脚
CLKHOLD_N	I	在SSPI模式下，低电平有效
SSPI_CLK	I	SSPI/QSSPI配置模式：时钟输入管脚
SSPI_WPN	I	QSSPI配置模式：数据输入管脚
SSPI_CS_N	I	SSPI模式：使能信号SSPI_CS_N，低电平有效，内部弱上拉
CSI_B	I	CPU模式：片选信号低有效 Master CPU模式：连接外部配置controller的片选信号，也可以直接接地或者串接1KΩ电阻接地 Slave CPU模式：外部配置controller可以通过控制CSI_B信号选择FPGA Master和Slave模式都是由外部controller发出，其他模式CSI_B信号没有关联
CSO_B	O	在FPGA级联配置模式(Daisy Chain)用于连接下一级器件 SERIAL模式：输出下一级器件的配置数据 Master SPI模式：输出下一级器件的配置数据 CPU模式：输出下一级器件的片选信号
PUDC_B	I	配置过程中的弱上拉选择信号管脚： FPGA上电后在配置过程中低电平有效使能内部弱上拉电阻 PUDC_B低电平：除PUDC_B外所有的GPIO 弱上拉 PUDC_B高电平：所有GPIO 高阻 PUDC_B不允许在配置过程中悬空
RDWR_B	I	CPU模式：数据读写控制信号 RDWR为高电平时，FPGA输出数据；为低电平时，外部控制器将数据写入FPGA Master CPU模式：可以接外部控制器RDWR信号，也可以直接或者串接≤1kΩ电阻器连接到GND。 Slave CPU模式：外部控制器RDWR信号 CPU模式的低8位专用IO在wakeup后会受RDWR状态影响，CPU模式低8位设置复用fuse不受RDWR影响
GCLKC_[x]	I	GCLKT_[x]的差分对比输入管脚，C(Comp)，[x]是时钟序号
GCLKT_[x]	I	全局时钟输入管脚，T(True)，[x]是时钟序号
DOUT	O	SERIAL模式：数据输出
DIN	I	SERIAL模式：数据输入
TMS	I	JTAG模式：串行模式输入
TCK	I	JTAG模式：串行时钟输入
TDO	O	JTAG模式：串行数据输出

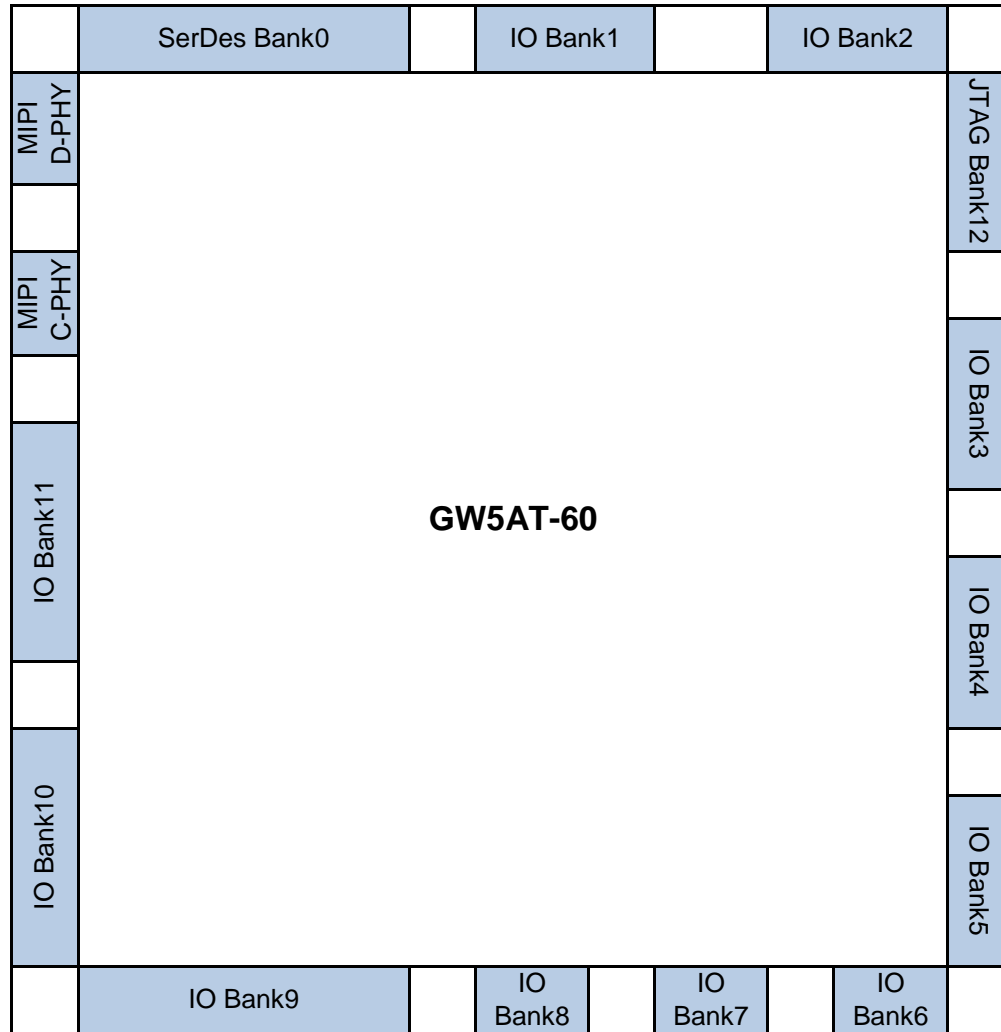
管脚名称	方向	说明
TDI	I	JTAG模式：串行数据输入
RECONFIG_N	I	全局复位GowinCONFIG逻辑信号，低电平有效
DONE ^[1]	O	高电平表示成功完成编程配置 低电平表示未完成编程配置或编程配置失败
	I	DONE信号为低电平时，延迟芯片启动，直到DONE信号为高电平
READY ^[1]	I/O	高电平表示当前可以对器件进行编程配置 低电平表示无法对器件进行编程配置
LPLL_C_FB/RPLL_C_FB	I	左边/右边PLL反馈输入管脚，C(Comp)
LPLL_T_FB/RPLL_T_FB	I	左边/右边PLL反馈输入管脚，T(True)
LPLL_C_IN/RPLL_C_IN	I	左边/右边PLL时钟输入管脚，C(Comp)
LPLL_T_IN/RPLL_T_IN	I	左边/右边PLL时钟输入管脚，T(True)
TPLL_C_FB/BPLL_C_FB	I	上边/下边PLL反馈输入管脚，C(Comp)
TPLL_T_FB/BPLL_T_FB	I	上边/下边PLL反馈输入管脚，T(True)
TPLL_C_IN/BPLL_C_IN	I	上边/下边PLL时钟输入管脚，C(Comp)
TPLL_T_IN/BPLL_T_IN	I	上边/下边PLL时钟输入管脚，T(True)
PCIE_HOST_RSTN	I	PCIE系统复位控制，低电平有效
SERDES_Q0CLKIN0	I	SERDES参考时钟
SERDES_Q0CLKIN1	I	SERDES参考时钟
MODE2	I	GowinCONFIG配置模式选择信号端口：若该管脚标记为“VCCIO”，表示该管脚内接电源；若该管脚标记为“GND”，表示该管脚内部接地
MODE1	I	GowinCONFIG配置模式选择信号端口：若该管脚标记为“VCCIO”，表示该管脚内接电源；若该管脚标记为“GND”，表示该管脚内部接地
MODE0	I	GowinCONFIG配置模式选择信号端口：若该管脚标记为“VCCIO”，表示该管脚内接电源；若该管脚标记为“GND”，表示该管脚内部接地
其他管脚		
ADCTN	DIO	SENSOR差分模拟信号输入管脚
ADCTP	DIO	SENSOR差分模拟信号输入管脚
ADCVN	DIO	SENSOR差分模拟信号输入管脚
ADCVP	DIO	SENSOR差分模拟信号输入管脚
Q0_ATEST_O	DIO	SerDes模拟信号测试管脚
M0_CKN	DIO	MIPI_DPHY的时钟通道差分输入输出管脚
M0_CKP	DIO	MIPI_DPHY的时钟通道差分输入输出管脚
M0_DON	DIO	MIPI_DPHY的数据通道0差分输入输出管脚

管脚名称	方向	说明
M0_D0P	DIO	MIPI_DPHY的数据通道0差分输入输出管脚
M0_D1N	DIO	MIPI_DPHY的数据通道1差分输入输出管脚
M0_D1P	DIO	MIPI_DPHY的数据通道1差分输入输出管脚
M0_D2N	DIO	MIPI_DPHY的数据通道2差分输入输出管脚
M0_D2P	DIO	MIPI_DPHY的数据通道2差分输入输出管脚
M0_D3N	DIO	MIPI_DPHY的数据通道3差分输入输出管脚
M0_D3P	DIO	MIPI_DPHY的数据通道3差分输入输出管脚
M1_D0A	DIO	MIPI_CPHY的数据通道0输入输出A管脚
M1_D0B	DIO	MIPI_CPHY的数据通道0输入输出B管脚
M1_D0C	DIO	MIPI_CPHY的数据通道0输入输出C管脚
M1_D1A	DIO	MIPI_CPHY的数据通道1输入输出A管脚
M1_D1B	DIO	MIPI_CPHY的数据通道1输入输出B管脚
M1_D1C	DIO	MIPI_CPHY的数据通道1输入输出C管脚
M1_D2A	DIO	MIPI_CPHY的数据通道2输入输出A管脚
M1_D2B	DIO	MIPI_CPHY的数据通道2输入输出B管脚
M1_D2C	DIO	MIPI_CPHY的数据通道2输入输出C管脚
Q0_LN0_RXM_I	DIO	SerDes Q0的数据通道0 RX差分输入管脚
Q0_LN0_RXP_I	DIO	SerDes Q0的数据通道0 RX差分输入管脚
Q0_LN0_TXM_O	DIO	SerDes Q0的数据通道0 TX差分输出管脚
Q0_LN0_TXP_O	DIO	SerDes Q0的数据通道0 TX差分输出管脚
Q0_LN1_RXM_I	DIO	SerDes Q0的数据通道1 RX差分输入管脚
Q0_LN1_RXP_I	DIO	SerDes Q0的数据通道1 RX差分输入管脚
Q0_LN1_TXM_O	DIO	SerDes Q0的数据通道1 TX差分输出管脚
Q0_LN1_TXP_O	DIO	SerDes Q0的数据通道1 TX差分输出管脚
Q0_LN2_RXM_I	DIO	SerDes Q0的数据通道2 RX差分输入管脚
Q0_LN2_RXP_I	DIO	SerDes Q0的数据通道2 RX差分输入管脚
Q0_LN2_TXM_O	DIO	SerDes Q0的数据通道2 TX差分输出管脚
Q0_LN2_TXP_O	DIO	SerDes Q0的数据通道2 TX差分输出管脚
Q0_LN3_RXM_I	DIO	SerDes Q0的数据通道3 RX差分输入管脚
Q0_LN3_RXP_I	DIO	SerDes Q0的数据通道3 RX差分输入管脚
Q0_LN3_TXM_O	DIO	SerDes Q0的数据通道3 TX差分输出管脚
Q0_LN3_TXP_O	DIO	SerDes Q0的数据通道3 TX差分输出管脚

管脚名称	方向	说明
Q0_REFCLKM_0	DIO	SerDes Q0的参考时钟通道0差分输入管脚
Q0_REFCLKP_0	DIO	SerDes Q0的参考时钟通道0差分输入管脚
Q0_REFCLKM_1	DIO	SerDes Q0的参考时钟通道1差分输入管脚
Q0_REFCLKP_1	DIO	SerDes Q0的参考时钟通道1差分输入管脚
VSS	NA	Ground管脚
VCC	NA	核电压供电管脚
VCCIO#	NA	I/O BANK#的I/O电压供电管脚
VCCX	NA	辅助电压供电管脚
VCC_LDO	NA	为PLL, SRAM提供电压的内部LDO模块的电源电压管脚
VCC_ADC	NA	ADC模块供电电压管脚
V_EFUSE	NA	eFuse写操作供电电压管脚
VDDA_Q*	NA	SerDes模块QUAD*内部模拟电路供电电压管脚
VDDD_Q*	NA	SerDes模块QUAD*内部数字电路供电电压管脚
VDDT_Q*	NA	SerDes模块QUAD* TX 发送端供电电压管脚
VDDHA_Q*	NA	SerDes模块QUAD*内部高压供电电压管脚
VDD12_MIPI	NA	MIPI模块LP模式供电电压管脚
VDDA_MIPI	NA	MIPI模块内部模拟电路供电电压管脚
VDDD_MIPI	NA	MIPI模块内部数字电路供电电压管脚
VDDX_MIPI	NA	MIPI模块辅助供电电压管脚
VREF*	NA	参考电压管脚

注!

[1] READY和DONE默认状态为open-drain输出，内部弱上拉。在配置期间，DONE输出0。



注!

[1]每个Bank还提供一个独立的参考电压 (VREF)。

[2]用户可选择使用IOB内置的VREF源 (0.6V、0.75V、0.9V、1.25V、1.5V, 以及基于VCCIO的比例电压 (36%, 50%, 64%))。

[3]用户也可选择外部的VREF输入 (使用Bank中任意一个IO管脚作为外部VREF输入)。

名称	描述	最小值	最大值
FPGA Logic			
V _{CC}	核电压, LV	0.87V	1.03V
	核电压, EV	1.14V	1.8V
V _{CCIO}	I/O Bank电压	1V	3.465V
V _{CCX} ^[1]	辅助电压	1.71V	3.465V
V _{EFUSE} ^[2]	eFuse写入所需电压	1.62V	1.98V
Gigabit Transceiver			
V _{DDHA_Q*}	QUAD*内部高压供电电压	1.71V	1.89V
V _{DDA_Q*}	QUAD*内部模拟电路供电电压	0.87V	1.03V
V _{DDT_Q*}	QUAD* TX发送端供电电压	0.87V	1.03V
V _{DDD_Q*}	QUAD*内部数字电路供电电压	0.87V	1.03V
MIPI			
V _{DDA_MIPI}	MIPI模块模拟电路供电电压	0.87V	1.03V
V _{DDX_MIPI}	MIPI模块模拟辅助供电电压	1.71V	3.465V
V _{DDD_MIPI}	MIPI模块数字电路供电电压	0.87V	1.03V
V _{DD12_MIPI}	MIPI模块LP模式供电电压	1.14V	1.32V
ADC			
V _{CC_ADC}	ADC模块供电电压	1.62V	1.98V
VREFN	ADC模块参考电压	0V	0V
VREFP	ADC模块参考电压	0V	1.25V
注!			
<p>[1] 当需要使用内部差分端接电阻时, V_{ccx}必须大于等于3V; V_{ccx}=1.8V时, IO输入输出的F_{max}会受到限制, 对于F_{max}>600Mbps的输入输出应用, V_{ccx}需要大于等于2.5V。</p> <p>[2] 当不需要写eFuse的时候, 这个电源可以接GND或floating。</p> <p>若某些封装或者PCB上多个电源短接, 需要取所有短接电源范围的交集, 同时满足多个电源的需求。</p>			

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
IOB102A/D11	I/O	7	bus4	DQ7	D11	True_of_IOB102B	True	AA20	pull up
IOB102B/D12	I/O	7	bus4	DQ7	D12	Comp_of_IOB102A	True	AA21	pull up
IOB104A/D09	I/O	7	bus4	DQ7	D09	True_of_IOB104B	True	W21	pull up
IOB104B/D10	I/O	7	bus4	DQ7	D10	Comp_of_IOB104A	True	W22	pull up
IOB106A	I/O	7	bus4	DQ7		True_of_IOB106B	True	AA16	pull up
IOB106B	I/O	7	bus4	DQ7		Comp_of_IOB106A	True	AA15	pull up
IOB108A	I/O	7	bus4	DQ7		True_of_IOB108B	True	AB17	pull up
IOB108B	I/O	7	bus4	DQ7		Comp_of_IOB108A	True	AB16	pull up
IOB111A/MCS_N	I/O	7	bus4	DQ6	MCS_N	True_of_IOB111B	True	T19	pull up
IOB111B/D08	I/O	7	bus4	DQ6	D08	Comp_of_IOB111A	True	T20	pull up
IOB113A/D06	I/O	7	bus4	DQ6	D06	True_of_IOB113B	True	P19	pull up
IOB113B/D07	I/O	7	bus4	DQ6	D07	Comp_of_IOB113A	True	R19	pull up
IOB115A/D04	I/O	7	bus4	DQ6	D04	True_of_IOB115B	True	T21	pull up
IOB115B/D05	I/O	7	bus4	DQ6	D05	Comp_of_IOB115A	True	U21	pull up
IOB117A/PUDC_B	I/O	7	bus4	DQS6	PUDC_B	True_of_IOB117B	True	U22	pull down
IOB117B/EMCCLK	I/O	7	bus4	DQS6	EMCCLK	Comp_of_IOB117A	True	V22	pull up
IOB120A/D02/MI2	I/O	7	bus4	DQ6	D02/MI2	True_of_IOB120B	True	P21	pull up
IOB120B/D03/MI3	I/O	7	bus4	DQ6	D03/MI3	Comp_of_IOB120A	True	R21	pull up
IOB122A	I/O	7	bus4	DQ6		True_of_IOB122B	True	Y17	pull up
IOB122B	I/O	7	bus4	DQ6		Comp_of_IOB122A	True	Y16	pull up
IOB124A	I/O	7	bus4	DQ6		True_of_IOB124B	True	P20	pull up
IOB124B	I/O	7	bus4	DQ6		Comp_of_IOB124A	True	V13	pull up
IOB126A	I/O	7	bus4	DQ6		True_of_IOB126B	True	W16	pull up
IOB126B	I/O	7	bus4	DQ6		Comp_of_IOB126A	True	W15	pull up
IOB128A/D00/MOSI/MI0	I/O	6	bus4	DQ6	D00/MOSI/MI0	True_of_IOB128B	True	P22	pull up
IOB128B/D01/DIN/MISO/MI1	I/O	6	bus4	DQ6	D01/DIN/MISO/MI1	Comp_of_IOB128A	True	R22	pull up
IOB12A	I/O	9		DQS12		True_of_IOB12B	True	R3	pull up
IOB12B	I/O	9		DQS12		Comp_of_IOB12A	True	R2	pull up
IOB130A	I/O	6	bus4	none		True_of_IOB130B	True	V15	pull up
IOB130B	I/O	6	bus4	none		Comp_of_IOB130A	True	U15	pull up
IOB132A	I/O	6	bus4	none		True_of_IOB132B	True	T15	pull up
IOB132B	I/O	6	bus4	none		Comp_of_IOB132A	True	T14	pull up
IOB134A	I/O	6	bus4	none		True_of_IOB134B	True	U16	pull up
IOB134B	I/O	6	bus4	none		Comp_of_IOB134A	True	N15	pull up
IOB136A	I/O	6	bus4	none		True_of_IOB136B	True	P16	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
IOB136B	I/O	6	bus4	none		Comp_of_IOB136A	True	R17	pull up
IOB138A	I/O	6	bus4	none		True_of_IOB138B	True	N13	pull up
IOB138B	I/O	6	bus4	none		Comp_of_IOB138A	True	N14	pull up
IOB140A	I/O	6	bus4	none		True_of_IOB140B	True	N17	pull up
IOB140B	I/O	6	bus4	none		Comp_of_IOB140A	True	P17	pull up
IOB142A	I/O	6	bus4	none		True_of_IOB142B	True	P15	pull up
IOB142B	I/O	6	bus4	none		Comp_of_IOB142A	True	R16	pull up
IOB144A	I/O	6		none		True_of_IOB144B	True	R18	pull up
IOB144B	I/O	6		none		Comp_of_IOB144A	True	T18	pull up
IOB146A	I/O	6		none		True_of_IOB146B	True	P14	pull up
IOB146B	I/O	6		none		Comp_of_IOB146A	True	R14	pull up
IOB14A	I/O	9		DQ12		True_of_IOB14B	True	W1	pull up
IOB14B	I/O	9		DQ12		Comp_of_IOB14A	True	Y1	pull up
IOB16A	I/O	9	bus4	DQ12		True_of_IOB16B	True	U3	pull up
IOB16B	I/O	9	bus4	DQ12		Comp_of_IOB16A	True	V3	pull up
IOB18A	I/O	9	bus4	DQ11		True_of_IOB18B	True	AB12	pull up
IOB18B	I/O	9	bus4	DQ11		Comp_of_IOB18A	True	AB11	pull up
IOB1A	I/O	9	bus4	DQ12		True_of_IOB1B	True	AA9	pull up
IOB1B	I/O	9	bus4	DQ12		Comp_of_IOB1A	True	AA10	pull up
IOB20A	I/O	9	bus4	DQ11		True_of_IOB20B	True	AA11	pull up
IOB20B	I/O	9	bus4	DQ11		Comp_of_IOB20A	True	Y11	pull up
IOB22A	I/O	9	bus4	DQ11		True_of_IOB22B	True	AA1	pull up
IOB22B	I/O	9	bus4	DQ11		Comp_of_IOB22A	True	AB1	pull up
IOB24A	I/O	9	bus4	DQ11		True_of_IOB24B	True	AB3	pull up
IOB24B	I/O	9	bus4	DQ11		Comp_of_IOB24A	True	AB2	pull up
IOB26A	I/O	9	bus4	DQS11		True_of_IOB26B	True	Y3	pull up
IOB26B	I/O	9	bus4	DQS11		Comp_of_IOB26A	True	AA3	pull up
IOB29A	I/O	9	bus4	DQ11		True_of_IOB29B	True	AA5	pull up
IOB29B	I/O	9	bus4	DQ11		Comp_of_IOB29A	True	AB5	pull up
IOB31A/GCLKT_14	I/O	9	bus4	DQ11	GCLKT_14	True_of_IOB31B	True	Y4	pull up
IOB31B/GCLKC_14	I/O	9	bus4	DQ11	GCLKC_14	Comp_of_IOB31A	True	AA4	pull up
IOB33A/GCLKT_15	I/O	9	bus4	DQ11	GCLKT_15	True_of_IOB33B	True	V4	pull up
IOB33B/GCLKC_15	I/O	9	bus4	DQ11	GCLKC_15	Comp_of_IOB33A	True	W4	pull up
IOB35A	I/O	9	bus4	DQ10		True_of_IOB35B	True	W11	pull up
IOB35B	I/O	9	bus4	DQ11		Comp_of_IOB35A	True	W10	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
I0B38A/GCLKT_13	I/O	9	bus4	DQ10	GCLKT_13	True_of_I0B38B	True	R4	pull up
I0B38B/GCLKC_13	I/O	9	bus4	DQ10	GCLKC_13	Comp_of_I0B38A	True	T4	pull up
I0B3A/BPLL0_T_IN1/BPLL0_T_FB0	I/O	9	bus4	DQ12	BPLL0_T_IN1/BPLL0_T_FB0	True_of_I0B3B	True	T3	pull up
I0B3B/BPLL0_C_IN1/BPLL0_C_FB0	I/O	9	bus4	DQ12	BPLL0_C_IN1/BPLL0_C_FB0	Comp_of_I0B3A	True	AB10	pull up
I0B40A/GCLKT_12	I/O	9	bus4	DQ10	GCLKT_12	True_of_I0B40B	True	T5	pull up
I0B40B/GCLKC_12	I/O	9	bus4	DQ10	GCLKC_12	Comp_of_I0B40A	True	U5	pull up
I0B42A	I/O	9	bus4	DQ10		True_of_I0B42B	True	U6	pull up
I0B42B	I/O	9	bus4	DQ10		Comp_of_I0B42A	True	V5	pull up
I0B44A	I/O	9	bus4	DQS10		True_of_I0B44B	True	W6	pull up
I0B44B	I/O	9	bus4	DQS10		Comp_of_I0B44A	True	W5	pull up
I0B48A	I/O	9	bus4	DQ10		True_of_I0B48B	True	R6	pull up
I0B48B	I/O	9	bus4	DQ10		Comp_of_I0B48A	True	T6	pull up
I0B50A	I/O	9	bus4	DQ10/DQ S_910		True_of_I0B50B	True	Y6	pull up
I0B50B	I/O	9	bus4	DQ10/DQ S_910		Comp_of_I0B50A	True	AA6	pull up
I0B52A	I/O	9	bus4	DQ10		True_of_I0B52B	True	Y12	pull up
I0B52B	I/O	9	bus4	DQ10		Comp_of_I0B52A	True	W12	pull up
I0B54A	I/O	9	bus4	DQ10		True_of_I0B54B	True	AB13	pull up
I0B54B	I/O	9	bus4	DQ10		Comp_of_I0B54A	True	AA13	pull up
I0B56A	I/O	9	bus4	DQ9		True_of_I0B56B	True	Y13	pull up
I0B56B	I/O	9	bus4	DQ10		Comp_of_I0B56A	True	Y14	pull up
I0B58A	I/O	9	bus4	DQ9		True_of_I0B58B	True	V7	pull up
I0B58B	I/O	9	bus4	DQ9		Comp_of_I0B58A	True	W7	pull up
I0B5A/BPLL0_T_IN2/BPLL0_T_FB1	I/O	9	bus4	DQ12	BPLL0_T_IN2/BPLL0_T_FB1	True_of_I0B5B	True	T1	pull up
I0B5B/BPLL0_C_IN2/BPLL0_C_FB1	I/O	9	bus4	DQ12	BPLL0_C_IN2/BPLL0_C_FB1	Comp_of_I0B5A	True	U1	pull up
I0B60A	I/O	9	bus4	DQ9		True_of_I0B60B	True	AB7	pull up
I0B60B	I/O	9	bus4	DQ9		Comp_of_I0B60A	True	AB6	pull up
I0B62A	I/O	9	bus4	DQS9		True_of_I0B62B	True	V9	pull up
I0B62B	I/O	9	bus4	DQS9		Comp_of_I0B62A	True	V8	pull up
I0B65A	I/O	9	bus4	DQ9		True_of_I0B65B	True	AA8	pull up
I0B65B	I/O	9	bus4	DQ9		Comp_of_I0B65A	True	AB8	pull up
I0B67A/BPLL1_T_IN0	I/O	9	bus4	DQ9	BPLL1_T_IN0	True_of_I0B67B	True	Y8	pull up
I0B67B/BPLL1_C_IN0	I/O	9	bus4	DQ9	BPLL1_C_IN0	Comp_of_I0B67A	True	Y7	pull up
I0B69A	I/O	9	bus4	DQ9		True_of_I0B69B	True	W9	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
I0B69B	I/O	9	bus4	DQ9		Comp_of_I0B69A	True	Y9	pull up
I0B71A/BPLL1_T_IN1/BPLL1_T_FB1	I/O	9	bus4	DQ9	BPLL1_T_IN1/BPLL1_T_FB1	True_of_I0B71B	True	U7	pull up
I0B71B/BPLL1_C_IN1/BPLL1_C_FB1	I/O	9	bus4	DQ9	BPLL1_C_IN1/BPLL1_C_FB1	Comp_of_I0B71A	True	V10	pull up
I0B75A	I/O	8	bus4	DQ8		True_of_I0B75B	True	U17	pull up
I0B75B	I/O	8	bus4	DQ8		Comp_of_I0B75A	True	U18	pull up
I0B77A/BPLL1_T_IN2/BPLL1_T_FB0	I/O	8	bus4	DQ8	BPLL1_T_IN2/BPLL1_T_FB0	True_of_I0B77B	True	AA18	pull up
I0B77B/BPLL1_C_IN2/BPLL1_C_FB0	I/O	8	bus4	DQ8	BPLL1_C_IN2/BPLL1_C_FB0	Comp_of_I0B77A	True	AB18	pull up
I0B79A/CSI_B	I/O	8	bus4	DQ8	CSI_B	True_of_I0B79B	True	V17	pull up
I0B79B	I/O	8	bus4	DQ8		Comp_of_I0B79A	True	W17	pull up
I0B7A	I/O	9	bus4	DQ12		True_of_I0B7B	True	U2	pull up
I0B7B	I/O	9	bus4	DQ12		Comp_of_I0B7A	True	V2	pull up
I0B81A/GCLKT_11	I/O	8		DQ8	GCLKT_11	True_of_I0B81B	True	V18	pull up
I0B81B/GCLKC_11	I/O	8		DQ8	GCLKC_11	Comp_of_I0B81A	True	V19	pull up
I0B85A/RDWR_B	I/O	8		DQS8	RDWR_B	True_of_I0B85B	True	AA19	pull up
I0B85B/CSO_B/DOUT	I/O	8		DQS8	CSO_B/DOUT	Comp_of_I0B85A	True	AB20	pull up
I0B87A/GCLKT_10	I/O	8	bus4	DQ8	GCLKT_10	True_of_I0B87B	True	Y18	pull up
I0B87B/GCLKC_10	I/O	8	bus4	DQ8	GCLKC_10	Comp_of_I0B87A	True	Y19	pull up
I0B89A	I/O	8	bus4	DQ8		True_of_I0B89B	True	AB15	pull up
I0B89B	I/O	8	bus4	DQ8		Comp_of_I0B89A	True	AA14	pull up
I0B91A	I/O	8	bus4	DQ8		True_of_I0B91B	True	W14	pull up
I0B91B	I/O	8	bus4	DQ8		Comp_of_I0B91A	True	V14	pull up
I0B93A/GCLKT_9	I/O	8	bus4	DQ7	GCLKT_9	True_of_I0B93B	True	W19	pull up
I0B93B/GCLKC_9	I/O	8	bus4	DQ7	GCLKC_9	Comp_of_I0B93A	True	W20	pull up
I0B95A/GCLKT_8	I/O	8	bus4	DQ7	GCLKT_8	True_of_I0B95B	True	U20	pull up
I0B95B/GCLKC_8	I/O	8	bus4	DQ7	GCLKC_8	Comp_of_I0B95A	True	V20	pull up
I0B97A/D14	I/O	8	bus4	DQ7	D14	True_of_I0B97B	True	AB21	pull up
I0B97B/D15	I/O	8	bus4	DQ7	D15	Comp_of_I0B97A	True	AB22	pull up
I0B99A	I/O	8	bus4	DQS7		True_of_I0B99B	True	Y21	pull up
I0B99B/D13	I/O	8	bus4	DQS7	D13	Comp_of_I0B99A	True	Y22	pull up
I0B9A	I/O	9	bus4	DQ12		True_of_I0B9B	True	W2	pull up
I0B9B	I/O	9	bus4	DQ12		Comp_of_I0B9A	True	Y2	pull up
I0L21B	I/O	11	bus2	none		Comp_of_I0L21A	none	F4	pull up
I0L23A	I/O	11		none		True_of_I0L23B	True	B1	pull up
I0L23B	I/O	11		none		Comp_of_I0L23A	True	A1	pull up
I0L25A	I/O	11		none		True_of_I0L25B	True	C2	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
IOL25B	I/O	11		none		Comp_of_IOL25A	True	B2	pull up
IOL27A	I/O	11	bus2	none		True_of_IOL27B	True	E1	pull up
IOL27B	I/O	11	bus2	none		Comp_of_IOL27A	True	D1	pull up
IOL29A	I/O	11	bus2	none		True_of_IOL29B	True	E2	pull up
IOL29B	I/O	11	bus2	none		Comp_of_IOL29A	True	D2	pull up
IOL31A	I/O	11	bus2	none		True_of_IOL31B	True	G1	pull up
IOL31B	I/O	11	bus2	none		Comp_of_IOL31A	True	F1	pull up
IOL33A	I/O	11	bus2	none		True_of_IOL33B	True	F3	pull up
IOL33B	I/O	11	bus2	none		Comp_of_IOL33A	True	E3	pull up
IOL35A/LPLL0_T_IN0/LPLL0_T_FB0	I/O	11	bus2	none	LPLL0_T_IN0/LPLL0_T_FB0	True_of_IOL35B	True	K1	pull up
IOL35B/LPLL0_C_IN0/LPLL0_C_FB0	I/O	11	bus2	none	LPLL0_C_IN0/LPLL0_C_FB0	Comp_of_IOL35A	True	J1	pull up
IOL37A	I/O	11		none		True_of_IOL37B	True	H2	pull up
IOL37B	I/O	11		none		Comp_of_IOL37A	True	G2	pull up
IOL39A	I/O	11	bus2	none		True_of_IOL39B	True	K2	pull up
IOL39B	I/O	11	bus2	none		Comp_of_IOL39A	True	J2	pull up
IOL41A	I/O	11	bus2	none		True_of_IOL41B	True	J5	pull up
IOL41B	I/O	11	bus2	none		Comp_of_IOL41A	True	H5	pull up
IOL43A/GCLKT_19/LPLL0_T_IN1/LPL L0_T_FB1	I/O	11	bus2	none	GCLKT_19/LPLL0_T_IN1/LPL L0_T_FB1	True_of_IOL43B	True	H3	pull up
IOL43B/GCLKC_19/LPLL0_C_IN1/LPL L0_C_FB1	I/O	11	bus2	none	GCLKC_19/LPLL0_C_IN1/LPL L0_C_FB1	Comp_of_IOL43A	True	G3	pull up
IOL45A/GCLKT_18	I/O	11	bus2	none	GCLKT_18	True_of_IOL45B	True	H4	pull up
IOL45B/GCLKC_18	I/O	11	bus2	none	GCLKC_18	Comp_of_IOL45A	True	G4	pull up
IOL48A/GCLKT_17	I/O	10	bus2	none	GCLKT_17	True_of_IOL48B	True	K4	pull up
IOL48B/GCLKC_17	I/O	10	bus2	none	GCLKC_17	Comp_of_IOL48A	True	J4	pull up
IOL50A/GCLKT_16	I/O	10	bus2	none	GCLKT_16	True_of_IOL50B	True	L3	pull up
IOL50B/GCLKC_16	I/O	10	bus2	none	GCLKC_16	Comp_of_IOL50A	True	K3	pull up
IOL52A	I/O	10	bus2	none		True_of_IOL52B	True	M1	pull up
IOL52B	I/O	10	bus2	none		Comp_of_IOL52A	True	L1	pull up
IOL54A	I/O	10	bus2	none		True_of_IOL54B	True	M3	pull up
IOL54B	I/O	10	bus2	none		Comp_of_IOL54A	True	M2	pull up
IOL57A	I/O	10	bus2	none		True_of_IOL57B	True	K6	pull up
IOL57B	I/O	10	bus2	none		Comp_of_IOL57A	True	J6	pull up
IOL59A	I/O	10	bus2	none		True_of_IOL59B	True	L5	pull up
IOL59B	I/O	10	bus2	none		Comp_of_IOL59A	True	L4	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
IOL61A	I/O	10	bus2	none		True_of_IOL61B	True	N4	pull up
IOL61B	I/O	10	bus2	none		Comp_of_IOL61A	True	N3	pull up
IOL63A/LPLL1_T_IN0	I/O	10		none	LPLL1_T_IN0	True_of_IOL63B	True	R1	pull up
IOL63B/LPLL1_C_IN0	I/O	10		none	LPLL1_C_IN0	Comp_of_IOL63A	True	P1	pull up
IOL65A	I/O	10	bus2	none		True_of_IOL65B	none	L6	pull up
IOL66A/LPLL1_T_IN1/LPLL1_T_FB0	I/O	10	bus2	none	LPLL1_T_IN1/LPLL1_T_FB0	True_of_IOL66B	True	P5	pull up
IOL66B/LPLL1_C_IN1/LPLL1_C_FB0	I/O	10	bus2	none	LPLL1_C_IN1/LPLL1_C_FB0	Comp_of_IOL66A	True	P4	pull up
IOL68A/LPLL1_T_IN2/LPLL1_T_FB0	I/O	10	bus2	none	LPLL1_T_IN2/LPLL1_T_FB0	True_of_IOL68B	True	P2	pull up
IOL68B/LPLL1_C_IN2/LPLL1_C_FB0	I/O	10	bus2	none	LPLL1_C_IN2/LPLL1_C_FB0	Comp_of_IOL68A	True	N2	pull up
IOL70A/BPLL0_T_IN0	I/O	10	bus2	none	BPLL0_T_IN0	True_of_IOL70B	True	M6	pull up
IOL70B/BPLL0_C_IN0	I/O	10	bus2	none	BPLL0_C_IN0	Comp_of_IOL70A	True	M5	pull up
IOL72A	I/O	10		none		True_of_IOL72B	True	P6	pull up
IOL72B	I/O	10		none		Comp_of_IOL72A	True	N5	pull up
IOR11A/TPLL1_T_IN0/TPLL1_T_FB1	I/O	3	bus3	none	TPLL1_T_IN0/TPLL1_T_FB1	True_of_IOR11B	True	U8	pull up
IOR11B/MODE2/TPLL1_C_IN0/TPLL1_C_FB1	I/O	3	bus3	none	MODE2/TPLL1_C_IN0/TPLL1_C_FB1	Comp_of_IOR11A	True	U9	pull up
IOR1A/TCK	I/O	12		none	TCK	True_of_IOR1B	none	V12	pull up
IOR1B/TDI	I/O	12		none	TDI	Comp_of_IOR1A	none	R13	pull up
IOR20A	I/O	4	bus4	DQ4		True_of_IOR20B	True	H13	pull up
IOR20B	I/O	4	bus4	DQ4		Comp_of_IOR20A	True	G13	pull up
IOR22A	I/O	4	bus4	DQ4		True_of_IOR22B	True	J15	pull up
IOR22B	I/O	4	bus4	DQ4		Comp_of_IOR22A	True	H15	pull up
IOR24A	I/O	4	bus4	DQ4		True_of_IOR24B	True	G15	pull up
IOR24B	I/O	4	bus4	DQ4		Comp_of_IOR24A	True	G16	pull up
IOR26A	I/O	4	bus4	DQS4		True_of_IOR26B	True	J14	pull up
IOR26B	I/O	4	bus4	DQS4		Comp_of_IOR26A	True	H14	pull up
IOR30A	I/O	4	bus4	DQ4		True_of_IOR30B	True	G17	pull up
IOR30B	I/O	4	bus4	DQ4		Comp_of_IOR30A	True	G18	pull up
IOR32A	I/O	4		DQ4		True_of_IOR32B	True	H17	pull up
IOR32B	I/O	4		DQ4		Comp_of_IOR32A	True	H18	pull up
IOR34A	I/O	4		DQ4		True_of_IOR34B	True	L16	pull up
IOR34B	I/O	4		DQ4		Comp_of_IOR34A	True	K16	pull up
IOR36A/RPLL0_T_IN1/RPLL0_T_FB0	I/O	4	bus4	DQ4	RPLL0_T_IN1/RPLL0_T_FB0	True_of_IOR36B	True	K13	pull up
IOR36B/RPLL0_C_IN1/RPLL0_C_FB0	I/O	4	bus4	DQ4	RPLL0_C_IN1/RPLL0_C_FB0	Comp_of_IOR36A	True	K14	pull up
IOR39A/RPLL0_T_IN0/RPLL0_T_FB1	I/O	5	bus4	none	RPLL0_T_IN0/RPLL0_T_FB1	True_of_IOR39B	True	K17	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
IOR39B/RPLL0_C_IN0/RPLL0_C_FB1	I/O	5	bus4	none	RPLL0_C_IN0/RPLL0_C_FB1	Comp_of_IOR39A	True	J17	pull up
IOR3A/TMS	I/O	12		none	TMS	True_of_IOR3B	none	T13	pull up
IOR3B/TDO	I/O	12		none	TDO	Comp_of_IOR3A	none	U13	pull up
IOR41A/GCLKT_7	I/O	5	bus4	none	GCLKT_7	True_of_IOR41B	True	J19	pull up
IOR41B/GCLKC_7	I/O	5	bus4	none	GCLKC_7	Comp_of_IOR41A	True	H19	pull up
IOR43A	I/O	5		none		True_of_IOR43B	True	H20	pull up
IOR43B	I/O	5		none		Comp_of_IOR43A	True	G20	pull up
IOR45A	I/O	5	bus4	none		True_of_IOR45B	True	J22	pull up
IOR45B	I/O	5	bus4	none		Comp_of_IOR45A	True	H22	pull up
IOR47A	I/O	5	bus4	none		True_of_IOR47B	none	J16	pull up
IOR48A	I/O	5	bus4	none		True_of_IOR48B	True	L14	pull up
IOR48B	I/O	5	bus4	none		Comp_of_IOR48A	True	L15	pull up
IOR50A	I/O	5	bus4	none		True_of_IOR50B	True	M13	pull up
IOR50B	I/O	5	bus4	none		Comp_of_IOR50A	True	L13	pull up
IOR52A	I/O	5	bus4	none		True_of_IOR52B	True	M15	pull up
IOR52B	I/O	5	bus4	none		Comp_of_IOR52A	True	M16	pull up
IOR54A/GCLKT_5	I/O	5	bus4	none	GCLKT_5	True_of_IOR54B	True	J20	pull up
IOR54B/GCLKC_5	I/O	5	bus4	none	GCLKC_5	Comp_of_IOR54A	True	J21	pull up
IOR56A	I/O	5	bus4	none		True_of_IOR56B	none	M17	pull up
IOR57A/GCLKT_6	I/O	5	bus4	DQ5	GCLKT_6	True_of_IOR57B	True	K18	pull up
IOR57B/GCLKC_6	I/O	5	bus4	DQ5	GCLKC_6	Comp_of_IOR57A	True	K19	pull up
IOR59A	I/O	5	bus4	DQ5		True_of_IOR59B	True	K21	pull up
IOR59B	I/O	5	bus4	DQ5		Comp_of_IOR59A	True	K22	pull up
IOR5A/CCLK	I/O	3		none	CCLK	True_of_IOR5B	True	L12	pull up
IOR5B/READY	I/O	3		none	READY	Comp_of_IOR5A	True	U12	pull down
IOR61A/SDA/GCLKT_4/RPLL1_T_IN2/RPLL1_T_FB0	I/O	5	bus4	DQ5	SDA/GCLKT_4/RPLL1_T_IN2/RPLL1_T_FB0	True_of_IOR61B	True	L19	pull up
IOR61B/SCL/GCLKC_4/RPLL1_C_IN2/RPLL1_C_FB0	I/O	5	bus4	DQ5	SCL/GCLKC_4/RPLL1_C_IN2/RPLL1_C_FB0	Comp_of_IOR61A	True	L20	pull up
IOR63A/SI/SSIO	I/O	5		DQS5	SI/SSIO	True_of_IOR63B	True	N22	pull up
IOR63B/SSPI_CS_N	I/O	5		DQS5	SSPI_CS_N	Comp_of_IOR63A	True	M22	pull up
IOR66A/SO/SSI1/RPLL1_T_IN1/RPLL1_T_FB1	I/O	5	bus4	DQ5	SO/SSI1/RPLL1_T_IN1/RPLL1_T_FB1	True_of_IOR66B	True	M18	pull up
IOR66B/SSPI_CLK/RPLL1_C_IN1/RPLL1_C_FB1	I/O	5	bus4	DQ5	SSPI_CLK/RPLL1_C_IN1/RPLL1_C_FB1	Comp_of_IOR66A	True	L18	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
IOR68A	I/O	5	bus4	DQ5		True_of_IOR68B	True	M21	pull up
IOR68B	I/O	5	bus4	DQ5		Comp_of_IOR68A	True	L21	pull up
IOR70A/SSPI_WPN/SSI2	I/O	5		DQ5	SSPI_WPN/SSI2	True_of_IOR70B	True	N20	pull up
IOR70B/CLKHOLD_N/SSI3	I/O	5		DQ5	CLKHOLD_N/SSI3	Comp_of_IOR70A	True	M20	pull up
IOR72A/RPLL1_T_IN0	I/O	5		DQ5	RPLL1_T_IN0	True_of_IOR72B	True	N18	pull up
IOR72B/RPLL1_C_IN0	I/O	5		DQ5	RPLL1_C_IN0	Comp_of_IOR72A	True	N19	pull up
IOR7A/MODE1	I/O	3		none	MODE1	True_of_IOR7B	True	U10	pull up
IOR7B/MODE0	I/O	3		none	MODE0	Comp_of_IOR7A	True	U11	pull down
IOR9A/DONE	I/O	3		none	DONE	True_of_IOR9B	True	G11	pull down
IOR9B/RECONFIG_N	I/O	3		none	RECONFIG_N	Comp_of_IOR9A	True	N12	pull up
IOT104A/GCLKT_1	I/O	1	bus2	DQ1	GCLKT_1	True_of_IOT104B	True	B17	pull up
IOT104B/GCLKC_1	I/O	1	bus2	DQ1	GCLKC_1	Comp_of_IOT104A	True	B18	pull up
IOT106A/GCLKT_0	I/O	1	bus2	DQ1	GCLKT_0	True_of_IOT106B	True	D17	pull up
IOT106B/GCLKC_0	I/O	1	bus2	DQ1	GCLKC_0	Comp_of_IOT106A	True	C17	pull up
IOT113A/Q0REF_T_IN1	I/O	2	bus1	DQ2	Q0REF_T_IN1	True_of_IOT113B	True	A18	pull up
IOT113B/Q0REF_C_IN1	I/O	2	bus1	DQ2	Q0REF_C_IN1	Comp_of_IOT113A	True	A19	pull up
IOT115A/GCLKT_3	I/O	2	bus1	DQ2	GCLKT_3	True_of_IOT115B	True	C18	pull up
IOT115B/GCLKC_3	I/O	2	bus1	DQ2	GCLKC_3	Comp_of_IOT115A	True	C19	pull up
IOT117A	I/O	2	bus1	DQS2		True_of_IOT117B	True	F18	pull up
IOT117B	I/O	2	bus1	DQS2		Comp_of_IOT117A	True	E18	pull up
IOT120A/GCLKT_2	I/O	2	bus1	DQ2	GCLKT_2	True_of_IOT120B	True	E19	pull up
IOT120B/GCLKC_2	I/O	2	bus1	DQ2	GCLKC_2	Comp_of_IOT120A	True	D19	pull up
IOT122A	I/O	2	bus1	DQ2		True_of_IOT122B	True	B20	pull up
IOT122B	I/O	2	bus1	DQ2		Comp_of_IOT122A	True	A20	pull up
IOT124A	I/O	2	bus1	DQ2		True_of_IOT124B	True	F19	pull up
IOT124B	I/O	2	bus1	DQ2		Comp_of_IOT124A	True	F20	pull up
IOT131A	I/O	2	bus0	DQ3		True_of_IOT131B	True	D20	pull up
IOT131B	I/O	2	bus0	DQ3		Comp_of_IOT131A	True	C20	pull up
IOT133A	I/O	2	bus0	DQ3		True_of_IOT133B	True	C22	pull up
IOT133B	I/O	2	bus0	DQ3		Comp_of_IOT133A	True	B22	pull up
IOT135A	I/O	2	bus0	DQS3		True_of_IOT135B	True	B21	pull up
IOT135B	I/O	2	bus0	DQS3		Comp_of_IOT135A	True	A21	pull up
IOT138A	I/O	2	bus0	DQ3		True_of_IOT138B	True	E21	pull up
IOT138B	I/O	2	bus0	DQ3		Comp_of_IOT138A	True	D21	pull up
IOT140A	I/O	2	bus0	DQ3		True_of_IOT140B	True	E22	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
IOT140B	I/O	2	bus0	DQ3		Comp_of_IOT140A	True	D22	pull up
IOT142A	I/O	2	bus0	DQ3		True_of_IOT142B	True	F21	pull up
IOT144A	I/O	2	bus0	DQ3		True_of_IOT144B	True	G21	pull up
IOT144B	I/O	2	bus0	DQ3		Comp_of_IOT144A	True	G22	pull up
IOT146A/TPLL1_T_IN1/TPLL1_T_FB0	I/O	2	bus0	DQ3	TPLL1_T_IN1/TPLL1_T_FB0	True_of_IOT146B	True	T16	pull up
IOT78A	I/O	1	bus2	DQ0		True_of_IOT78B	True	E13	pull up
IOT78B	I/O	1	bus2	DQ0		Comp_of_IOT78A	True	E14	pull up
IOT80A/TPLL0_T_IN1/TPLL0_T_FB1	I/O	1	bus2	DQ0	TPLL0_T_IN1/TPLL0_T_FB1	True_of_IOT80B	True	F13	pull up
IOT80B/TPLL0_C_IN1/TPLL0_C_FB1	I/O	1	bus2	DQ0	TPLL0_C_IN1/TPLL0_C_FB1	Comp_of_IOT80A	True	F14	pull up
IOT82A/PCIE_HOST_RSTN/TPLL0_T_IN2/TPLL0_T_FB0	I/O	1	bus2	DQ0	PCIE_HOST_RSTN/TPLL0_T_IN2/TPLL0_T_FB0	True_of_IOT82B	True	D14	pull up
IOT82B/TPLL0_C_IN2/TPLL0_C_FB0	I/O	1	bus2	DQ0	TPLL0_C_IN2/TPLL0_C_FB0	Comp_of_IOT82A	True	D15	pull up
IOT85A	I/O	1		DQS0		True_of_IOT85B	True	C14	pull up
IOT85B	I/O	1		DQS0		Comp_of_IOT85A	True	C15	pull up
IOT87A	I/O	1	bus2	DQ0		True_of_IOT87B	True	E16	pull up
IOT87B	I/O	1	bus2	DQ0		Comp_of_IOT87A	True	D16	pull up
IOT89A/Q0REF_T_IN0	I/O	1	bus2	DQ0	Q0REF_T_IN0	True_of_IOT89B	True	F16	pull up
IOT89B/Q0REF_C_IN0	I/O	1	bus2	DQ0	Q0REF_C_IN0	Comp_of_IOT89A	True	E17	pull up
IOT91A	I/O	1	bus2	DQ1		True_of_IOT91B	True	F15	pull up
IOT93A	I/O	1	bus2	DQ1		True_of_IOT93B	True	C13	pull up
IOT93B	I/O	1	bus2	DQ1		Comp_of_IOT93A	True	B13	pull up
IOT95A	I/O	1	bus2	DQ1		True_of_IOT95B	True	A13	pull up
IOT95B	I/O	1	bus2	DQ1		Comp_of_IOT95A	True	A14	pull up
IOT97A	I/O	1	bus2	DQ1		True_of_IOT97B	True	B15	pull up
IOT97B	I/O	1	bus2	DQ1		Comp_of_IOT97A	True	B16	pull up
IOT99A	I/O	1	bus2	DQS1		True_of_IOT99B	True	A15	pull up
IOT99B	I/O	1	bus2	DQS1		Comp_of_IOT99A	True	A16	pull up
ADCTN	DIO	ADC		none			none	N9	pull up
ADCTP	DIO	ADC		none			none	N10	pull up
ADCVN	DIO	ADC		none			none	M9	pull up
ADCVP	DIO	ADC		none			none	L10	pull up
Q0_ATEST_O	DIO	Q0		none			none	F8	pull up
Q0_LN0_RXM_I	DIO	Q0		none			none	A8	pull up
Q0_LN0_RXP_I	DIO	Q0		none			none	B8	pull up
Q0_LN0_TXM_O	DIO	Q0		none			none	A4	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
Q0_LN0_TXP_O	DIO	Q0		none			none	B4	pull up
Q0_LN1_RXM_I	DIO	Q0		none			none	C11	pull up
Q0_LN1_RXP_I	DIO	Q0		none			none	D11	pull up
Q0_LN1_TXM_O	DIO	Q0		none			none	C5	pull up
Q0_LN1_TXP_O	DIO	Q0		none			none	D5	pull up
Q0_LN2_RXM_I	DIO	Q0		none			none	A10	pull up
Q0_LN2_RXP_I	DIO	Q0		none			none	B10	pull up
Q0_LN2_TXM_O	DIO	Q0		none			none	A6	pull up
Q0_LN2_TXP_O	DIO	Q0		none			none	B6	pull up
Q0_LN3_RXM_I	DIO	Q0		none			none	C9	pull up
Q0_LN3_RXP_I	DIO	Q0		none			none	D9	pull up
Q0_LN3_TXM_O	DIO	Q0		none			none	C7	pull up
Q0_LN3_TXP_O	DIO	Q0		none			none	D7	pull up
Q0_REFCLKM_0	DIO	Q0		none			none	E6	pull up
Q0_REFCLKM_1	DIO	Q0		none			none	E10	pull up
Q0_REFCLKP_0	DIO	Q0		none			none	F6	pull up
Q0_REFCLKP_1	DIO	Q0		none			none	F10	pull up
VDDA_Q0	Power	N/A						F9	pull up
VDDA_Q0	Power	N/A						E8	pull up
VDDA_Q0	Power	N/A						F7	pull up
VDDHA_Q0	Power	N/A						K12	pull up
VDDT_Q0	Power	N/A						D10	pull up
VDDT_Q0	Power	N/A						D6	pull up
VCC	Power	N/A						J9	pull up
VCC	Power	N/A						P10	pull up
VCC	Power	N/A						P8	pull up
VCC	Power	N/A						N7	pull up
VCC	Power	N/A						H10	pull up
VCC	Power	N/A						M8	pull up
VCC	Power	N/A						R9	pull up
VCC	Power	N/A						T8	pull up
VCC	Power	N/A						R7	pull up
VCC	Power	N/A						T10	pull up
VCC	Power	N/A						K8	pull up
VCC	Power	N/A						H8	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
VCC	Power	N/A						J7	pull up
VCC	Power	N/A						L7	pull up
VCCIO1	Power	N/A						B14	pull up
VCCIO1	Power	N/A						A17	pull up
VCCIO1	Power	N/A						C21	pull up
VCCIO10	Power	N/A						C1	pull up
VCCIO10	Power	N/A						H6	pull up
VCCIO10	Power	N/A						F2	pull up
VCCIO11	Power	N/A						M4	pull up
VCCIO11	Power	N/A						J3	pull up
VCCIO11	Power	N/A						N1	pull up
VCCIO12/VCCIO3	Power	N/A						F12	pull up
VCCIO12/VCCIO3	Power	N/A						T12	pull up
VCCIO2	Power	N/A						D18	pull up
VCCIO2	Power	N/A						E15	pull up
VCCIO2	Power	N/A						F22	pull up
VCCIO4	Power	N/A						H16	pull up
VCCIO4	Power	N/A						J13	pull up
VCCIO4	Power	N/A						G19	pull up
VCCIO5	Power	N/A						L17	pull up
VCCIO5	Power	N/A						N21	pull up
VCCIO5	Power	N/A						K20	pull up
VCCIO6	Power	N/A						M14	pull up
VCCIO6	Power	N/A						P18	pull up
VCCIO7	Power	N/A						T22	pull up
VCCIO7	Power	N/A						R15	pull up
VCCIO8	Power	N/A						Y20	pull up
VCCIO8	Power	N/A						U19	pull up
VCCIO9	Power	N/A						V6	pull up
VCCIO9	Power	N/A						AB4	pull up
VCCIO9	Power	N/A						T2	pull up
VCCIO9	Power	N/A						AA7	pull up
VCCIO9	Power	N/A						W3	pull up
VCCIO9	Power	N/A						R5	pull up
VCCX	Power	N/A						P12	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
VCCX	Power	N/A						R11	pull up
VCCX	Power	N/A						H12	pull up
VCCX	Power	N/A						M12	pull up
VCC_ADC	Power	N/A						K10	pull up
V_EFUSE	Power	N/A						T17	pull up
VREFN	Power	N/A						L9	pull up
VREFP	Power	N/A						M10	pull up
VSS	Ground	N/A						D8	pull up
VSS	Ground	N/A						A2	pull up
VSS	Ground	N/A						A3	pull up
VSS	Ground	N/A						A5	pull up
VSS	Ground	N/A						A7	pull up
VSS	Ground	N/A						A9	pull up
VSS	Ground	N/A						A11	pull up
VSS	Ground	N/A						A12	pull up
VSS	Ground	N/A						A22	pull up
VSS	Ground	N/A						AA2	pull up
VSS	Ground	N/A						AA12	pull up
VSS	Ground	N/A						AA22	pull up
VSS	Ground	N/A						AB9	pull up
VSS	Ground	N/A						AB19	pull up
VSS	Ground	N/A						B3	pull up
VSS	Ground	N/A						B12	pull up
VSS	Ground	N/A						B19	pull up
VSS	Ground	N/A						C3	pull up
VSS	Ground	N/A						C6	pull up
VSS	Ground	N/A						C10	pull up
VSS	Ground	N/A						C12	pull up
VSS	Ground	N/A						C16	pull up
VSS	Ground	N/A						D3	pull up
VSS	Ground	N/A						D4	pull up
VSS	Ground	N/A						D12	pull up
VSS	Ground	N/A						D13	pull up
VSS	Ground	N/A						E4	pull up
VSS	Ground	N/A						E5	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
VSS	Ground	N/A						E7	pull up
VSS	Ground	N/A						E9	pull up
VSS	Ground	N/A						E11	pull up
VSS	Ground	N/A						E20	pull up
VSS	Ground	N/A						F5	pull up
VSS	Ground	N/A						F11	pull up
VSS	Ground	N/A						F17	pull up
VSS	Ground	N/A						G5	pull up
VSS	Ground	N/A						G6	pull up
VSS	Ground	N/A						G7	pull up
VSS	Ground	N/A						G8	pull up
VSS	Ground	N/A						G9	pull up
VSS	Ground	N/A						G10	pull up
VSS	Ground	N/A						G12	pull up
VSS	Ground	N/A						G14	pull up
VSS	Ground	N/A						H1	pull up
VSS	Ground	N/A						H7	pull up
VSS	Ground	N/A						H9	pull up
VSS	Ground	N/A						H11	pull up
VSS	Ground	N/A						H21	pull up
VSS	Ground	N/A						J8	pull up
VSS	Ground	N/A						J10	pull up
VSS	Ground	N/A						J12	pull up
VSS	Ground	N/A						J18	pull up
VSS	Ground	N/A						K5	pull up
VSS	Ground	N/A						K7	pull up
VSS	Ground	N/A						K11	pull up
VSS	Ground	N/A						K15	pull up
VSS	Ground	N/A						L2	pull up
VSS	Ground	N/A						L8	pull up
VSS	Ground	N/A						L22	pull up
VSS	Ground	N/A						M7	pull up
VSS	Ground	N/A						M11	pull up
VSS	Ground	N/A						M19	pull up
VSS	Ground	N/A						N6	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
VSS	Ground	N/A						N8	pull up
VSS	Ground	N/A						N16	pull up
VSS	Ground	N/A						P3	pull up
VSS	Ground	N/A						P7	pull up
VSS	Ground	N/A						P9	pull up
VSS	Ground	N/A						P11	pull up
VSS	Ground	N/A						P13	pull up
VSS	Ground	N/A						R8	pull up
VSS	Ground	N/A						R10	pull up
VSS	Ground	N/A						R12	pull up
VSS	Ground	N/A						R20	pull up
VSS	Ground	N/A						T7	pull up
VSS	Ground	N/A						T9	pull up
VSS	Ground	N/A						T11	pull up
VSS	Ground	N/A						U4	pull up
VSS	Ground	N/A						U14	pull up
VSS	Ground	N/A						V1	pull up
VSS	Ground	N/A						V11	pull up
VSS	Ground	N/A						V21	pull up
VSS	Ground	N/A						W8	pull up
VSS	Ground	N/A						W18	pull up
VSS	Ground	N/A						Y5	pull up
VSS	Ground	N/A						Y15	pull up
VSS	Ground	N/A						K9	pull up
NC	N/A	N/A						AA17	pull up
NC	N/A	N/A						AB14	pull up
NC	N/A	N/A						V16	pull up
NC	N/A	N/A						W13	pull up
NC	N/A	N/A						Y10	pull up
NC	N/A	N/A						E12	pull up
NC	N/A	N/A						J11	pull up
NC	N/A	N/A						L11	pull up
NC	N/A	N/A						N11	pull up
NC	N/A	N/A						B5	pull up
NC	N/A	N/A						B7	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
NC	N/A	N/A						B9	pull up
NC	N/A	N/A						B11	pull up
NC	N/A	N/A						C4	pull up
NC	N/A	N/A						C8	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225	配置过程中的IO上下拉状态
IOB12A	I/O	9		DQS12		True_of_IOB12B	True	R3	pull down
IOB12B	I/O	9		DQS12		Comp_of_IOB12A	True	P3	pull down
IOB14A	I/O	9		DQ12		True_of_IOB14B	True	N4	pull down
IOB14B	I/O	9		DQ12		Comp_of_IOB14A	True	M4	pull down
IOB16A	I/O	9	bus4	DQ12		True_of_IOB16B	True	P5	pull down
IOB16B	I/O	9	bus4	DQ12		Comp_of_IOB16A	True	N5	pull down
IOB18A/GCLKT_12	I/O	9	bus4	DQ11	GCLKT_12	True_of_IOB18B	True	M5	pull down
IOB18B/GCLKC_12	I/O	9	bus4	DQ11	GCLKC_12	Comp_of_IOB18A	True	L5	pull down
IOB1A	I/O	9	bus4	DQ12		True_of_IOB1B	True	L1	pull down
IOB1B	I/O	9	bus4	DQ12		Comp_of_IOB1A	True	L2	pull down
IOB20A	I/O	9	bus4	DQ11		True_of_IOB20B	True	R4	pull down
IOB20B	I/O	9	bus4	DQ11		Comp_of_IOB20A	True	R5	pull down
IOB22A	I/O	9	bus4	DQ11		True_of_IOB22B	True	R6	pull down
IOB22B	I/O	9	bus4	DQ11		Comp_of_IOB22A	True	R7	pull down
IOB24A	I/O	9	bus4	DQ11		True_of_IOB24B	True	N6	pull down
IOB24B	I/O	9	bus4	DQ11		Comp_of_IOB24A	True	M6	pull down
IOB26A	I/O	9	bus4	DQS11		True_of_IOB26B	True	R8	pull down
IOB26B	I/O	9	bus4	DQS11		Comp_of_IOB26A	True	R9	pull down
IOB29A	I/O	9	bus4	DQ11		True_of_IOB29B	True	P7	pull down
IOB29B	I/O	9	bus4	DQ11		Comp_of_IOB29A	True	N7	pull down
IOB31A/GCLKT_14	I/O	9	bus4	DQ11	GCLKT_14	True_of_IOB31B	True	N8	pull down
IOB31B/GCLKC_14	I/O	9	bus4	DQ11	GCLKC_14	Comp_of_IOB31A	True	M8	pull down
IOB33A/GCLKT_15	I/O	9	bus4	DQ11	GCLKT_15	True_of_IOB33B	True	P9	pull down
IOB33B/GCLKC_15	I/O	9	bus4	DQ11	GCLKC_15	Comp_of_IOB33A	True	N9	pull down
IOB35A	I/O	9	bus4	DQ10		True_of_IOB35B	True	L6	pull down
IOB35B	I/O	9	bus4	DQ11		Comp_of_IOB35A	True	L7	pull down
IOB38A/GCLKT_13	I/O	9	bus4	DQ10	GCLKT_13	True_of_IOB38B	True	M9	pull down
IOB38B/GCLKC_13	I/O	9	bus4	DQ10	GCLKC_13	Comp_of_IOB38A	True	L8	pull down
IOB3A/BPLL0_T_IN1/BPLL0_T_FB0	I/O	9	bus4	DQ12	BPLL0_T_IN1/BPLL0_T_FB0	True_of_IOB3B	True	M3	pull down
IOB3B/BPLL0_C_IN1/BPLL0_C_FB0	I/O	9	bus4	DQ12	BPLL0_C_IN1/BPLL0_C_FB0	Comp_of_IOB3A	True	L3	pull down
IOB5A/BPLL0_T_IN2/BPLL0_T_FB1	I/O	9	bus4	DQ12	BPLL0_T_IN2/BPLL0_T_FB1	True_of_IOB5B	True	M1	pull down
IOB5B/BPLL0_C_IN2/BPLL0_C_FB1	I/O	9	bus4	DQ12	BPLL0_C_IN2/BPLL0_C_FB1	Comp_of_IOB5A	True	N2	pull down
IOB75A	I/O	8	bus4	DQ8		True_of_IOB75B	True	L9	pull down
IOB75B	I/O	8	bus4	DQ8		Comp_of_IOB75A	True	K8	pull down
IOB77A/BPLL1_T_IN2/BPLL1_T_FB0	I/O	8	bus4	DQ8	BPLL1_T_IN2/BPLL1_T_FB0	True_of_IOB77B	True	R10	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225	配置过程中的IO上下拉状态
I0B77B/BPLL1_C_IN2/BPLL1_C_FB0	I/O	8	bus4	DQ8	BPLL1_C_IN2/BPLL1_C_FB0	Comp_of_I0B77A	True	R11	pull down
I0B79A	I/O	8	bus4	DQ8		True_of_I0B79B	True	P11	pull down
I0B79B	I/O	8	bus4	DQ8		Comp_of_I0B79A	True	N11	pull down
I0B7A	I/O	9	bus4	DQ12		True_of_I0B7B	True	N1	pull down
I0B7B	I/O	9	bus4	DQ12		Comp_of_I0B7A	True	P1	pull down
I0B81A/GCLKT_11	I/O	8		DQ8	GCLKT_11	True_of_I0B81B	True	N10	pull down
I0B81B/GCLKC_11	I/O	8		DQ8	GCLKC_11	Comp_of_I0B81A	True	M10	pull down
I0B85A	I/O	8		DQS8		True_of_I0B85B	True	R13	pull down
I0B85B	I/O	8		DQS8		Comp_of_I0B85A	True	R12	pull down
I0B87A/GCLKT_10	I/O	8	bus4	DQ8	GCLKT_10	True_of_I0B87B	True	N12	pull down
I0B87B/GCLKC_10	I/O	8	bus4	DQ8	GCLKC_10	Comp_of_I0B87A	True	M11	pull down
I0B89A	I/O	8	bus4	DQ8		True_of_I0B89B	True	L10	pull down
I0B89B	I/O	8	bus4	DQ8		Comp_of_I0B89A	True	K10	pull down
I0B91A	I/O	8	bus4	DQ8		True_of_I0B91B	True	R14	pull down
I0B91B	I/O	8	bus4	DQ8		Comp_of_I0B91A	True	P13	pull down
I0B9A	I/O	9	bus4	DQ12		True_of_I0B9B	True	R2	pull down
I0B9B	I/O	9	bus4	DQ12		Comp_of_I0B9A	True	P2	pull down
I0L41A	I/O	11	bus2	none		True_of_I0L41B	True	F5	pull down
I0L41B	I/O	11	bus2	none		Comp_of_I0L41A	True	G5	pull down
I0L45A/GCLKT_18	I/O	11	bus2	none	GCLKT_18	True_of_I0L45B	True	K4	pull down
I0L45B/GCLKC_18	I/O	11	bus2	none	GCLKC_18	Comp_of_I0L45A	True	K3	pull down
I0L48A/GCLKT_17	I/O	10	bus2	none	GCLKT_17	True_of_I0L48B	True	H5	pull down
I0L48B/GCLKC_17	I/O	10	bus2	none	GCLKC_17	Comp_of_I0L48A	True	H6	pull down
I0L50A/GCLKT_16	I/O	10	bus2	none	GCLKT_16	True_of_I0L50B	True	K5	pull down
I0L50B/GCLKC_16	I/O	10	bus2	none	GCLKC_16	Comp_of_I0L50A	True	J5	pull down
I0L66A/PUDC_B/LPLL1_T_IN1/LPLL1_T_FB0	I/O	10	bus2	none	PUDC_B/LPLL1_T_IN1/LPLL1_T_FB0	True_of_I0L66B	True	K1	pull down
I0R15A/D11	I/O	3	bus3	none	D11	True_of_I0R15B	True	F11	pull down
I0R15B/D12	I/O	3	bus3	none	D12	Comp_of_I0R15A	True	G11	pull down
I0R17A/MODE1/GCLKT_4	I/O	3	bus3	none	MODE1/GCLKT_4	True_of_I0R17B	True	F15	pull up
I0R17B/D10/GCLKC_4	I/O	3	bus3	none	D10/GCLKC_4	Comp_of_I0R17A	True	G15	pull down
I0R1A/TCK	I/O	12		none	TCK	True_of_I0R1B	none	C15	pull up
I0R1B/TDI	I/O	12		none	TDI	Comp_of_I0R1A	none	B15	pull up
I0R20A/D01/MI2	I/O	4	bus4	DQ4	D01/MI2	True_of_I0R20B	True	G13	pull down
I0R20B/D02/MI3	I/O	4	bus4	DQ4	D02/MI3	Comp_of_I0R20A	True	G14	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225	配置过程中的IO上下拉状态
IOR34A/D13	I/O	4		DQ4	D13	True_of_IOR34B	True	H15	pull down
IOR34B/EMCCLK	I/O	4		DQ4	EMCCLK	Comp_of_IOR34A	True	J15	pull down
IOR36A/GCLKT_5/RPLL0_T_IN1/RPLL0_T_FB0	I/O	4	bus4	DQ4	GCLKT_5/RPLL0_T_IN1/RPLL0_T_FB0	True_of_IOR36B	True	G12	pull down
IOR36B/GCLKC_5/RPLL0_C_IN1/RPLL0_C_FB0	I/O	4	bus4	DQ4	GCLKC_5/RPLL0_C_IN1/RPLL0_C_FB0	Comp_of_IOR36A	True	H12	pull down
IOR3A/TMS	I/O	12		none	TMS	True_of_IOR3B	none	E15	pull up
IOR3B/TDO	I/O	12		none	TDO	Comp_of_IOR3A	none	D15	pull up
IOR41A/D14/GCLKT_7	I/O	5	bus4	none	D14/GCLKT_7	True_of_IOR41B	True	H11	pull down
IOR41B/D15/GCLKC_7	I/O	5	bus4	none	D15/GCLKC_7	Comp_of_IOR41A	True	H10	pull down
IOR47A/DONE	I/O	5	bus4	none	DONE	True_of_IOR47B	none	H13	pull down
IOR56A/RECONFIG_N	I/O	5	bus4	none	RECONFIG_N	True_of_IOR56B	none	K15	pull up
IOR57A/GCLKT_6	I/O	5	bus4	DQ5	GCLKT_6	True_of_IOR57B	True	J13	pull down
IOR57B/GCLKC_6	I/O	5	bus4	DQ5	GCLKC_6	Comp_of_IOR57A	True	J14	pull down
IOR59A	I/O	5	bus4	DQ5		True_of_IOR59B	True	J11	pull down
IOR59B	I/O	5	bus4	DQ5		Comp_of_IOR59A	True	K11	pull down
IOR5A/CCLK	I/O	3		none	CCLK	True_of_IOR5B	True	E13	pull down
IOR5B/MODE0	I/O	3		none	MODE0	Comp_of_IOR5A	True	E14	pull down
IOR61A/D07/SDA/RPLL1_T_IN2/RPLL1_T_FB0	I/O	5	bus4	DQ5	D07/SDA/RPLL1_T_IN2/RPLL1_T_FB0	True_of_IOR61B	True	K12	pull down
IOR61B/RDWR_B/SCL/RPLL1_C_IN2/RPLL1_C_FB0	I/O	5	bus4	DQ5	RDWR_B/SCL/RPLL1_C_IN2/RPLL1_C_FB0	Comp_of_IOR61A	True	K13	pull down
IOR63A/D03/SI/SSI0	I/O	5		DQS5	D03/SI/SSI0	True_of_IOR63B	True	L13	pull down
IOR63B/D04/SSPI_CS_N	I/O	5		DQS5	D04/SSPI_CS_N	Comp_of_IOR63A	True	L14	pull down
IOR66A/D05/SO/SSI1/RPLL1_T_IN1/RPLL1_T_FB1	I/O	5	bus4	DQ5	D05/SO/SSI1/RPLL1_T_IN1/RPLL1_T_FB1	True_of_IOR66B	True	L15	pull down
IOR66B/D06/SSPI_CLK/RPLL1_C_IN1/RPLL1_C_FB1	I/O	5	bus4	DQ5	D06/SSPI_CLK/RPLL1_C_IN1/RPLL1_C_FB1	Comp_of_IOR66A	True	M15	pull down
IOR68A	I/O	5	bus4	DQ5		True_of_IOR68B	True	L12	pull down
IOR68B	I/O	5	bus4	DQ5		Comp_of_IOR68A	True	M13	pull down
IOR70A/D08/SSPI_WPN/SSI2	I/O	5		DQ5	D08/SSPI_WPN/SSI2	True_of_IOR70B	True	N14	pull down
IOR70B/D09/CLKHOLD_N/SSI3	I/O	5		DQ5	D09/CLKHOLD_N/SSI3	Comp_of_IOR70A	True	N15	pull down
IOR72A/READY/RPLL1_T_IN0	I/O	5		DQ5	READY/RPLL1_T_IN0	True_of_IOR72B	True	P14	pull down
IOR72B/CSO_B/MCS_N/RPLL1_C_IN0	I/O	5		DQ5	CSO_B/MCS_N/RPLL1_C_IN0	Comp_of_IOR72A	True	P15	pull down
IOR9A/D00/DIN/MISO/MI1	I/O	3		none	D00/DIN/MISO/MI1	True_of_IOR9B	True	F12	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225	配置过程中的IO上下拉状态
IOR9B/CSI_B/MOSI/MIO	I/O	3		none	CSI_B/MOSI/MIO	Comp_of_IOR9A	True	F13	pull down
IOT142A	I/O	2	bus0	DQ3		True_of_IOT142B	True	B13	pull down
IOT142B/DOOUT	I/O	2	bus0	DQ3	DOOUT	Comp_of_IOT142A	True	A13	pull up
IOT144A	I/O	2	bus0	DQ3		True_of_IOT144B	True	B14	pull down
IOT144B	I/O	2	bus0	DQ3		Comp_of_IOT144A	True	A14	pull down
IOT146A/TPLL1_T_IN1/TPLL1_T_FB0	I/O	2	bus0	DQ3	TPLL1_T_IN1/TPLL1_T_FB0	True_of_IOT146B	True	D13	pull down
IOT146B/TPLL1_C_IN1/TPLL1_C_FB0	I/O	2	bus0	DQ3	TPLL1_C_IN1/TPLL1_C_FB0	Comp_of_IOT146A	True	C14	pull down
IOT74A	I/O	1	bus2	DQ0		True_of_IOT74B	True	E5	pull down
IOT74B	I/O	1	bus2	DQ0		Comp_of_IOT74A	True	E6	pull down
IOT76A/TPLL0_T_IN0	I/O	1	bus2	DQ0	TPLL0_T_IN0	True_of_IOT76B	True	E7	pull down
IOT76B/TPLL0_C_IN0	I/O	1	bus2	DQ0	TPLL0_C_IN0	Comp_of_IOT76A	True	E8	pull down
IOT78A	I/O	1	bus2	DQ0		True_of_IOT78B	True	F8	pull down
IOT78B	I/O	1	bus2	DQ0		Comp_of_IOT78A	True	E9	pull down
IOT89A/GCLKT_1/Q0REF_T_IN0	I/O	1	bus2	DQ0	GCLKT_1/Q0REF_T_IN0	True_of_IOT89B	True	F10	pull down
IOT89B/GCLKC_1/Q0REF_C_IN0	I/O	1	bus2	DQ0	GCLKC_1/Q0REF_C_IN0	Comp_of_IOT89A	True	E10	pull down
M0_CKN	DIO	MIPI		none			none	F3	pull down
M0_CKP	DIO	MIPI		none			none	G3	pull down
M0_D0N	DIO	MIPI		none			none	J3	pull down
M0_D0P	DIO	MIPI		none			none	J4	pull down
M0_D1N	DIO	MIPI		none			none	H3	pull down
M0_D1P	DIO	MIPI		none			none	H4	pull down
M0_D2N	DIO	MIPI		none			none	E3	pull down
M0_D2P	DIO	MIPI		none			none	F4	pull down
M0_D3N	DIO	MIPI		none			none	C2	pull down
M0_D3P	DIO	MIPI		none			none	C1	pull down
M1_D0A	DIO	MIPI		none			none	D1	pull down
M1_D0B	DIO	MIPI		none			none	E2	pull down
M1_D0C	DIO	MIPI		none			none	E1	pull down
M1_D1A	DIO	MIPI		none			none	F1	pull down
M1_D1B	DIO	MIPI		none			none	G1	pull down
M1_D1C	DIO	MIPI		none			none	G2	pull down
M1_D2A	DIO	MIPI		none			none	H1	pull down
M1_D2B	DIO	MIPI		none			none	J1	pull down
M1_D2C	DIO	MIPI		none			none	J2	pull down
Q0_LN0_RXM_I	DIO	Q0		none			none	A3	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225	配置过程中的IO上下拉状态
Q0_LN0_RXP_I	DIO	Q0		none			none	B3	pull down
Q0_LN0_TXM_O	DIO	Q0		none			none	C4	pull down
Q0_LN0_TXP_O	DIO	Q0		none			none	D4	pull down
Q0_LN1_RXM_I	DIO	Q0		none			none	A7	pull down
Q0_LN1_RXP_I	DIO	Q0		none			none	B7	pull down
Q0_LN1_TXM_O	DIO	Q0		none			none	C6	pull down
Q0_LN1_TXP_O	DIO	Q0		none			none	D6	pull down
Q0_LN2_RXM_I	DIO	Q0		none			none	A9	pull down
Q0_LN2_RXP_I	DIO	Q0		none			none	B9	pull down
Q0_LN2_TXM_O	DIO	Q0		none			none	C8	pull down
Q0_LN2_TXP_O	DIO	Q0		none			none	D8	pull down
Q0_LN3_RXM_I	DIO	Q0		none			none	A11	pull down
Q0_LN3_RXP_I	DIO	Q0		none			none	B11	pull down
Q0_LN3_TXM_O	DIO	Q0		none			none	C12	pull down
Q0_LN3_TXP_O	DIO	Q0		none			none	D12	pull down
Q0_REFCLKM_0	DIO	Q0		none			none	A5	pull down
Q0_REFCLKM_1	DIO	Q0		none			none	C10	pull down
Q0_REFCLKP_0	DIO	Q0		none			none	B5	pull down
Q0_REFCLKP_1	DIO	Q0		none			none	D10	pull down
VDD12_MIP1	Power	N/A						G6	pull down
VDDA_MIP1/VDDD_MIP1	Power	N/A						D2	pull down
VDDA_MIP1/VDDD_MIP1	Power	N/A						H2	pull down
VDDA_MIP1/VDDD_MIP1	Power	N/A						G4	pull down
VDDX_MIP1/VCCIO6/VCCIO7/VCCX	Power	N/A						M12	pull down
VDDX_MIP1/VCCIO6/VCCIO7/VCCX	Power	N/A						J6	pull down
VDDX_MIP1/VCCIO6/VCCIO7/VCCX	Power	N/A						B1	pull down
VDDX_MIP1/VCCIO6/VCCIO7/VCCX	Power	N/A						F7	pull down
VDDX_MIP1/VCCIO6/VCCIO7/VCCX	Power	N/A						K9	pull down
VDDX_MIP1/VCCIO6/VCCIO7/VCCX	Power	N/A						G10	pull down
VDDA_Q0	Power	N/A						D5	pull down
VDDA_Q0	Power	N/A						D11	pull down
VDDA_Q0	Power	N/A						B8	pull down
VDDHA_Q0	Power	N/A						D7	pull down
VDDHA_Q0	Power	N/A						D9	pull down
VDDT_Q0	Power	N/A						A10	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225	配置过程中的IO上下拉状态
VDDT_Q0	Power	N/A						A6	pull down
VDDT_Q0	Power	N/A						B4	pull down
VCC	Power	N/A						F9	pull down
VCC	Power	N/A						H9	pull down
VCC	Power	N/A						G8	pull down
VCC	Power	N/A						J8	pull down
VCC	Power	N/A						H7	pull down
VCC	Power	N/A						K7	pull down
VCC	Power	N/A						J10	pull down
VCCIO1/VCCIO2	Power	N/A						B12	pull down
VCCIO10/VCCIO11	Power	N/A						M2	pull down
VCCIO10/VCCIO11	Power	N/A						L4	pull down
VCCIO12/VCCIO3/VCCIO4	Power	N/A						D14	pull down
VCCIO12/VCCIO3/VCCIO4	Power	N/A						H14	pull down
VCCIO5	Power	N/A						M14	pull down
VCCIO5	Power	N/A						J12	pull down
VCCIO8	Power	N/A						P12	pull down
VCCIO8	Power	N/A						P8	pull down
VCCIO9	Power	N/A						M7	pull down
VCCIO9	Power	N/A						P4	pull down
VCC_ADC	Power	N/A						E12	pull down
V_EFUSE	Power	N/A						L11	pull down
VSS	Ground	N/A						A1	pull down
VSS	Ground	N/A						A15	pull down
VSS	Ground	N/A						B10	pull down
VSS	Ground	N/A						B6	pull down
VSS	Ground	N/A						C13	pull down
VSS	Ground	N/A						C3	pull down
VSS	Ground	N/A						E11	pull down
VSS	Ground	N/A						F14	pull down
VSS	Ground	N/A						F2	pull down
VSS	Ground	N/A						F6	pull down
VSS	Ground	N/A						G7	pull down
VSS	Ground	N/A						G9	pull down
VSS	Ground	N/A						H8	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225	配置过程中的IO上下拉状态
VSS	Ground	N/A						J7	pull down
VSS	Ground	N/A						J9	pull down
VSS	Ground	N/A						K14	pull down
VSS	Ground	N/A						K2	pull down
VSS	Ground	N/A						K6	pull down
VSS	Ground	N/A						N13	pull down
VSS	Ground	N/A						N3	pull down
VSS	Ground	N/A						P10	pull down
VSS	Ground	N/A						P6	pull down
VSS	Ground	N/A						R1	pull down
VSS	Ground	N/A						R15	pull down
VSS	Ground	N/A						A2	pull down
VSS	Ground	N/A						B2	pull down
VSS	Ground	N/A						A4	pull down
VSS	Ground	N/A						D3	pull down
VSS	Ground	N/A						E4	pull down
VSS	Ground	N/A						A8	pull down
VSS	Ground	N/A						C9	pull down
VSS	Ground	N/A						C11	pull down
VSS	Ground	N/A						A12	pull down
VSS	Ground	N/A						C7	pull down
VSS	Ground	N/A						C5	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225H	配置过程中的IO上下拉状态
IOB12A	I/O	9		DQS12		True_of_IOB12B	True	R3	pull down
IOB12B	I/O	9		DQS12		Comp_of_IOB12A	True	P3	pull down
IOB14A	I/O	9		DQ12		True_of_IOB14B	True	N4	pull down
IOB14B	I/O	9		DQ12		Comp_of_IOB14A	True	M4	pull down
IOB16A	I/O	9	bus4	DQ12		True_of_IOB16B	True	P5	pull down
IOB16B	I/O	9	bus4	DQ12		Comp_of_IOB16A	True	N5	pull down
IOB18A/GCLKT_12	I/O	9	bus4	DQ11	GCLKT_12	True_of_IOB18B	True	M5	pull down
IOB18B/GCLKC_12	I/O	9	bus4	DQ11	GCLKC_12	Comp_of_IOB18A	True	L5	pull down
IOB1A	I/O	9	bus4	DQ12		True_of_IOB1B	True	L1	pull down
IOB1B	I/O	9	bus4	DQ12		Comp_of_IOB1A	True	L2	pull down
IOB20A	I/O	9	bus4	DQ11		True_of_IOB20B	True	R4	pull down
IOB20B	I/O	9	bus4	DQ11		Comp_of_IOB20A	True	R5	pull down
IOB22A	I/O	9	bus4	DQ11		True_of_IOB22B	True	R6	pull down
IOB22B	I/O	9	bus4	DQ11		Comp_of_IOB22A	True	R7	pull down
IOB24A	I/O	9	bus4	DQ11		True_of_IOB24B	True	N6	pull down
IOB24B	I/O	9	bus4	DQ11		Comp_of_IOB24A	True	M6	pull down
IOB26A	I/O	9	bus4	DQS11		True_of_IOB26B	True	R8	pull down
IOB26B	I/O	9	bus4	DQS11		Comp_of_IOB26A	True	R9	pull down
IOB29A	I/O	9	bus4	DQ11		True_of_IOB29B	True	P7	pull down
IOB29B	I/O	9	bus4	DQ11		Comp_of_IOB29A	True	N7	pull down
IOB31A/GCLKT_14	I/O	9	bus4	DQ11	GCLKT_14	True_of_IOB31B	True	N8	pull down
IOB31B/GCLKC_14	I/O	9	bus4	DQ11	GCLKC_14	Comp_of_IOB31A	True	M8	pull down
IOB33A/GCLKT_15	I/O	9	bus4	DQ11	GCLKT_15	True_of_IOB33B	True	P9	pull down
IOB33B/GCLKC_15	I/O	9	bus4	DQ11	GCLKC_15	Comp_of_IOB33A	True	N9	pull down
IOB35A	I/O	9	bus4	DQ10		True_of_IOB35B	True	L6	pull down
IOB35B	I/O	9	bus4	DQ11		Comp_of_IOB35A	True	L7	pull down
IOB38A/GCLKT_13	I/O	9	bus4	DQ10	GCLKT_13	True_of_IOB38B	True	M9	pull down
IOB38B/GCLKC_13	I/O	9	bus4	DQ10	GCLKC_13	Comp_of_IOB38A	True	L8	pull down
IOB3A/BPLL0_T_IN1/BPLL0_T_FB0	I/O	9	bus4	DQ12	BPLL0_T_IN1/BPLL0_T_FB0	True_of_IOB3B	True	M3	pull down
IOB3B/BPLL0_C_IN1/BPLL0_C_FB0	I/O	9	bus4	DQ12	BPLL0_C_IN1/BPLL0_C_FB0	Comp_of_IOB3A	True	L3	pull down
IOB5A/BPLL0_T_IN2/BPLL0_T_FB1	I/O	9	bus4	DQ12	BPLL0_T_IN2/BPLL0_T_FB1	True_of_IOB5B	True	M1	pull down
IOB5B/BPLL0_C_IN2/BPLL0_C_FB1	I/O	9	bus4	DQ12	BPLL0_C_IN2/BPLL0_C_FB1	Comp_of_IOB5A	True	N2	pull down
IOB75A	I/O	8	bus4	DQ8		True_of_IOB75B	True	L9	pull down
IOB75B	I/O	8	bus4	DQ8		Comp_of_IOB75A	True	K8	pull down
IOB77A/BPLL1_T_IN2/BPLL1_T_FB0	I/O	8	bus4	DQ8	BPLL1_T_IN2/BPLL1_T_FB0	True_of_IOB77B	True	R10	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225H	配置过程中的IO上下拉状态
I0B77B/BPLL1_C_IN2/BPLL1_C_FB0	I/O	8	bus4	DQ8	BPLL1_C_IN2/BPLL1_C_FB0	Comp_of_I0B77A	True	R11	pull down
I0B79A	I/O	8	bus4	DQ8		True_of_I0B79B	True	P11	pull down
I0B79B	I/O	8	bus4	DQ8		Comp_of_I0B79A	True	N11	pull down
I0B7A	I/O	9	bus4	DQ12		True_of_I0B7B	True	N1	pull down
I0B7B	I/O	9	bus4	DQ12		Comp_of_I0B7A	True	P1	pull down
I0B81A/GCLKT_11	I/O	8		DQ8	GCLKT_11	True_of_I0B81B	True	N10	pull down
I0B81B/GCLKC_11	I/O	8		DQ8	GCLKC_11	Comp_of_I0B81A	True	M10	pull down
I0B85A	I/O	8		DQS8		True_of_I0B85B	True	R13	pull down
I0B85B	I/O	8		DQS8		Comp_of_I0B85A	True	R12	pull down
I0B87A/GCLKT_10	I/O	8	bus4	DQ8	GCLKT_10	True_of_I0B87B	True	N12	pull down
I0B87B/GCLKC_10	I/O	8	bus4	DQ8	GCLKC_10	Comp_of_I0B87A	True	M11	pull down
I0B89A	I/O	8	bus4	DQ8		True_of_I0B89B	True	L10	pull down
I0B89B	I/O	8	bus4	DQ8		Comp_of_I0B89A	True	K10	pull down
I0B91A	I/O	8	bus4	DQ8		True_of_I0B91B	True	R14	pull down
I0B91B	I/O	8	bus4	DQ8		Comp_of_I0B91A	True	P13	pull down
I0B9A	I/O	9	bus4	DQ12		True_of_I0B9B	True	R2	pull down
I0B9B	I/O	9	bus4	DQ12		Comp_of_I0B9A	True	P2	pull down
I0L41A	I/O	11	bus2	none		True_of_I0L41B	True	F5	pull down
I0L41B	I/O	11	bus2	none		Comp_of_I0L41A	True	G5	pull down
I0L45A/GCLKT_18	I/O	11	bus2	none	GCLKT_18	True_of_I0L45B	True	K4	pull down
I0L45B/GCLKC_18	I/O	11	bus2	none	GCLKC_18	Comp_of_I0L45A	True	K3	pull down
I0L48A/GCLKT_17	I/O	10	bus2	none	GCLKT_17	True_of_I0L48B	True	H5	pull down
I0L48B/GCLKC_17	I/O	10	bus2	none	GCLKC_17	Comp_of_I0L48A	True	H6	pull down
I0L50A/GCLKT_16	I/O	10	bus2	none	GCLKT_16	True_of_I0L50B	True	K5	pull down
I0L50B/GCLKC_16	I/O	10	bus2	none	GCLKC_16	Comp_of_I0L50A	True	J5	pull down
I0L66A/PUDC_B/LPLL1_T_IN1/LPLL1_T_FB0	I/O	10	bus2	none	PUDC_B/LPLL1_T_IN1/LPLL1_T_FB0	True_of_I0L66B	True	K1	pull down
I0R15A/D11	I/O	3	bus3	none	D11	True_of_I0R15B	True	F11	pull down
I0R15B/D12	I/O	3	bus3	none	D12	Comp_of_I0R15A	True	G11	pull down
I0R17A/MODE1/GCLKT_4	I/O	3	bus3	none	MODE1/GCLKT_4	True_of_I0R17B	True	F15	pull up
I0R17B/D10/GCLKC_4	I/O	3	bus3	none	D10/GCLKC_4	Comp_of_I0R17A	True	G15	pull down
I0R1A/TCK	I/O	12		none	TCK	True_of_I0R1B	none	C15	pull up
I0R1B/TDI	I/O	12		none	TDI	Comp_of_I0R1A	none	B15	pull up
I0R20A/D01/MI2	I/O	4	bus4	DQ4	D01/MI2	True_of_I0R20B	True	G13	pull down
I0R20B/D02/MI3	I/O	4	bus4	DQ4	D02/MI3	Comp_of_I0R20A	True	G14	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225H	配置过程中的IO上下拉状态
IOR34A/D13	I/O	4		DQ4	D13	True_of_IOR34B	True	H15	pull down
IOR34B/EMCCLK	I/O	4		DQ4	EMCCLK	Comp_of_IOR34A	True	J15	pull down
IOR36A/GCLKT_5/RPLL0_T_IN1/RPLL0_T_FB0	I/O	4	bus4	DQ4	GCLKT_5/RPLL0_T_IN1/RPLL0_T_FB0	True_of_IOR36B	True	G12	pull down
IOR36B/GCLKC_5/RPLL0_C_IN1/RPLL0_C_FB0	I/O	4	bus4	DQ4	GCLKC_5/RPLL0_C_IN1/RPLL0_C_FB0	Comp_of_IOR36A	True	H12	pull down
IOR3A/TMS	I/O	12		none	TMS	True_of_IOR3B	none	E15	pull up
IOR3B/TDO	I/O	12		none	TDO	Comp_of_IOR3A	none	D15	pull up
IOR41A/D14/GCLKT_7	I/O	5	bus4	none	D14/GCLKT_7	True_of_IOR41B	True	H11	pull down
IOR41B/D15/GCLKC_7	I/O	5	bus4	none	D15/GCLKC_7	Comp_of_IOR41A	True	H10	pull down
IOR47A/DONE	I/O	5	bus4	none	DONE	True_of_IOR47B	none	H13	pull down
IOR56A/RECONFIG_N	I/O	5	bus4	none	RECONFIG_N	True_of_IOR56B	none	K15	pull up
IOR57A/GCLKT_6	I/O	5	bus4	DQ5	GCLKT_6	True_of_IOR57B	True	J13	pull down
IOR57B/GCLKC_6	I/O	5	bus4	DQ5	GCLKC_6	Comp_of_IOR57A	True	J14	pull down
IOR59A	I/O	5	bus4	DQ5		True_of_IOR59B	True	J11	pull down
IOR59B	I/O	5	bus4	DQ5		Comp_of_IOR59A	True	K11	pull down
IOR5A/CCLK	I/O	3		none	CCLK	True_of_IOR5B	True	E13	pull down
IOR5B/MODE0	I/O	3		none	MODE0	Comp_of_IOR5A	True	E14	pull down
IOR61A/D07/SDA/RPLL1_T_IN2/RPLL1_T_FB0	I/O	5	bus4	DQ5	D07/SDA/RPLL1_T_IN2/RPLL1_T_FB0	True_of_IOR61B	True	K12	pull down
IOR61B/RDWR_B/SCL/RPLL1_C_IN2/RPLL1_C_FB0	I/O	5	bus4	DQ5	RDWR_B/SCL/RPLL1_C_IN2/RPLL1_C_FB0	Comp_of_IOR61A	True	K13	pull down
IOR63A/D03/SI/SSI0	I/O	5		DQS5	D03/SI/SSI0	True_of_IOR63B	True	L13	pull down
IOR63B/D04/SSPI_CS_N	I/O	5		DQS5	D04/SSPI_CS_N	Comp_of_IOR63A	True	L14	pull down
IOR66A/D05/SO/SSI1/RPLL1_T_IN1/RPLL1_T_FB1	I/O	5	bus4	DQ5	D05/SO/SSI1/RPLL1_T_IN1/RPLL1_T_FB1	True_of_IOR66B	True	L15	pull down
IOR66B/D06/SSPI_CLK/RPLL1_C_IN1/RPLL1_C_FB1	I/O	5	bus4	DQ5	D06/SSPI_CLK/RPLL1_C_IN1/RPLL1_C_FB1	Comp_of_IOR66A	True	M15	pull down
IOR68A	I/O	5	bus4	DQ5		True_of_IOR68B	True	L12	pull down
IOR68B	I/O	5	bus4	DQ5		Comp_of_IOR68A	True	M13	pull down
IOR70A/D08/SSPI_WPN/SSI2	I/O	5		DQ5	D08/SSPI_WPN/SSI2	True_of_IOR70B	True	N14	pull down
IOR70B/D09/CLKHOLD_N/SSI3	I/O	5		DQ5	D09/CLKHOLD_N/SSI3	Comp_of_IOR70A	True	N15	pull down
IOR72A/READY/RPLL1_T_IN0	I/O	5		DQ5	READY/RPLL1_T_IN0	True_of_IOR72B	True	P14	pull down
IOR72B/CSO_B/MCS_N/RPLL1_C_IN0	I/O	5		DQ5	CSO_B/MCS_N/RPLL1_C_IN0	Comp_of_IOR72A	True	P15	pull down
IOR9A/D00/DIN/MISO/MI1	I/O	3		none	D00/DIN/MISO/MI1	True_of_IOR9B	True	F12	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225H	配置过程中的IO上下拉状态
IOR9B/CSI_B/MOSI/MIO	I/O	3		none	CSI_B/MOSI/MIO	Comp_of_IOR9A	True	F13	pull down
IOT142A	I/O	2	bus0	DQ3		True_of_IOT142B	True	B13	pull down
IOT142B/DOOUT	I/O	2	bus0	DQ3	DOOUT	Comp_of_IOT142A	True	A13	pull up
IOT144A	I/O	2	bus0	DQ3		True_of_IOT144B	True	B14	pull down
IOT144B	I/O	2	bus0	DQ3		Comp_of_IOT144A	True	A14	pull down
IOT146A/TPLL1_T_IN1/TPLL1_T_FB0	I/O	2	bus0	DQ3	TPLL1_T_IN1/TPLL1_T_FB0	True_of_IOT146B	True	D13	pull down
IOT146B/TPLL1_C_IN1/TPLL1_C_FB0	I/O	2	bus0	DQ3	TPLL1_C_IN1/TPLL1_C_FB0	Comp_of_IOT146A	True	C14	pull down
IOT74A	I/O	1	bus2	DQ0		True_of_IOT74B	True	E5	pull down
IOT74B	I/O	1	bus2	DQ0		Comp_of_IOT74A	True	E6	pull down
IOT76A/TPLL0_T_IN0	I/O	1	bus2	DQ0	TPLL0_T_IN0	True_of_IOT76B	True	E7	pull down
IOT76B/TPLL0_C_IN0	I/O	1	bus2	DQ0	TPLL0_C_IN0	Comp_of_IOT76A	True	E8	pull down
IOT78A	I/O	1	bus2	DQ0		True_of_IOT78B	True	F8	pull down
IOT78B	I/O	1	bus2	DQ0		Comp_of_IOT78A	True	E9	pull down
IOT89A/GCLKT_1/Q0REF_T_IN0	I/O	1	bus2	DQ0	GCLKT_1/Q0REF_T_IN0	True_of_IOT89B	True	F10	pull down
IOT89B/GCLKC_1/Q0REF_C_IN0	I/O	1	bus2	DQ0	GCLKC_1/Q0REF_C_IN0	Comp_of_IOT89A	True	E10	pull down
M0_CKN	DIO	MIPI		none			none	F3	pull down
M0_CKP	DIO	MIPI		none			none	G3	pull down
M0_D0N	DIO	MIPI		none			none	J3	pull down
M0_D0P	DIO	MIPI		none			none	J4	pull down
M0_D1N	DIO	MIPI		none			none	H3	pull down
M0_D1P	DIO	MIPI		none			none	H4	pull down
M0_D2N	DIO	MIPI		none			none	E3	pull down
M0_D2P	DIO	MIPI		none			none	F4	pull down
M0_D3N	DIO	MIPI		none			none	C2	pull down
M0_D3P	DIO	MIPI		none			none	C1	pull down
M1_D0A	DIO	MIPI		none			none	D1	pull down
M1_D0B	DIO	MIPI		none			none	E2	pull down
M1_D0C	DIO	MIPI		none			none	E1	pull down
M1_D1A	DIO	MIPI		none			none	F1	pull down
M1_D1B	DIO	MIPI		none			none	G1	pull down
M1_D1C	DIO	MIPI		none			none	G2	pull down
M1_D2A	DIO	MIPI		none			none	H1	pull down
M1_D2B	DIO	MIPI		none			none	J1	pull down
M1_D2C	DIO	MIPI		none			none	J2	pull down
Q0_LN0_RXM_I	DIO	Q0		none			none	A3	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225H	配置过程中的IO上下拉状态
Q0_LN0_RXP_I	DIO	Q0		none			none	B3	pull down
Q0_LN0_TXM_O	DIO	Q0		none			none	C4	pull down
Q0_LN0_TXP_O	DIO	Q0		none			none	D4	pull down
Q0_LN1_RXM_I	DIO	Q0		none			none	A7	pull down
Q0_LN1_RXP_I	DIO	Q0		none			none	B7	pull down
Q0_LN1_TXM_O	DIO	Q0		none			none	C6	pull down
Q0_LN1_TXP_O	DIO	Q0		none			none	D6	pull down
Q0_LN2_RXM_I	DIO	Q0		none			none	A9	pull down
Q0_LN2_RXP_I	DIO	Q0		none			none	B9	pull down
Q0_LN2_TXM_O	DIO	Q0		none			none	C8	pull down
Q0_LN2_TXP_O	DIO	Q0		none			none	D8	pull down
Q0_LN3_RXM_I	DIO	Q0		none			none	A11	pull down
Q0_LN3_RXP_I	DIO	Q0		none			none	B11	pull down
Q0_LN3_TXM_O	DIO	Q0		none			none	C12	pull down
Q0_LN3_TXP_O	DIO	Q0		none			none	D12	pull down
Q0_REFCLKM_0	DIO	Q0		none			none	A5	pull down
Q0_REFCLKM_1	DIO	Q0		none			none	C10	pull down
Q0_REFCLKP_0	DIO	Q0		none			none	B5	pull down
Q0_REFCLKP_1	DIO	Q0		none			none	D10	pull down
VDD12_MIP1	Power	N/A						G6	pull down
VDDA_MIP1/VDDD_MIP1	Power	N/A						D2	pull down
VDDA_MIP1/VDDD_MIP1	Power	N/A						H2	pull down
VDDA_MIP1/VDDD_MIP1	Power	N/A						G4	pull down
VDDX_MIP1/VCCIO6/VCCIO7/VCCX	Power	N/A						M12	pull down
VDDX_MIP1/VCCIO6/VCCIO7/VCCX	Power	N/A						J6	pull down
VDDX_MIP1/VCCIO6/VCCIO7/VCCX	Power	N/A						B1	pull down
VDDX_MIP1/VCCIO6/VCCIO7/VCCX	Power	N/A						F7	pull down
VDDX_MIP1/VCCIO6/VCCIO7/VCCX	Power	N/A						K9	pull down
VDDX_MIP1/VCCIO6/VCCIO7/VCCX	Power	N/A						G10	pull down
VDDA_Q0	Power	N/A						D5	pull down
VDDA_Q0	Power	N/A						D11	pull down
VDDA_Q0	Power	N/A						B8	pull down
VDDHA_Q0	Power	N/A						D7	pull down
VDDHA_Q0	Power	N/A						D9	pull down
VDDT_Q0	Power	N/A						A10	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225H	配置过程中的IO上下拉状态
VDDT_Q0	Power	N/A						A6	pull down
VDDT_Q0	Power	N/A						B4	pull down
VCC	Power	N/A						F9	pull down
VCC	Power	N/A						H9	pull down
VCC	Power	N/A						G8	pull down
VCC	Power	N/A						J8	pull down
VCC	Power	N/A						H7	pull down
VCC	Power	N/A						K7	pull down
VCC	Power	N/A						J10	pull down
VCCIO1/VCCIO2	Power	N/A						B12	pull down
VCCIO10/VCCIO11	Power	N/A						M2	pull down
VCCIO10/VCCIO11	Power	N/A						L4	pull down
VCCIO12/VCCIO3/VCCIO4	Power	N/A						D14	pull down
VCCIO12/VCCIO3/VCCIO4	Power	N/A						H14	pull down
VCCIO5	Power	N/A						M14	pull down
VCCIO5	Power	N/A						J12	pull down
VCCIO8	Power	N/A						P12	pull down
VCCIO8	Power	N/A						P8	pull down
VCCIO9	Power	N/A						M7	pull down
VCCIO9	Power	N/A						P4	pull down
VCC_ADC	Power	N/A						E12	pull down
V_EFUSE	Power	N/A						L11	pull down
VSS	Ground	N/A						A1	pull down
VSS	Ground	N/A						A15	pull down
VSS	Ground	N/A						B10	pull down
VSS	Ground	N/A						B6	pull down
VSS	Ground	N/A						C13	pull down
VSS	Ground	N/A						C3	pull down
VSS	Ground	N/A						E11	pull down
VSS	Ground	N/A						F14	pull down
VSS	Ground	N/A						F2	pull down
VSS	Ground	N/A						F6	pull down
VSS	Ground	N/A						G7	pull down
VSS	Ground	N/A						G9	pull down
VSS	Ground	N/A						H8	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225H	配置过程中的IO上下拉状态
VSS	Ground	N/A						J7	pull down
VSS	Ground	N/A						J9	pull down
VSS	Ground	N/A						K14	pull down
VSS	Ground	N/A						K2	pull down
VSS	Ground	N/A						K6	pull down
VSS	Ground	N/A						N13	pull down
VSS	Ground	N/A						N3	pull down
VSS	Ground	N/A						P10	pull down
VSS	Ground	N/A						P6	pull down
VSS	Ground	N/A						R1	pull down
VSS	Ground	N/A						R15	pull down
VSS	Ground	N/A						A2	pull down
VSS	Ground	N/A						B2	pull down
VSS	Ground	N/A						A4	pull down
VSS	Ground	N/A						D3	pull down
VSS	Ground	N/A						E4	pull down
VSS	Ground	N/A						A8	pull down
VSS	Ground	N/A						C9	pull down
VSS	Ground	N/A						C11	pull down
VSS	Ground	N/A						A12	pull down
VSS	Ground	N/A						C7	pull down
VSS	Ground	N/A						C5	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324A	配置过程中的IO上下拉状态
IOB102A/D11	I/O	7	bus4	DQ7	D11	True_of_IOB102B	True	M14	Pull up&HighZ
IOB102B/D12	I/O	7	bus4	DQ7	D12	Comp_of_IOB102A	True	N14	Pull up&HighZ
IOB104A/D09	I/O	7	bus4	DQ7	D09	True_of_IOB104B	True	M16	Pull up&HighZ
IOB104B/D10	I/O	7	bus4	DQ7	D10	Comp_of_IOB104A	True	M17	Pull up&HighZ
IOB108B	I/O	7	bus4	DQ7		Comp_of_IOB108A	True	L14	Pull up&HighZ
IOB111A/MCS_N	I/O	7	bus4	DQ6	MCS_N	True_of_IOB111B	True	L15	Pull up&HighZ
IOB111B/D08	I/O	7	bus4	DQ6	D08	Comp_of_IOB111A	True	M15	Pull up&HighZ
IOB113A/D06	I/O	7	bus4	DQ6	D06	True_of_IOB113B	True	J14	Pull up&HighZ
IOB113B/D07	I/O	7	bus4	DQ6	D07	Comp_of_IOB113A	True	K15	Pull up&HighZ
IOB115A/D04	I/O	7	bus4	DQ6	D04	True_of_IOB115B	True	K17	Pull up&HighZ
IOB115B/D05	I/O	7	bus4	DQ6	D05	Comp_of_IOB115A	True	L18	Pull up&HighZ
IOB117A/PUDC_B	I/O	7	bus4	DQS6	PUDC_B	True_of_IOB117B	True	J18	Pull down
IOB117B/EMCCLK	I/O	7	bus4	DQS6	EMCCLK	Comp_of_IOB117A	True	K18	Pull up&HighZ
IOB120A/D02/MI2	I/O	7	bus4	DQ6	D02/MI2	True_of_IOB120B	True	J15	Pull up&HighZ
IOB120B/D03/MI3	I/O	7	bus4	DQ6	D03/MI3	Comp_of_IOB120A	True	J16	Pull up&HighZ
IOB128A/D00/MOSI/MI0	I/O	6	bus4	DQ6	D00/MOSI/MI0	True_of_IOB128B	True	K16	Pull up&HighZ
IOB128B/D01/DIN/MISO/MI1	I/O	6	bus4	DQ6	D01/DIN/MISO/MI1	Comp_of_IOB128A	True	L17	Pull up&HighZ
IOB12A	I/O	9		DQS12		True_of_IOB12B	True	R5	Pull up&HighZ
IOB12B	I/O	9		DQS12		Comp_of_IOB12A	True	T5	Pull up&HighZ
IOB134B	I/O	6	bus4	none		Comp_of_IOB134A	True	U10	Pull up&HighZ
IOB136A	I/O	6	bus4	none		True_of_IOB136B	True	U9	Pull up&HighZ
IOB136B	I/O	6	bus4	none		Comp_of_IOB136A	True	V9	Pull up&HighZ
IOB138A	I/O	6	bus4	none		True_of_IOB138B	True	U11	Pull up&HighZ
IOB138B	I/O	6	bus4	none		Comp_of_IOB138A	True	V11	Pull up&HighZ
IOB140A	I/O	6	bus4	none		True_of_IOB140B	True	T12	Pull up&HighZ
IOB140B	I/O	6	bus4	none		Comp_of_IOB140A	True	U12	Pull up&HighZ
IOB142A	I/O	6	bus4	none		True_of_IOB142B	True	V12	Pull up&HighZ
IOB142B	I/O	6	bus4	none		Comp_of_IOB142A	True	V13	Pull up&HighZ
IOB144A	I/O	6		none		True_of_IOB144B	True	U14	Pull up&HighZ
IOB144B	I/O	6		none		Comp_of_IOB144A	True	V14	Pull up&HighZ
IOB146A	I/O	6		none		True_of_IOB146B	True	R13	Pull up&HighZ
IOB146B	I/O	6		none		Comp_of_IOB146A	True	T13	Pull up&HighZ
IOB14A	I/O	9		DQ12		True_of_IOB14B	True	U6	Pull up&HighZ
IOB14B	I/O	9		DQ12		Comp_of_IOB14A	True	U5	Pull up&HighZ
IOB16A	I/O	9	bus4	DQ12		True_of_IOB16B	True	P6	Pull up&HighZ

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324A	配置过程中的IO上下拉状态
IOB16B	I/O	9	bus4	DQ12		Comp_of_IOB16A	True	P5	Pull up&HighZ
IOB18A	I/O	9	bus4	DQ11		True_of_IOB18B	True	U4	Pull up&HighZ
IOB18B	I/O	9	bus4	DQ11		Comp_of_IOB18A	True	V4	Pull up&HighZ
IOB1A	I/O	9	bus4	DQ12		True_of_IOB1B	True	V8	Pull up&HighZ
IOB1B	I/O	9	bus4	DQ12		Comp_of_IOB1A	True	V7	Pull up&HighZ
IOB20A	I/O	9	bus4	DQ11		True_of_IOB20B	True	T4	Pull up&HighZ
IOB20B	I/O	9	bus4	DQ11		Comp_of_IOB20A	True	T3	Pull up&HighZ
IOB22A	I/O	9	bus4	DQ11		True_of_IOB22B	True	V3	Pull up&HighZ
IOB22B	I/O	9	bus4	DQ11		Comp_of_IOB22A	True	V2	Pull up&HighZ
IOB26A	I/O	9	bus4	DQS11		True_of_IOB26B	True	U2	Pull up&HighZ
IOB26B	I/O	9	bus4	DQS11		Comp_of_IOB26A	True	U1	Pull up&HighZ
IOB31A/GCLKT_14	I/O	9	bus4	DQ11	GCLKT_14	True_of_IOB31B	True	R3	Pull up&HighZ
IOB31B/GCLKC_14	I/O	9	bus4	DQ11	GCLKC_14	Comp_of_IOB31A	True	T2	Pull up&HighZ
IOB33A/GCLKT_15	I/O	9	bus4	DQ11	GCLKT_15	True_of_IOB33B	True	R2	Pull up&HighZ
IOB33B/GCLKC_15	I/O	9	bus4	DQ11	GCLKC_15	Comp_of_IOB33A	True	R1	Pull up&HighZ
IOB38A/GCLKT_13	I/O	9	bus4	DQ10	GCLKT_13	True_of_IOB38B	True	P4	Pull up&HighZ
IOB38B/GCLKC_13	I/O	9	bus4	DQ10	GCLKC_13	Comp_of_IOB38A	True	P3	Pull up&HighZ
IOB3A/BPLL0_T_IN3/BPLL0_T_FB4	I/O	9	bus4	DQ12	BPLL0_T_IN3/BPLL0_T_FB4	True_of_IOB3B	True	U7	Pull up&HighZ
IOB3B/BPLL0_C_IN3/BPLL0_C_FB4	I/O	9	bus4	DQ12	BPLL0_C_IN3/BPLL0_C_FB4	Comp_of_IOB3A	True	V6	Pull up&HighZ
IOB40A/GCLKT_12	I/O	9	bus4	DQ10	GCLKT_12	True_of_IOB40B	True	N3	Pull up&HighZ
IOB40B/GCLKC_12	I/O	9	bus4	DQ10	GCLKC_12	Comp_of_IOB40A	True	N2	Pull up&HighZ
IOB42A	I/O	9	bus4	DQ10		True_of_IOB42B	True	M4	Pull up&HighZ
IOB42B	I/O	9	bus4	DQ10		Comp_of_IOB42A	True	N4	Pull up&HighZ
IOB44A	I/O	9	bus4	DQS10		True_of_IOB44B	True	N1	Pull up&HighZ
IOB44B	I/O	9	bus4	DQS10		Comp_of_IOB44A	True	P1	Pull up&HighZ
IOB50A	I/O	9	bus4	DQ10/DQ S_910		True_of_IOB50B	True	M6	Pull up&HighZ
IOB50B	I/O	9	bus4	DQ10/DQ S_910		Comp_of_IOB50A	True	N6	Pull up&HighZ
IOB52A	I/O	9	bus4	DQ10		True_of_IOB52B	True	M2	Pull up&HighZ
IOB52B	I/O	9	bus4	DQ10		Comp_of_IOB52A	True	M1	Pull up&HighZ
IOB58A	I/O	9	bus4	DQ9		True_of_IOB58B	True	L5	Pull up&HighZ
IOB58B	I/O	9	bus4	DQ9		Comp_of_IOB58A	True	M5	Pull up&HighZ
IOB5A/BPLL0_T_IN4/BPLL0_T_FB3	I/O	9	bus4	DQ12	BPLL0_T_IN4/BPLL0_T_FB3	True_of_IOB5B	True	R7	Pull up&HighZ
IOB5B/BPLL0_C_IN4/BPLL0_C_FB3	I/O	9	bus4	DQ12	BPLL0_C_IN4/BPLL0_C_FB3	Comp_of_IOB5A	True	T7	Pull up&HighZ

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324A	配置过程中的IO上下拉状态
IOB60A	I/O	9	bus4	DQ9		True_of_IOB60B	True	L4	Pull up&HighZ
IOB60B	I/O	9	bus4	DQ9		Comp_of_IOB60A	True	L3	Pull up&HighZ
IOB62A	I/O	9	bus4	DQS9		True_of_IOB62B	True	K2	Pull up&HighZ
IOB62B	I/O	9	bus4	DQS9		Comp_of_IOB62A	True	K1	Pull up&HighZ
IOB65A	I/O	9	bus4	DQ9		True_of_IOB65B	True	K3	Pull up&HighZ
IOB65B	I/O	9	bus4	DQ9		Comp_of_IOB65A	True	L2	Pull up&HighZ
IOB67A/BPLL1_T_IN2	I/O	9	bus4	DQ9	BPLL1_T_IN2	True_of_IOB67B	True	J5	Pull up&HighZ
IOB67B/BPLL1_C_IN2	I/O	9	bus4	DQ9	BPLL1_C_IN2	Comp_of_IOB67A	True	J4	Pull up&HighZ
IOB69B	I/O	9	bus4	DQ9		Comp_of_IOB69A	True	J6	Pull up&HighZ
IOB71A/BPLL1_T_IN3/BPLL1_T_FB4	I/O	9	bus4	DQ9	BPLL1_T_IN3/BPLL1_T_FB4	True_of_IOB71B	True	K6	Pull up&HighZ
IOB71B/BPLL1_C_IN3/BPLL1_C_FB4	I/O	9	bus4	DQ9	BPLL1_C_IN3/BPLL1_C_FB4	Comp_of_IOB71A	True	K5	Pull up&HighZ
IOB75A	I/O	8	bus4	DQ8		True_of_IOB75B	True	V16	Pull up&HighZ
IOB75B	I/O	8	bus4	DQ8		Comp_of_IOB75A	True	V17	Pull up&HighZ
IOB77A/BPLL1_T_IN4/BPLL1_T_FB3	I/O	8	bus4	DQ8	BPLL1_T_IN4/BPLL1_T_FB3	True_of_IOB77B	True	U15	Pull up&HighZ
IOB77B/BPLL1_C_IN4/BPLL1_C_FB3	I/O	8	bus4	DQ8	BPLL1_C_IN4/BPLL1_C_FB3	Comp_of_IOB77A	True	U16	Pull up&HighZ
IOB79A/CSI_B	I/O	8	bus4	DQ8	CSI_B	True_of_IOB79B	True	T17	Pull up&HighZ
IOB79B	I/O	8	bus4	DQ8		Comp_of_IOB79A	True	U17	Pull up&HighZ
IOB7A	I/O	9	bus4	DQ12		True_of_IOB7B	True	R6	Pull up&HighZ
IOB81A/GCLKT_11	I/O	8		DQ8	GCLKT_11	True_of_IOB81B	True	R16	Pull up&HighZ
IOB81B/GCLKC_11	I/O	8		DQ8	GCLKC_11	Comp_of_IOB81A	True	R17	Pull up&HighZ
IOB85A/RDWR_B	I/O	8		DQS8	RDWR_B	True_of_IOB85B	True	R18	Pull up&HighZ
IOB85B/CSO_B/DOUT	I/O	8		DQS8	CSO_B/DOUT	Comp_of_IOB85A	True	T18	Pull up
IOB87A/GCLKT_10	I/O	8	bus4	DQ8	GCLKT_10	True_of_IOB87B	True	T14	Pull up&HighZ
IOB87B/GCLKC_10	I/O	8	bus4	DQ8	GCLKC_10	Comp_of_IOB87A	True	T15	Pull up&HighZ
IOB93A/GCLKT_9	I/O	8	bus4	DQ7	GCLKT_9	True_of_IOB93B	True	P14	Pull up&HighZ
IOB93B/GCLKC_9	I/O	8	bus4	DQ7	GCLKC_9	Comp_of_IOB93A	True	R15	Pull up&HighZ
IOB95A/GCLKT_8	I/O	8	bus4	DQ7	GCLKT_8	True_of_IOB95B	True	P15	Pull up&HighZ
IOB95B/GCLKC_8	I/O	8	bus4	DQ7	GCLKC_8	Comp_of_IOB95A	True	P16	Pull up&HighZ
IOB97A/D14	I/O	8	bus4	DQ7	D14	True_of_IOB97B	True	N18	Pull up&HighZ
IOB97B/D15	I/O	8	bus4	DQ7	D15	Comp_of_IOB97A	True	P18	Pull up&HighZ
IOB99A	I/O	8	bus4	DQS7		True_of_IOB99B	True	N16	Pull up&HighZ
IOB99B/D13	I/O	8	bus4	DQS7	D13	Comp_of_IOB99A	True	N17	Pull up&HighZ
IOR11A/TPLL1_T_IN3/TPLL1_T_FB4	I/O	3	bus3	none	TPLL1_T_IN3/TPLL1_T_FB4	True_of_IOR11B	True	E12	Pull up&HighZ
IOR11B/MODE2/TPLL1_C_IN3/TPLL1_C_FB4	I/O	3	bus3	none	MODE2/TPLL1_C_IN3/TPLL1_C_FB4	Comp_of_IOR11A	True	F13	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324A	配置过程中的IO上下拉状态
IOR1A/TCK	I/O	12		none	TCK		none	F8	Pull up
IOR1B/TDI	I/O	12		none	TDI		none	T9	Pull up
IOR3A/TMS	I/O	12		none	TMS		none	R8	Pull up
IOR3B/TDO	I/O	12		none	TDO		none	T8	Pull up
IOR5A/CCLK	I/O	3		none	CCLK	True_of_IOR5B	True	E8	Pull up&HighZ
IOR5B/READY	I/O	3		none	READY	Comp_of_IOR5A	True	T10	Pull down
IOR7A/MODE1	I/O	3		none	MODE1	True_of_IOR7B	True	R11	Pull up
IOR7B/MODE0	I/O	3		none	MODE0	Comp_of_IOR7A	True	R12	Pull down
IOR9A/DONE	I/O	3		none	DONE	True_of_IOR9B	True	F12	Pull down
IOR9B/RECONFIG_N	I/O	3		none	RECONFIG_N	Comp_of_IOR9A	True	P10	Pull up
IOT102A	I/O	1	bus2	DQ1		True_of_IOT102B	True	B14	Pull up&HighZ
IOT102B	I/O	1	bus2	DQ1		Comp_of_IOT102A	True	A15	Pull up&HighZ
IOT104A/GCLKT_1	I/O	1	bus2	DQ1	GCLKT_1	True_of_IOT104B	True	E13	Pull up&HighZ
IOT104B/GCLKC_1	I/O	1	bus2	DQ1	GCLKC_1	Comp_of_IOT104A	True	D14	Pull up&HighZ
IOT106A/GCLKT_0	I/O	1	bus2	DQ1	GCLKT_0	True_of_IOT106B	True	D13	Pull up&HighZ
IOT106B/GCLKC_0	I/O	1	bus2	DQ1	GCLKC_0	Comp_of_IOT106A	True	C13	Pull up&HighZ
IOT113A/GCLKT_2/Q0REF_T_IN1	I/O	2	bus1	DQ2	GCLKT_2/Q0REF_T_IN1	True_of_IOT113B	True	E15	Pull up&HighZ
IOT113B/GCLKC_2/Q0REF_C_IN1	I/O	2	bus1	DQ2	GCLKC_2/Q0REF_C_IN1	Comp_of_IOT113A	True	D15	Pull up&HighZ
IOT115A/GCLKT_3	I/O	2	bus1	DQ2	GCLKT_3	True_of_IOT115B	True	E16	Pull up&HighZ
IOT115B/GCLKC_3	I/O	2	bus1	DQ2	GCLKC_3	Comp_of_IOT115A	True	D16	Pull up&HighZ
IOT117A	I/O	2	bus1	DQS2		True_of_IOT117B	True	B16	Pull up&HighZ
IOT117B	I/O	2	bus1	DQS2		Comp_of_IOT117A	True	A17	Pull up&HighZ
IOT120A	I/O	2	bus1	DQ2		True_of_IOT120B	True	C16	Pull up&HighZ
IOT120B	I/O	2	bus1	DQ2		Comp_of_IOT120A	True	B17	Pull up&HighZ
IOT122A	I/O	2	bus1	DQ2		True_of_IOT122B	True	E17	Pull up&HighZ
IOT122B	I/O	2	bus1	DQ2		Comp_of_IOT122A	True	D18	Pull up&HighZ
IOT124A	I/O	2	bus1	DQ2		True_of_IOT124B	True	C17	Pull up&HighZ
IOT124B	I/O	2	bus1	DQ2		Comp_of_IOT124A	True	C18	Pull up&HighZ
IOT129A	I/O	2	bus0	DQ3		True_of_IOT129B	True	G17	Pull up&HighZ
IOT129B	I/O	2	bus0	DQ2		Comp_of_IOT129A	True	F18	Pull up&HighZ
IOT131A	I/O	2	bus0	DQ3		True_of_IOT131B	True	H16	Pull up&HighZ
IOT131B	I/O	2	bus0	DQ3		Comp_of_IOT131A	True	G16	Pull up&HighZ
IOT135A	I/O	2	bus0	DQS3		True_of_IOT135B	True	G15	Pull up&HighZ
IOT135B	I/O	2	bus0	DQS3		Comp_of_IOT135A	True	F15	Pull up&HighZ
IOT138A	I/O	2	bus0	DQ3		True_of_IOT138B	True	G14	Pull up&HighZ

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324A	配置过程中的IO上下拉状态
IOT138B	I/O	2	bus0	DQ3		Comp_of_IOT138A	True	F14	Pull up&HighZ
IOT140A	I/O	2	bus0	DQ3		True_of_IOT140B	True	H14	Pull up&HighZ
IOT142A	I/O	2	bus0	DQ3		True_of_IOT142B	True	H17	Pull up&HighZ
IOT142B	I/O	2	bus0	DQ3		Comp_of_IOT142A	True	H18	Pull up&HighZ
IOT146A/TPLL1_T_IN4/TPLL1_T_FB3	I/O	2	bus0	DQ3	TPLL1_T_IN4/TPLL1_T_FB3	True_of_IOT146B	True	F17	Pull up&HighZ
IOT146B/TPLL1_C_IN4/TPLL1_C_FB3	I/O	2	bus0	DQ3	TPLL1_C_IN4/TPLL1_C_FB3	Comp_of_IOT146A	True	E18	Pull up&HighZ
IOT76A/TPLL0_T_IN2	I/O	1	bus2	DQ0	TPLL0_T_IN2	True_of_IOT76B	True	D8	Pull up&HighZ
IOT76B/TPLL0_C_IN2	I/O	1	bus2	DQ0	TPLL0_C_IN2	Comp_of_IOT76A	True	C8	Pull up&HighZ
IOT80A/TPLL0_T_IN3/TPLL0_T_FB4	I/O	1	bus2	DQ0	TPLL0_T_IN3/TPLL0_T_FB4	True_of_IOT80B	True	D9	Pull up&HighZ
IOT80B/TPLL0_C_IN3/TPLL0_C_FB4	I/O	1	bus2	DQ0	TPLL0_C_IN3/TPLL0_C_FB4	Comp_of_IOT80A	True	C9	Pull up&HighZ
IOT82A/TPLL0_T_IN4/TPLL0_T_FB3/ PCIE_HOST_RSTN	I/O	1	bus2	DQ0	TPLL0_T_IN4/TPLL0_T_FB3/ PCIE_HOST_RSTN	True_of_IOT82B	True	B10	Pull up&HighZ
IOT82B/TPLL0_C_IN4/TPLL0_C_FB3/ PCIE_HOST_RSTN	I/O	1	bus2	DQ0	TPLL0_C_IN4/TPLL0_C_FB3/ PCIE_HOST_RSTN	Comp_of_IOT82A	True	A10	Pull up&HighZ
IOT85A	I/O	1		DQS0		True_of_IOT85B	True	B9	Pull up&HighZ
IOT85B	I/O	1		DQS0		Comp_of_IOT85A	True	A9	Pull up&HighZ
IOT87A	I/O	1	bus2	DQ0		True_of_IOT87B	True	C11	Pull up&HighZ
IOT87B	I/O	1	bus2	DQ0		Comp_of_IOT87A	True	B11	Pull up&HighZ
IOT89A/Q0REF_T_IN0	I/O	1	bus2	DQ0	Q0REF_T_IN0	True_of_IOT89B	True	D11	Pull up&HighZ
IOT89B/Q0REF_C_IN0	I/O	1	bus2	DQ0	Q0REF_C_IN0	Comp_of_IOT89A	True	C12	Pull up&HighZ
IOT91A	I/O	1	bus2	DQ1		True_of_IOT91B	True	D10	Pull up&HighZ
IOT95A	I/O	1	bus2	DQ1		True_of_IOT95B	True	B12	Pull up&HighZ
IOT95B	I/O	1	bus2	DQ1		Comp_of_IOT95A	True	A12	Pull up&HighZ
IOT97A	I/O	1	bus2	DQ1		True_of_IOT97B	True	A13	Pull up&HighZ
IOT97B	I/O	1	bus2	DQ1		Comp_of_IOT97A	True	A14	Pull up&HighZ
IOT99A	I/O	1	bus2	DQS1		True_of_IOT99B	True	C14	Pull up&HighZ
IOT99B	I/O	1	bus2	DQS1		Comp_of_IOT99A	True	B15	Pull up&HighZ
ADC_TN	DIO	ADC		none			none	M9	
ADC_TP	DIO	ADC		none			none	M10	
ADC_VN	DIO	ADC		none			none	L9	
ADC_VP	DIO	ADC		none			none	K10	
Q0_ATEST_O	DIO	Q0		none			none	A6	
Q0_LN0_RXM_I	DIO	Q0		none			none	A3	
Q0_LN0_RXP_I	DIO	Q0		none			none	A4	
Q0_LN0_TXM_O	DIO	Q0		none			none	F1	

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324A	配置过程中的IO上下拉状态
Q0_LN0_TXP_O	DIO	Q0		none			none	F2	
Q0_LN1_RXM_I	DIO	Q0		none			none	E3	
Q0_LN1_RXP_I	DIO	Q0		none			none	E4	
Q0_LN1_TXM_O	DIO	Q0		none			none	H1	
Q0_LN1_TXP_O	DIO	Q0		none			none	H2	
Q0_LN2_RXM_I	DIO	Q0		none			none	C3	
Q0_LN2_RXP_I	DIO	Q0		none			none	C4	
Q0_LN2_TXM_O	DIO	Q0		none			none	D1	
Q0_LN2_TXP_O	DIO	Q0		none			none	D2	
Q0_LN3_RXM_I	DIO	Q0		none			none	G3	
Q0_LN3_RXP_I	DIO	Q0		none			none	G4	
Q0_LN3_TXM_O	DIO	Q0		none			none	B1	
Q0_LN3_TXP_O	DIO	Q0		none			none	B2	
Q0_REFCLKM_0	DIO	Q0		none			none	D5	
Q0_REFCLKM_1	DIO	Q0		none			none	B5	
Q0_REFCLKP_0	DIO	Q0		none			none	D6	
Q0_REFCLKP_1	DIO	Q0		none			none	B6	
VCC	Power	N/A						K7	
VCC	Power	N/A						F9	
VCC	Power	N/A						M7	
VCC	Power	N/A						J12	
VCC	Power	N/A						N10	
VCC	Power	N/A						M11	
VCC	Power	N/A						N8	
VCC	Power	N/A						K11	
VCC	Power	N/A						N12	
VCC	Power	N/A						H7	
VCC	Power	N/A						P11	
VCC	Power	N/A						L12	
VCC	Power	N/A						J8	
VCC	Power	N/A						P9	
VCC	Power	N/A						G8	
VCC	Power	N/A						L8	
VCC	Power	N/A						F7	
VCC	Power	N/A						H9	

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324A	配置过程中的IO上下拉状态
VCC_ADC/V_EFUSE	Power	N/A						J10	
VDD12_MIPI	Power	N/A						G2	
VDD12_MIPI	Power	N/A						E1	
VDD12_MIPI	Power	N/A						A2	
VDD12_MIPI	Power	N/A						C1	
VDD12_MIPI	Power	N/A						F3	
VCCIO1	Power	N/A						C10	
VCCIO1	Power	N/A						A16	
VCCIO1	Power	N/A						B13	
VCCIO10/VCCIO11/VCCIO4/VCCIO5/ VCCX	Power	N/A						K13	
VCCIO10/VCCIO11/VCCIO4/VCCIO5/ VCCX	Power	N/A						H13	
VCCIO10/VCCIO11/VCCIO4/VCCIO5/ VCCX	Power	N/A						P13	
VCCIO10/VCCIO11/VCCIO4/VCCIO5/ VCCX	Power	N/A						M13	
VCCIO12	Power	N/A						R10	
VCCIO2	Power	N/A						G18	
VCCIO2	Power	N/A						H15	
VCCIO2	Power	N/A						D17	
VCCIO2	Power	N/A						E14	
VCCIO3	Power	N/A						E10	
VCCIO6	Power	N/A						U8	
VCCIO6	Power	N/A						T11	
VCCIO7	Power	N/A						P17	
VCCIO7	Power	N/A						L16	
VCCIO8	Power	N/A						R14	
VCCIO8	Power	N/A						V15	
VCCIO8	Power	N/A						U18	
VCCIO9	Power	N/A						V5	
VCCIO9	Power	N/A						M3	
VCCIO9	Power	N/A						P7	
VCCIO9	Power	N/A						T1	
VCCIO9	Power	N/A						R4	

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324A	配置过程中的IO上下拉状态
VCCIO9	Power	N/A						L6	
VDDA_Q0	Power	N/A						F5	
VDDA_Q0	Power	N/A						E5	
VDDHA_Q0	Power	N/A						G12	
VDDT_Q0	Power	N/A						B4	
VDDT_Q0	Power	N/A						C5	
VREFN	Power	N/A						K9	
VREFP	Power	N/A						L10	
VSS	Ground	N/A						A1	
VSS	Ground	N/A						A11	
VSS	Ground	N/A						A18	
VSS	Ground	N/A						A5	
VSS	Ground	N/A						A7	
VSS	Ground	N/A						A8	
VSS	Ground	N/A						B18	
VSS	Ground	N/A						B3	
VSS	Ground	N/A						B7	
VSS	Ground	N/A						B8	
VSS	Ground	N/A						C15	
VSS	Ground	N/A						C2	
VSS	Ground	N/A						C6	
VSS	Ground	N/A						C7	
VSS	Ground	N/A						D12	
VSS	Ground	N/A						D3	
VSS	Ground	N/A						D4	
VSS	Ground	N/A						D7	
VSS	Ground	N/A						E2	
VSS	Ground	N/A						E6	
VSS	Ground	N/A						E7	
VSS	Ground	N/A						E9	
VSS	Ground	N/A						F10	
VSS	Ground	N/A						F16	
VSS	Ground	N/A						F4	
VSS	Ground	N/A						F6	
VSS	Ground	N/A						G1	

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324A	配置过程中的IO上下拉状态
VSS	Ground	N/A						G11	
VSS	Ground	N/A						G13	
VSS	Ground	N/A						G5	
VSS	Ground	N/A						G6	
VSS	Ground	N/A						G7	
VSS	Ground	N/A						G9	
VSS	Ground	N/A						H10	
VSS	Ground	N/A						H12	
VSS	Ground	N/A						H3	
VSS	Ground	N/A						H4	
VSS	Ground	N/A						H5	
VSS	Ground	N/A						H6	
VSS	Ground	N/A						H8	
VSS	Ground	N/A						J1	
VSS	Ground	N/A						J11	
VSS	Ground	N/A						J13	
VSS	Ground	N/A						J17	
VSS	Ground	N/A						J2	
VSS	Ground	N/A						J3	
VSS	Ground	N/A						J7	
VSS	Ground	N/A						J9	
VSS	Ground	N/A						K12	
VSS	Ground	N/A						K14	
VSS	Ground	N/A						K4	
VSS	Ground	N/A						K8	
VSS	Ground	N/A						L1	
VSS	Ground	N/A						L11	
VSS	Ground	N/A						L13	
VSS	Ground	N/A						L7	
VSS	Ground	N/A						M12	
VSS	Ground	N/A						M18	
VSS	Ground	N/A						M8	
VSS	Ground	N/A						N11	
VSS	Ground	N/A						N13	

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324A	配置过程中的IO上下拉状态
VSS	Ground	N/A						N15	
VSS	Ground	N/A						N5	
VSS	Ground	N/A						N7	
VSS	Ground	N/A						N9	
VSS	Ground	N/A						P12	
VSS	Ground	N/A						P2	
VSS	Ground	N/A						P8	
VSS	Ground	N/A						R9	
VSS	Ground	N/A						T16	
VSS	Ground	N/A						T6	
VSS	Ground	N/A						U13	
VSS	Ground	N/A						U3	
VSS	Ground	N/A						V1	
VSS	Ground	N/A						V10	
VSS	Ground	N/A						V18	
NC	N/A	N/A						E11	
NC	N/A	N/A						F11	
NC	N/A	N/A						G10	
NC	N/A	N/A						H11	

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
IOB12A	I/O	9		DQS12		True_of_IOB12B	True	E3	pull up
IOB12B	I/O	9		DQS12		Comp_of_IOB12A	True	E1	pull up
IOB14A	I/O	9		DQ12		True_of_IOB14B	True	F2	pull up
IOB14B	I/O	9		DQ12		Comp_of_IOB14A	True	F1	pull up
IOB16A	I/O	9	bus4	DQ12		True_of_IOB16B	True	G3	pull up
IOB16B	I/O	9	bus4	DQ12		Comp_of_IOB16A	True	G1	pull up
IOB18A/GCLKT_12	I/O	9	bus4	DQ11	GCLKT_12	True_of_IOB18B	True	H2	pull up
IOB18B/GCLKC_12	I/O	9	bus4	DQ11	GCLKC_12	Comp_of_IOB18A	True	H1	pull up
IOB1A	I/O	9	bus4	DQ12		True_of_IOB1B	True	C2	pull up
IOB1B	I/O	9	bus4	DQ12		Comp_of_IOB1A	True	C1	pull up
IOB20A/GCLKT_15	I/O	9	bus4	DQ11	GCLKT_15	True_of_IOB20B	True	H4	pull up
IOB20B/GCLKC_15	I/O	9	bus4	DQ11	GCLKC_15	Comp_of_IOB20A	True	H3	pull up
IOB22A	I/O	9	bus4	DQ11		True_of_IOB22B	True	H7	pull up
IOB22B	I/O	9	bus4	DQ11		Comp_of_IOB22A	True	G6	pull up
IOB24A	I/O	9	bus4	DQ11		True_of_IOB24B	True	H6	pull up
IOB24B	I/O	9	bus4	DQ11		Comp_of_IOB24A	True	H5	pull up
IOB26A	I/O	9	bus4	DQS11		True_of_IOB26B	True	L4	pull up
IOB26B	I/O	9	bus4	DQS11		Comp_of_IOB26A	True	L3	pull up
IOB29A	I/O	9	bus4	DQ11		True_of_IOB29B	True	J3	pull up
IOB29B	I/O	9	bus4	DQ11		Comp_of_IOB29A	True	J1	pull up
IOB31A	I/O	9	bus4	DQ11		True_of_IOB31B	True	K2	pull up
IOB31B	I/O	9	bus4	DQ11		Comp_of_IOB31A	True	K1	pull up
IOB33A	I/O	9	bus4	DQ11		True_of_IOB33B	True	L2	pull up
IOB33B	I/O	9	bus4	DQ11		Comp_of_IOB33A	True	L1	pull up
IOB35A/GCLKT_14	I/O	9	bus4	DQ10	GCLKT_14	True_of_IOB35B	True	K4	pull up
IOB35B/GCLKC_14	I/O	9	bus4	DQ11	GCLKC_14	Comp_of_IOB35A	True	K3	pull up
IOB38A/GCLKT_13	I/O	9	bus4	DQ10	GCLKT_13	True_of_IOB38B	True	L5	pull up
IOB38B/GCLKC_13	I/O	9	bus4	DQ10	GCLKC_13	Comp_of_IOB38A	True	K5	pull up
IOB3A/BPLL0_T_IN3/BPLL0_T_FB4	I/O	9	bus4	DQ12	BPLL0_T_IN3/BPLL0_T_FB4	True_of_IOB3B	True	F6	pull up
IOB3B/BPLL0_C_IN3/BPLL0_C_FB4	I/O	9	bus4	DQ12	BPLL0_C_IN3/BPLL0_C_FB4	Comp_of_IOB3A	True	F5	pull up
IOB40A	I/O	9	bus4	DQ10		True_of_IOB40B	True	M3	pull up
IOB40B	I/O	9	bus4	DQ10		Comp_of_IOB40A	True	M1	pull up
IOB42A	I/O	9	bus4	DQ10		True_of_IOB42B	True	N2	pull up
IOB42B	I/O	9	bus4	DQ10		Comp_of_IOB42A	True	N1	pull up
IOB44A	I/O	9	bus4	DQS10		True_of_IOB44B	True	P2	pull up
IOB44B	I/O	9	bus4	DQS10		Comp_of_IOB44A	True	P1	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
I0B48A	I/O	9	bus4	DQ10		True_of_I0B48B	True	T2	pull up
I0B48B	I/O	9	bus4	DQ10		Comp_of_I0B48A	True	T1	pull up
I0B50A	I/O	9	bus4	DQ10/DQ S_910		True_of_I0B50B	True	U2	pull up
I0B50B	I/O	9	bus4	DQ10/DQ S_910		Comp_of_I0B50A	True	U1	pull up
I0B5A/BPLL0_T_IN4/BPLL0_T_FB3	I/O	9	bus4	DQ12	BPLL0_T_IN4/BPLL0_T_FB3	True_of_I0B5B	True	E4	pull up
I0B5B/BPLL0_C_IN4/BPLL0_C_FB3	I/O	9	bus4	DQ12	BPLL0_C_IN4/BPLL0_C_FB3	Comp_of_I0B5A	True	D3	pull up
I0B62A	I/O	9	bus4	DQS9		True_of_I0B62B	True	J7	pull up
I0B62B	I/O	9	bus4	DQS9		Comp_of_I0B62A	True	J6	pull up
I0B65A	I/O	9	bus4	DQ9		True_of_I0B65B	True	N4	pull up
I0B65B	I/O	9	bus4	DQ9		Comp_of_I0B65A	True	N3	pull up
I0B67A/BPLL1_T_IN2	I/O	9	bus4	DQ9	BPLL1_T_IN2	True_of_I0B67B	True	P4	pull up
I0B67B/BPLL1_C_IN2	I/O	9	bus4	DQ9	BPLL1_C_IN2	Comp_of_I0B67A	True	P3	pull up
I0B69A	I/O	9	bus4	DQ9		True_of_I0B69B	True	L6	pull up
I0B69B	I/O	9	bus4	DQ9		Comp_of_I0B69A	True	M5	pull up
I0B71A/BPLL1_T_IN3/BPLL1_T_FB4	I/O	9	bus4	DQ9	BPLL1_T_IN3/BPLL1_T_FB4	True_of_I0B71B	True	L7	pull up
I0B71B/BPLL1_C_IN3/BPLL1_C_FB4	I/O	9	bus4	DQ9	BPLL1_C_IN3/BPLL1_C_FB4	Comp_of_I0B71A	True	K6	pull up
I0B7A	I/O	9	bus4	DQ12		True_of_I0B7B	True	D2	pull up
I0B7B	I/O	9	bus4	DQ12		Comp_of_I0B7A	True	D1	pull up
I0B9A	I/O	9	bus4	DQ12		True_of_I0B9B	True	F4	pull up
I0B9B	I/O	9	bus4	DQ12		Comp_of_I0B9A	True	F3	pull up
I0L37A	I/O	11		none		True_of_I0L37B	True	E14	pull up
I0L37B	I/O	11		none		Comp_of_I0L37A	True	D15	pull up
I0L39A	I/O	11	bus2	none		True_of_I0L39B	True	B16	pull up
I0L39B	I/O	11	bus2	none		Comp_of_I0L39A	True	A16	pull up
I0L41A	I/O	11	bus2	none		True_of_I0L41B	True	C15	pull up
I0L41B	I/O	11	bus2	none		Comp_of_I0L41A	True	A15	pull up
I0L43A/GCLKT_19/LPLL0_T_IN3/LPLL0_T_FB4	I/O	11	bus2	none	GCLKT_19/LPLL0_T_IN3/LPLL0_T_FB4	True_of_I0L43B	True	F12	pull up
I0L43B/GCLKC_19/LPLL0_C_IN3/LPLL0_C_FB4	I/O	11	bus2	none	GCLKC_19/LPLL0_C_IN3/LPLL0_C_FB4	Comp_of_I0L43A	True	E12	pull up
I0L45A/GCLKT_18	I/O	11	bus2	none	GCLKT_18	True_of_I0L45B	True	G9	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
IOL45B/GCLKC_18	I/O	11	bus2	none	GCLKC_18	Comp_of_IOL45A	True	G11	pull up
IOL48A/GCLKT_17	I/O	10	bus2	none	GCLKT_17	True_of_IOL48B	True	G8	pull up
IOL48B/GCLKC_17	I/O	10	bus2	none	GCLKC_17	Comp_of_IOL48A	True	E8	pull up
IOL50A/GCLKT_16	I/O	10	bus2	none	GCLKT_16	True_of_IOL50B	True	E6	pull up
IOL50B/GCLKC_16	I/O	10	bus2	none	GCLKC_16	Comp_of_IOL50A	True	F7	pull up
IOL52A	I/O	10	bus2	none		True_of_IOL52B	True	B3	pull up
IOL52B	I/O	10	bus2	none		Comp_of_IOL52A	True	A3	pull up
IOL66A/LPLL1_T_IN3/LPLL1_T_FB4/PUDC_B	I/O	10	bus2	none	LPLL1_T_IN3/LPLL1_T_FB4/PUDC_B	True_of_IOL66B	True	B2	pull down
IOL66B/LPLL1_C_IN3/LPLL1_C_FB4	I/O	10	bus2	none	LPLL1_C_IN3/LPLL1_C_FB4	Comp_of_IOL66A	True	A2	pull up
IOR11A/TPLL1_T_IN3/TPLL1_T_FB4	I/O	3	bus3	none	TPLL1_T_IN3/TPLL1_T_FB4	True_of_IOR11B	True	U16	pull up
IOR11B/TPLL1_C_IN3/TPLL1_C_FB4	I/O	3	bus3	none	TPLL1_C_IN3/TPLL1_C_FB4	Comp_of_IOR11A	True	V16	pull up
IOR13A	I/O	3	bus3	none		True_of_IOR13B	True	U15	pull up
IOR13B	I/O	3	bus3	none		Comp_of_IOR13A	True	V15	pull up
IOR15A/D11	I/O	3	bus3	none	D11	True_of_IOR15B	True	U13	pull up
IOR15B/D12	I/O	3	bus3	none	D12	Comp_of_IOR15A	True	V13	pull up
IOR17A/MODE1	I/O	3	bus3	none	MODE1	True_of_IOR17B	True	N12	pull up
IOR17B/D10	I/O	3	bus3	none	D10	Comp_of_IOR17A	True	P12	pull up
IOR1A/TCK	I/O	12		none	TCK	True_of_IOR1B	none	A17	pull up
IOR1B/TDI	I/O	12		none	TDI	Comp_of_IOR1A	none	F13	pull up
IOR20A/D01/MI2	I/O	4	bus4	DQ4	D01/MI2	True_of_IOR20B	True	T14	pull up
IOR20B/D02/MI3	I/O	4	bus4	DQ4	D02/MI3	Comp_of_IOR20A	True	V14	pull up
IOR22A	I/O	4	bus4	DQ4		True_of_IOR22B	True	T12	pull up
IOR22B	I/O	4	bus4	DQ4		Comp_of_IOR22A	True	V12	pull up
IOR24A	I/O	4	bus4	DQ4		True_of_IOR24B	True	M11	pull up
IOR24B	I/O	4	bus4	DQ4		Comp_of_IOR24A	True	N11	pull up
IOR26A	I/O	4	bus4	DQS4		True_of_IOR26B	True	N10	pull up
IOR26B	I/O	4	bus4	DQS4		Comp_of_IOR26A	True	P11	pull up
IOR30A	I/O	4	bus4	DQ4		True_of_IOR30B	True	R11	pull up
IOR30B	I/O	4	bus4	DQ4		Comp_of_IOR30A	True	T11	pull up
IOR32A	I/O	4		DQ4		True_of_IOR32B	True	U11	pull up
IOR32B	I/O	4		DQ4		Comp_of_IOR32A	True	V11	pull up
IOR34A/GCLKT_4/D13	I/O	4		DQ4	GCLKT_4/D13	True_of_IOR34B	True	U10	pull up
IOR34B/GCLKC_4/EMCCLK	I/O	4		DQ4	GCLKC_4/EMCCLK	Comp_of_IOR34A	True	V10	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
IOR36A/GCLKT_5/RPLL0_T_IN4/RPLL0_T_FB3	I/O	4	bus4	DQ4	GCLKT_5/RPLL0_T_IN4/RPLL0_T_FB3	True_of_IOR36B	True	R10	pull up
IOR36B/GCLKC_5/RPLL0_C_IN4/RPLL0_C_FB3	I/O	4	bus4	DQ4	GCLKC_5/RPLL0_C_IN4/RPLL0_C_FB3	Comp_of_IOR36A	True	T10	pull up
IOR39A/GCLKT_6/RPLL0_T_IN3/RPLL0_T_FB4	I/O	5	bus4	none	GCLKT_6/RPLL0_T_IN3/RPLL0_T_FB4	True_of_IOR39B	True	T9	pull up
IOR39B/GCLKC_6/RPLL0_C_IN3/RPLL0_C_FB4	I/O	5	bus4	none	GCLKC_6/RPLL0_C_IN3/RPLL0_C_FB4	Comp_of_IOR39A	True	V9	pull up
IOR3A/TMS	I/O	12		none	TMS	True_of_IOR3B	none	B18	pull up
IOR3B/TDO	I/O	12		none	TDO	Comp_of_IOR3A	none	D16	pull up
IOR41A/GCLKT_7/D14	I/O	5	bus4	none	GCLKT_7/D14	True_of_IOR41B	True	R8	pull up
IOR41B/GCLKC_7/D15	I/O	5	bus4	none	GCLKC_7/D15	Comp_of_IOR41A	True	T8	pull up
IOR43A	I/O	5		none		True_of_IOR43B	True	M10	pull up
IOR43B	I/O	5		none		Comp_of_IOR43A	True	N9	pull up
IOR45A	I/O	5	bus4	none		True_of_IOR45B	True	M8	pull up
IOR45B	I/O	5	bus4	none		Comp_of_IOR45A	True	N8	pull up
IOR47A/DONE	I/O	5	bus4	none	DONE		none	V17	pull down
IOR48A	I/O	5	bus4	none		True_of_IOR48B	True	U8	pull up
IOR48B	I/O	5	bus4	none		Comp_of_IOR48A	True	V8	pull up
IOR50A	I/O	5	bus4	none		True_of_IOR50B	True	N7	pull up
IOR50B	I/O	5	bus4	none		Comp_of_IOR50A	True	P8	pull up
IOR52A	I/O	5	bus4	none		True_of_IOR52B	True	U7	pull up
IOR52B	I/O	5	bus4	none		Comp_of_IOR52A	True	V7	pull up
IOR54A	I/O	5	bus4	none		True_of_IOR54B	True	R7	pull up
IOR54B	I/O	5	bus4	none		Comp_of_IOR54A	True	T7	pull up
IOR56A/RECONFIG_N	I/O	5	bus4	none	RECONFIG_N		none	V2	pull up
IOR57A	I/O	5	bus4	DQ5		True_of_IOR57B	True	T6	pull up
IOR57B	I/O	5	bus4	DQ5		Comp_of_IOR57A	True	V6	pull up
IOR59A	I/O	5	bus4	DQ5		True_of_IOR59B	True	N6	pull up
IOR59B	I/O	5	bus4	DQ5		Comp_of_IOR59A	True	P7	pull up
IOR5A/CCLK	I/O	3		none	CCLK	True_of_IOR5B	True	R15	pull up
IOR5B/MODE0	I/O	3		none	MODE0	Comp_of_IOR5A	True	T15	pull down
IOR61A/RPLL1_T_IN4/RPLL1_T_FB3/D07/SDA	I/O	5	bus4	DQ5	RPLL1_T_IN4/RPLL1_T_FB3/D07/SDA	True_of_IOR61B	True	R5	pull up
IOR61B/RPLL1_C_IN4/RPLL1_C_FB3/RDWR_B/SCL	I/O	5	bus4	DQ5	RPLL1_C_IN4/RPLL1_C_FB3/RDWR_B/SCL	Comp_of_IOR61A	True	T5	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
IOR63A/D03/SI/SSI0	I/O	5		DQS5	D03/SI/SSI0	True_of_IOR63B	True	U5	pull up
IOR63B/D04/SSPI_CS_N	I/O	5		DQS5	D04/SSPI_CS_N	Comp_of_IOR63A	True	V5	pull up
IOR66A/RPLL1_T_IN3/RPLL1_T_FB4/D05/SO/SSI1	I/O	5	bus4	DQ5	RPLL1_T_IN3/RPLL1_T_FB4/D05/SO/SSI1	True_of_IOR66B	True	R3	pull up
IOR66B/RPLL1_C_IN3/RPLL1_C_FB4/D06/SSPI_CLK	I/O	5	bus4	DQ5	RPLL1_C_IN3/RPLL1_C_FB4/D06/SSPI_CLK	Comp_of_IOR66A	True	T3	pull up
IOR68A	I/O	5	bus4	DQ5		True_of_IOR68B	True	T4	pull up
IOR68B	I/O	5	bus4	DQ5		Comp_of_IOR68A	True	V4	pull up
IOR70A/D08/SSPI_WPN/SSI2	I/O	5		DQ5	D08/SSPI_WPN/SSI2	True_of_IOR70B	True	N5	pull up
IOR70B/D09/CLKHOLD_N/SSI3	I/O	5		DQ5	D09/CLKHOLD_N/SSI3	Comp_of_IOR70A	True	P6	pull up
IOR72A/RPLL1_T_IN2/READY	I/O	5		DQ5	RPLL1_T_IN2/READY	True_of_IOR72B	True	U3	pull down
IOR72B/RPLL1_C_IN2/CSO_B/MCS_N	I/O	5		DQ5	RPLL1_C_IN2/CSO_B/MCS_N	Comp_of_IOR72A	True	V3	pull up
IOR7A	I/O	3		none		True_of_IOR7B	True	P13	pull up
IOR7B	I/O	3		none		Comp_of_IOR7A	True	R16	pull up
IOR9A/D00/DIN/MISO/MI1	I/O	3		none	D00/DIN/MISO/MI1	True_of_IOR9B	True	R13	pull up
IOR9B/CSI_B/MOSI/MI0	I/O	3		none	CSI_B/MOSI/MI0	Comp_of_IOR9A	True	T13	pull up
IOT102A	I/O	1	bus2	DQ1		True_of_IOT102B	True	J16	pull up
IOT102B	I/O	1	bus2	DQ1		Comp_of_IOT102A	True	J18	pull up
IOT104A	I/O	1	bus2	DQ1		True_of_IOT104B	True	L17	pull up
IOT104B	I/O	1	bus2	DQ1		Comp_of_IOT104A	True	L18	pull up
IOT106A	I/O	1	bus2	DQ1		True_of_IOT106B	True	M16	pull up
IOT106B	I/O	1	bus2	DQ1		Comp_of_IOT106A	True	M18	pull up
IOT108A/GCLKT_1	I/O	1	bus2	DQ2	GCLKT_1	True_of_IOT108B	True	L15	pull up
IOT108B/GCLKC_1	I/O	1	bus2	DQ1	GCLKC_1	Comp_of_IOT108A	True	L16	pull up
IOT111A	I/O	2	bus1	DQ2		True_of_IOT111B	True	J13	pull up
IOT111B	I/O	2	bus1	DQ2		Comp_of_IOT111A	True	K14	pull up
IOT113A/GCLKT_2/Q0REF_T_IN1	I/O	2	bus1	DQ2	GCLKT_2/Q0REF_T_IN1	True_of_IOT113B	True	K15	pull up
IOT113B/GCLKC_2/Q0REF_C_IN1	I/O	2	bus1	DQ2	GCLKC_2/Q0REF_C_IN1	Comp_of_IOT113A	True	K16	pull up
IOT115A/GCLKT_3	I/O	2	bus1	DQ2	GCLKT_3	True_of_IOT115B	True	L12	pull up
IOT115B/GCLKC_3	I/O	2	bus1	DQ2	GCLKC_3	Comp_of_IOT115A	True	L13	pull up
IOT117A	I/O	2	bus1	DQS2		True_of_IOT117B	True	N15	pull up
IOT117B	I/O	2	bus1	DQS2		Comp_of_IOT117A	True	N16	pull up
IOT120A	I/O	2	bus1	DQ2		True_of_IOT120B	True	N17	pull up
IOT120B	I/O	2	bus1	DQ2		Comp_of_IOT120A	True	N18	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
IOT122A	I/O	2	bus1	DQ2		True_of_IOT122B	True	P17	pull up
IOT122B	I/O	2	bus1	DQ2		Comp_of_IOT122A	True	P18	pull up
IOT124A	I/O	2	bus1	DQ2		True_of_IOT124B	True	T17	pull up
IOT124B	I/O	2	bus1	DQ2		Comp_of_IOT124A	True	T18	pull up
IOT126A	I/O	2	bus1	DQ2		True_of_IOT126B	True	U17	pull up
IOT126B	I/O	2	bus1	DQ2		Comp_of_IOT126A	True	U18	pull up
IOT129A	I/O	2	bus0	DQ3		True_of_IOT129B	True	L14	pull up
IOT129B	I/O	2	bus0	DQ2		Comp_of_IOT129A	True	M13	pull up
IOT131A	I/O	2	bus0	DQ3		True_of_IOT131B	True	M14	pull up
IOT131B	I/O	2	bus0	DQ3		Comp_of_IOT131A	True	N14	pull up
IOT142A	I/O	2	bus0	DQ3		True_of_IOT142B	True	P15	pull up
IOT142B/DOUT	I/O	2	bus0	DQ3	DOUT	Comp_of_IOT142A	True	P16	pull up
IOT74A	I/O	1	bus2	DQ0		True_of_IOT74B	True	H12	pull up
IOT74B	I/O	1	bus2	DQ0		Comp_of_IOT74A	True	G13	pull up
IOT76A/TPLL0_T_IN2	I/O	1	bus2	DQ0	TPLL0_T_IN2	True_of_IOT76B	True	F14	pull up
IOT76B/TPLL0_C_IN2	I/O	1	bus2	DQ0	TPLL0_C_IN2	Comp_of_IOT76A	True	G14	pull up
IOT78A	I/O	1	bus2	DQ0		True_of_IOT78B	True	C17	pull up
IOT78B	I/O	1	bus2	DQ0		Comp_of_IOT78A	True	C18	pull up
IOT80A/TPLL0_T_IN3/TPLL0_T_FB4	I/O	1	bus2	DQ0	TPLL0_T_IN3/TPLL0_T_FB4	True_of_IOT80B	True	F15	pull up
IOT80B/TPLL0_C_IN3/TPLL0_C_FB4	I/O	1	bus2	DQ0	TPLL0_C_IN3/TPLL0_C_FB4	Comp_of_IOT80A	True	F16	pull up
IOT82A/TPLL0_T_IN4/TPLL0_T_FB3/PCIE_HOST_RSTN	I/O	1	bus2	DQ0	TPLL0_T_IN4/TPLL0_T_FB3/PCIE_HOST_RSTN	True_of_IOT82B	True	H13	pull up
IOT82B/TPLL0_C_IN4/TPLL0_C_FB3/PCIE_HOST_RSTN	I/O	1	bus2	DQ0	TPLL0_C_IN4/TPLL0_C_FB3/PCIE_HOST_RSTN	Comp_of_IOT82A	True	H14	pull up
IOT85A	I/O	1		DQS0		True_of_IOT85B	True	D17	pull up
IOT85B	I/O	1		DQS0		Comp_of_IOT85A	True	D18	pull up
IOT87A	I/O	1	bus2	DQ0		True_of_IOT87B	True	E16	pull up
IOT87B	I/O	1	bus2	DQ0		Comp_of_IOT87A	True	E18	pull up
IOT89A/Q0REF_T_IN0	I/O	1	bus2	DQ0	Q0REF_T_IN0	True_of_IOT89B	True	F17	pull up
IOT89B/Q0REF_C_IN0	I/O	1	bus2	DQ0	Q0REF_C_IN0	Comp_of_IOT89A	True	F18	pull up
IOT91A	I/O	1	bus2	DQ1		True_of_IOT91B	True	K12	pull up
IOT91B	I/O	1	bus2	DQ1		Comp_of_IOT91A	True	K13	pull up
IOT93A	I/O	1	bus2	DQ1		True_of_IOT93B	True	H15	pull up
IOT93B	I/O	1	bus2	DQ1		Comp_of_IOT93A	True	H16	pull up
IOT95A	I/O	1	bus2	DQ1		True_of_IOT95B	True	G16	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
IOT95B	I/O	1	bus2	DQ1		Comp_of_IOT95A	True	G18	pull up
IOT97A/GCLKT_0	I/O	1	bus2	DQ1	GCLKT_0	True_of_IOT97B	True	H17	pull up
IOT97B/GCLKC_0	I/O	1	bus2	DQ1	GCLKC_0	Comp_of_IOT97A	True	H18	pull up
IOT99A	I/O	1	bus2	DQS1		True_of_IOT99B	True	K17	pull up
IOT99B	I/O	1	bus2	DQS1		Comp_of_IOT99A	True	K18	pull up
Q0_ATEST_O	DIO	Q0		none			none	E7	pull up
Q0_LN0_RXM_I	DIO	Q0		none			none	C5	pull up
Q0_LN0_RXP_I	DIO	Q0		none			none	D5	pull up
Q0_LN0_TXM_O	DIO	Q0		none			none	A4	pull up
Q0_LN0_TXP_O	DIO	Q0		none			none	B4	pull up
Q0_LN1_RXM_I	DIO	Q0		none			none	C7	pull up
Q0_LN1_RXP_I	DIO	Q0		none			none	D7	pull up
Q0_LN1_TXM_O	DIO	Q0		none			none	A6	pull up
Q0_LN1_TXP_O	DIO	Q0		none			none	B6	pull up
Q0_LN2_RXM_I	DIO	Q0		none			none	C11	pull up
Q0_LN2_RXP_I	DIO	Q0		none			none	D11	pull up
Q0_LN2_TXM_O	DIO	Q0		none			none	A12	pull up
Q0_LN2_TXP_O	DIO	Q0		none			none	B12	pull up
Q0_LN3_RXM_I	DIO	Q0		none			none	C13	pull up
Q0_LN3_RXP_I	DIO	Q0		none			none	D13	pull up
Q0_LN3_TXM_O	DIO	Q0		none			none	A14	pull up
Q0_LN3_TXP_O	DIO	Q0		none			none	B14	pull up
Q0_REFCLKM_0	DIO	Q0		none			none	A8	pull up
Q0_REFCLKM_1	DIO	Q0		none			none	C9	pull up
Q0_REFCLKM_2	DIO	Q0		none			none	A10	pull up
Q0_REFCLKM_3	DIO	Q0		none			none	E10	pull up
Q0_REFCLKP_0	DIO	Q0		none			none	B8	pull up
Q0_REFCLKP_1	DIO	Q0		none			none	D9	pull up
Q0_REFCLKP_2	DIO	Q0		none			none	B10	pull up
Q0_REFCLKP_3	DIO	Q0		none			none	F10	pull up
VCC	Power	N/A						H9	pull up
VCC	Power	N/A						J10	pull up
VCC	Power	N/A						K9	pull up
VCC	Power	N/A						J8	pull up
VCC	Power	N/A						M12	pull up
VCC	Power	N/A						G7	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
VCC	Power	N/A						K11	pull up
VCC	Power	N/A						L8	pull up
VCC	Power	N/A						M7	pull up
VCC	Power	N/A						L10	pull up
VCC	Power	N/A						H11	pull up
VDDA_Q0	Power	N/A						E11	pull up
VDDA_Q0	Power	N/A						B7	pull up
VDDA_Q0	Power	N/A						B11	pull up
VDDA_Q0	Power	N/A						D10	pull up
VCCIO1	Power	N/A						E17	pull up
VCCIO1	Power	N/A						G15	pull up
VCCIO1	Power	N/A						J14	pull up
VCCIO10	Power	N/A						F8	pull up
VCCIO10	Power	N/A						C3	pull up
VCCIO11	Power	N/A						B15	pull up
VCCIO11	Power	N/A						G12	pull up
VCCIO12/VCCIO6/VCCIO7/VCCIO8/ VCCX/VDDHA_Q0	Power	N/A						P10	pull up
VCCIO12/VCCIO6/VCCIO7/VCCIO8/ VCCX/VDDHA_Q0	Power	N/A						M9	pull up
VCCIO12/VCCIO6/VCCIO7/VCCIO8/ VCCX/VDDHA_Q0	Power	N/A						D14	pull up
VCCIO12/VCCIO6/VCCIO7/VCCIO8/ VCCX/VDDHA_Q0	Power	N/A						B1	pull up
VCCIO12/VCCIO6/VCCIO7/VCCIO8/ VCCX/VDDHA_Q0	Power	N/A						P14	pull up
VCCIO12/VCCIO6/VCCIO7/VCCIO8/ VCCX/VDDHA_Q0	Power	N/A						G10	pull up
VCCIO12/VCCIO6/VCCIO7/VCCIO8/ VCCX/VDDHA_Q0	Power	N/A						J12	pull up
VCCIO12/VCCIO6/VCCIO7/VCCIO8/ VCCX/VDDHA_Q0	Power	N/A						D4	pull up
VCCIO12/VCCIO6/VCCIO7/VCCIO8/ VCCX/VDDHA_Q0	Power	N/A						K7	pull up
VCCIO12/VCCIO6/VCCIO7/VCCIO8/ VCCX/VDDHA_Q0	Power	N/A						B17	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
VCCIO12/VCCIO6/VCCIO7/VCCIO8/ VCCX/VDDHA_Q0	Power	N/A						P5	pull up
VCCIO2	Power	N/A						J17	pull up
VCCIO2	Power	N/A						M15	pull up
VCCIO2	Power	N/A						R17	pull up
VCCIO3	Power	N/A						R12	pull up
VCCIO3	Power	N/A						U14	pull up
VCCIO4	Power	N/A						P9	pull up
VCCIO4	Power	N/A						U9	pull up
VCCIO5	Power	N/A						R6	pull up
VCCIO5	Power	N/A						U4	pull up
VCCIO9	Power	N/A						R2	pull up
VCCIO9	Power	N/A						G4	pull up
VCCIO9	Power	N/A						E2	pull up
VCCIO9	Power	N/A						M4	pull up
VCCIO9	Power	N/A						J2	pull up
VCCIO9	Power	N/A						J5	pull up
V_EFUSE	Power	N/A						R4	pull up
VSS	Ground	N/A						A1	pull up
VSS	Ground	N/A						A11	pull up
VSS	Ground	N/A						A18	pull up
VSS	Ground	N/A						A7	pull up
VSS	Ground	N/A						A9	pull up
VSS	Ground	N/A						B13	pull up
VSS	Ground	N/A						B5	pull up
VSS	Ground	N/A						B9	pull up
VSS	Ground	N/A						C10	pull up
VSS	Ground	N/A						C12	pull up
VSS	Ground	N/A						C14	pull up
VSS	Ground	N/A						C16	pull up
VSS	Ground	N/A						C4	pull up
VSS	Ground	N/A						C6	pull up
VSS	Ground	N/A						D8	pull up
VSS	Ground	N/A						E13	pull up
VSS	Ground	N/A						E15	pull up
VSS	Ground	N/A						F11	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
VSS	Ground	N/A						F9	pull up
VSS	Ground	N/A						G17	pull up
VSS	Ground	N/A						G2	pull up
VSS	Ground	N/A						G5	pull up
VSS	Ground	N/A						H10	pull up
VSS	Ground	N/A						H8	pull up
VSS	Ground	N/A						J11	pull up
VSS	Ground	N/A						J15	pull up
VSS	Ground	N/A						J4	pull up
VSS	Ground	N/A						J9	pull up
VSS	Ground	N/A						K10	pull up
VSS	Ground	N/A						K8	pull up
VSS	Ground	N/A						L11	pull up
VSS	Ground	N/A						L9	pull up
VSS	Ground	N/A						M17	pull up
VSS	Ground	N/A						M2	pull up
VSS	Ground	N/A						M6	pull up
VSS	Ground	N/A						N13	pull up
VSS	Ground	N/A						R1	pull up
VSS	Ground	N/A						R14	pull up
VSS	Ground	N/A						R18	pull up
VSS	Ground	N/A						R9	pull up
VSS	Ground	N/A						T16	pull up
VSS	Ground	N/A						U12	pull up
VSS	Ground	N/A						U6	pull up
VSS	Ground	N/A						V1	pull up
VSS	Ground	N/A						V18	pull up
NC	N/A	N/A						C8	pull up
NC	N/A	N/A						E9	pull up
NC	N/A	N/A						E5	pull up
NC	N/A	N/A						A5	pull up
NC	N/A	N/A						A13	pull up
NC	N/A	N/A						D6	pull up
NC	N/A	N/A						D12	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
BANK11 True LVDS Pair									
IOL23A	I/O	11		none		True_of_IOL23B	True	B1	pull up
IOL23B	I/O	11		none		Comp_of_IOL23A	True	A1	pull up
IOL25A	I/O	11		none		True_of_IOL25B	True	C2	pull up
IOL25B	I/O	11		none		Comp_of_IOL25A	True	B2	pull up
IOL27A	I/O	11	bus2	none		True_of_IOL27B	True	E1	pull up
IOL27B	I/O	11	bus2	none		Comp_of_IOL27A	True	D1	pull up
IOL29A	I/O	11	bus2	none		True_of_IOL29B	True	E2	pull up
IOL29B	I/O	11	bus2	none		Comp_of_IOL29A	True	D2	pull up
IOL31A	I/O	11	bus2	none		True_of_IOL31B	True	G1	pull up
IOL31B	I/O	11	bus2	none		Comp_of_IOL31A	True	F1	pull up
IOL33A	I/O	11	bus2	none		True_of_IOL33B	True	F3	pull up
IOL33B	I/O	11	bus2	none		Comp_of_IOL33A	True	E3	pull up
IOL35A/LPLL0_T_IN0/LPLL0_T_FB0	I/O	11	bus2	none	LPLL0_T_IN0/LPLL0_T_FB0	True_of_IOL35B	True	K1	pull up
IOL35B/LPLL0_C_IN0/LPLL0_C_FB0	I/O	11	bus2	none	LPLL0_C_IN0/LPLL0_C_FB0	Comp_of_IOL35A	True	J1	pull up
IOL37A	I/O	11		none		True_of_IOL37B	True	H2	pull up
IOL37B	I/O	11		none		Comp_of_IOL37A	True	G2	pull up
IOL39A	I/O	11	bus2	none		True_of_IOL39B	True	K2	pull up
IOL39B	I/O	11	bus2	none		Comp_of_IOL39A	True	J2	pull up
IOL41A	I/O	11	bus2	none		True_of_IOL41B	True	J5	pull up
IOL41B	I/O	11	bus2	none		Comp_of_IOL41A	True	H5	pull up
IOL43A/GCLKT_19/LPLL0_T_IN1/LPLL0_T_FB1	I/O	11	bus2	none	GCLKT_19/LPLL0_T_IN1/LPLL0_T_FB1	True_of_IOL43B	True	H3	pull up
IOL43B/GCLKC_19/LPLL0_C_IN1/LPLL0_C_FB1	I/O	11	bus2	none	GCLKC_19/LPLL0_C_IN1/LPLL0_C_FB1	Comp_of_IOL43A	True	G3	pull up
IOL45A/GCLKT_18	I/O	11	bus2	none	GCLKT_18	True_of_IOL45B	True	H4	pull up
IOL45B/GCLKC_18	I/O	11	bus2	none	GCLKC_18	Comp_of_IOL45A	True	G4	pull up
BANK10 True LVDS Pair									
IOL48A/GCLKT_17	I/O	10	bus2	none	GCLKT_17	True_of_IOL48B	True	K4	pull up
IOL48B/GCLKC_17	I/O	10	bus2	none	GCLKC_17	Comp_of_IOL48A	True	J4	pull up
IOL50A/GCLKT_16	I/O	10	bus2	none	GCLKT_16	True_of_IOL50B	True	L3	pull up
IOL50B/GCLKC_16	I/O	10	bus2	none	GCLKC_16	Comp_of_IOL50A	True	K3	pull up
IOL52A	I/O	10	bus2	none		True_of_IOL52B	True	M1	pull up
IOL52B	I/O	10	bus2	none		Comp_of_IOL52A	True	L1	pull up
IOL54A	I/O	10	bus2	none		True_of_IOL54B	True	M3	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
IOL54B	I/O	10	bus2	none		Comp_of_IOL54A	True	M2	pull up
IOL57A	I/O	10	bus2	none		True_of_IOL57B	True	K6	pull up
IOL57B	I/O	10	bus2	none		Comp_of_IOL57A	True	J6	pull up
IOL59A	I/O	10	bus2	none		True_of_IOL59B	True	L5	pull up
IOL59B	I/O	10	bus2	none		Comp_of_IOL59A	True	L4	pull up
IOL61A	I/O	10	bus2	none		True_of_IOL61B	True	N4	pull up
IOL61B	I/O	10	bus2	none		Comp_of_IOL61A	True	N3	pull up
IOL63A/LPLL1_T_IN0	I/O	10		none	LPLL1_T_IN0	True_of_IOL63B	True	R1	pull up
IOL63B/LPLL1_C_IN0	I/O	10		none	LPLL1_C_IN0	Comp_of_IOL63A	True	P1	pull up
IOL66A/LPLL1_T_IN1/LPLL1_T_FB0	I/O	10	bus2	none	LPLL1_T_IN1/LPLL1_T_FB0	True_of_IOL66B	True	P5	pull up
IOL66B/LPLL1_C_IN1/LPLL1_C_FB0	I/O	10	bus2	none	LPLL1_C_IN1/LPLL1_C_FB0	Comp_of_IOL66A	True	P4	pull up
IOL68A/LPLL1_T_IN2/LPLL1_T_FB0	I/O	10	bus2	none	LPLL1_T_IN2/LPLL1_T_FB0	True_of_IOL68B	True	P2	pull up
IOL68B/LPLL1_C_IN2/LPLL1_C_FB0	I/O	10	bus2	none	LPLL1_C_IN2/LPLL1_C_FB0	Comp_of_IOL68A	True	N2	pull up
IOL70A/BPLL0_T_IN0	I/O	10	bus2	none	BPLL0_T_IN0	True_of_IOL70B	True	M6	pull up
IOL70B/BPLL0_C_IN0	I/O	10	bus2	none	BPLL0_C_IN0	Comp_of_IOL70A	True	M5	pull up
IOL72A	I/O	10		none		True_of_IOL72B	True	P6	pull up
IOL72B	I/O	10		none		Comp_of_IOL72A	True	N5	pull up
BANK9 True LVDS Pair									
IOB12A	I/O	9		DQS12		True_of_IOB12B	True	R3	pull up
IOB12B	I/O	9		DQS12		Comp_of_IOB12A	True	R2	pull up
IOB14A	I/O	9		DQ12		True_of_IOB14B	True	W1	pull up
IOB14B	I/O	9		DQ12		Comp_of_IOB14A	True	Y1	pull up
IOB16A	I/O	9	bus4	DQ12		True_of_IOB16B	True	U3	pull up
IOB16B	I/O	9	bus4	DQ12		Comp_of_IOB16A	True	V3	pull up
IOB18A	I/O	9	bus4	DQ11		True_of_IOB18B	True	AB12	pull up
IOB18B	I/O	9	bus4	DQ11		Comp_of_IOB18A	True	AB11	pull up
IOB1A	I/O	9	bus4	DQ12		True_of_IOB1B	True	AA9	pull up
IOB1B	I/O	9	bus4	DQ12		Comp_of_IOB1A	True	AA10	pull up
IOB20A	I/O	9	bus4	DQ11		True_of_IOB20B	True	AA11	pull up
IOB20B	I/O	9	bus4	DQ11		Comp_of_IOB20A	True	Y11	pull up
IOB22A	I/O	9	bus4	DQ11		True_of_IOB22B	True	AA1	pull up
IOB22B	I/O	9	bus4	DQ11		Comp_of_IOB22A	True	AB1	pull up
IOB24A	I/O	9	bus4	DQ11		True_of_IOB24B	True	AB3	pull up
IOB24B	I/O	9	bus4	DQ11		Comp_of_IOB24A	True	AB2	pull up
IOB26A	I/O	9	bus4	DQS11		True_of_IOB26B	True	Y3	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
IOB26B	I/O	9	bus4	DQS11		Comp_of_IOB26A	True	AA3	pull up
IOB29A	I/O	9	bus4	DQ11		True_of_IOB29B	True	AA5	pull up
IOB29B	I/O	9	bus4	DQ11		Comp_of_IOB29A	True	AB5	pull up
IOB31A/GCLKT_14	I/O	9	bus4	DQ11	GCLKT_14	True_of_IOB31B	True	Y4	pull up
IOB31B/GCLKC_14	I/O	9	bus4	DQ11	GCLKC_14	Comp_of_IOB31A	True	AA4	pull up
IOB33A/GCLKT_15	I/O	9	bus4	DQ11	GCLKT_15	True_of_IOB33B	True	V4	pull up
IOB33B/GCLKC_15	I/O	9	bus4	DQ11	GCLKC_15	Comp_of_IOB33A	True	W4	pull up
IOB35A	I/O	9	bus4	DQ10		True_of_IOB35B	True	W11	pull up
IOB35B	I/O	9	bus4	DQ11		Comp_of_IOB35A	True	W10	pull up
IOB38A/GCLKT_13	I/O	9	bus4	DQ10	GCLKT_13	True_of_IOB38B	True	R4	pull up
IOB38B/GCLKC_13	I/O	9	bus4	DQ10	GCLKC_13	Comp_of_IOB38A	True	T4	pull up
IOB3A/BPLL0_T_IN1/BPLL0_T_FB0	I/O	9	bus4	DQ12	BPLL0_T_IN1/BPLL0_T_FB0	True_of_IOB3B	True	T3	pull up
IOB3B/BPLL0_C_IN1/BPLL0_C_FB0	I/O	9	bus4	DQ12	BPLL0_C_IN1/BPLL0_C_FB0	Comp_of_IOB3A	True	AB10	pull up
IOB40A/GCLKT_12	I/O	9	bus4	DQ10	GCLKT_12	True_of_IOB40B	True	T5	pull up
IOB40B/GCLKC_12	I/O	9	bus4	DQ10	GCLKC_12	Comp_of_IOB40A	True	U5	pull up
IOB42A	I/O	9	bus4	DQ10		True_of_IOB42B	True	U6	pull up
IOB42B	I/O	9	bus4	DQ10		Comp_of_IOB42A	True	V5	pull up
IOB44A	I/O	9	bus4	DQS10		True_of_IOB44B	True	W6	pull up
IOB44B	I/O	9	bus4	DQS10		Comp_of_IOB44A	True	W5	pull up
IOB48A	I/O	9	bus4	DQ10		True_of_IOB48B	True	R6	pull up
IOB48B	I/O	9	bus4	DQ10		Comp_of_IOB48A	True	T6	pull up
IOB50A	I/O	9	bus4	DQ10/DQS_910		True_of_IOB50B	True	Y6	pull up
IOB50B	I/O	9	bus4	DQ10/DQS_910		Comp_of_IOB50A	True	AA6	pull up
IOB52A	I/O	9	bus4	DQ10		True_of_IOB52B	True	Y12	pull up
IOB52B	I/O	9	bus4	DQ10		Comp_of_IOB52A	True	W12	pull up
IOB54A	I/O	9	bus4	DQ10		True_of_IOB54B	True	AB13	pull up
IOB54B	I/O	9	bus4	DQ10		Comp_of_IOB54A	True	AA13	pull up
IOB56A	I/O	9	bus4	DQ9		True_of_IOB56B	True	Y13	pull up
IOB56B	I/O	9	bus4	DQ10		Comp_of_IOB56A	True	Y14	pull up
IOB58A	I/O	9	bus4	DQ9		True_of_IOB58B	True	V7	pull up
IOB58B	I/O	9	bus4	DQ9		Comp_of_IOB58A	True	W7	pull up
IOB5A/BPLL0_T_IN2/BPLL0_T_FB1	I/O	9	bus4	DQ12	BPLL0_T_IN2/BPLL0_T_FB1	True_of_IOB5B	True	T1	pull up
IOB5B/BPLL0_C_IN2/BPLL0_C_FB1	I/O	9	bus4	DQ12	BPLL0_C_IN2/BPLL0_C_FB1	Comp_of_IOB5A	True	U1	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
IOB60A	I/O	9	bus4	DQ9		True_of_IOB60B	True	AB7	pull up
IOB60B	I/O	9	bus4	DQ9		Comp_of_IOB60A	True	AB6	pull up
IOB62A	I/O	9	bus4	DQS9		True_of_IOB62B	True	V9	pull up
IOB62B	I/O	9	bus4	DQS9		Comp_of_IOB62A	True	V8	pull up
IOB65A	I/O	9	bus4	DQ9		True_of_IOB65B	True	AA8	pull up
IOB65B	I/O	9	bus4	DQ9		Comp_of_IOB65A	True	AB8	pull up
IOB67A/BPLL1_T_IN0	I/O	9	bus4	DQ9	BPLL1_T_IN0	True_of_IOB67B	True	Y8	pull up
IOB67B/BPLL1_C_IN0	I/O	9	bus4	DQ9	BPLL1_C_IN0	Comp_of_IOB67A	True	Y7	pull up
IOB69A	I/O	9	bus4	DQ9		True_of_IOB69B	True	W9	pull up
IOB69B	I/O	9	bus4	DQ9		Comp_of_IOB69A	True	Y9	pull up
IOB71A/BPLL1_T_IN1/BPLL1_T_FB1	I/O	9	bus4	DQ9	BPLL1_T_IN1/BPLL1_T_FB1	True_of_IOB71B	True	U7	pull up
IOB71B/BPLL1_C_IN1/BPLL1_C_FB1	I/O	9	bus4	DQ9	BPLL1_C_IN1/BPLL1_C_FB1	Comp_of_IOB71A	True	V10	pull up
IOB7A	I/O	9	bus4	DQ12		True_of_IOB7B	True	U2	pull up
IOB7B	I/O	9	bus4	DQ12		Comp_of_IOB7A	True	V2	pull up
IOB9A	I/O	9	bus4	DQ12		True_of_IOB9B	True	W2	pull up
IOB9B	I/O	9	bus4	DQ12		Comp_of_IOB9A	True	Y2	pull up
BANK8 True LVDS Pair									
IOB75A	I/O	8	bus4	DQ8		True_of_IOB75B	True	U17	pull up
IOB75B	I/O	8	bus4	DQ8		Comp_of_IOB75A	True	U18	pull up
IOB77A/BPLL1_T_IN2/BPLL1_T_FB0	I/O	8	bus4	DQ8	BPLL1_T_IN2/BPLL1_T_FB0	True_of_IOB77B	True	AA18	pull up
IOB77B/BPLL1_C_IN2/BPLL1_C_FB0	I/O	8	bus4	DQ8	BPLL1_C_IN2/BPLL1_C_FB0	Comp_of_IOB77A	True	AB18	pull up
IOB79A/CSI_B	I/O	8	bus4	DQ8	CSI_B	True_of_IOB79B	True	V17	pull up
IOB79B	I/O	8	bus4	DQ8		Comp_of_IOB79A	True	W17	pull up
IOB81A/GCLKT_11	I/O	8		DQ8	GCLKT_11	True_of_IOB81B	True	V18	pull up
IOB81B/GCLKC_11	I/O	8		DQ8	GCLKC_11	Comp_of_IOB81A	True	V19	pull up
IOB85A/RDWR_B	I/O	8		DQS8	RDWR_B	True_of_IOB85B	True	AA19	pull up
IOB85B/CSO_B/DOUT	I/O	8		DQS8	CSO_B/DOUT	Comp_of_IOB85A	True	AB20	pull up
IOB87A/GCLKT_10	I/O	8	bus4	DQ8	GCLKT_10	True_of_IOB87B	True	Y18	pull up
IOB87B/GCLKC_10	I/O	8	bus4	DQ8	GCLKC_10	Comp_of_IOB87A	True	Y19	pull up
IOB89A	I/O	8	bus4	DQ8		True_of_IOB89B	True	AB15	pull up
IOB89B	I/O	8	bus4	DQ8		Comp_of_IOB89A	True	AA14	pull up
IOB91A	I/O	8	bus4	DQ8		True_of_IOB91B	True	W14	pull up
IOB91B	I/O	8	bus4	DQ8		Comp_of_IOB91A	True	V14	pull up
IOB93A/GCLKT_9	I/O	8	bus4	DQ7	GCLKT_9	True_of_IOB93B	True	W19	pull up
IOB93B/GCLKC_9	I/O	8	bus4	DQ7	GCLKC_9	Comp_of_IOB93A	True	W20	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
IOB95A/GCLKT_8	I/O	8	bus4	DQ7	GCLKT_8	True_of_IOB95B	True	U20	pull up
IOB95B/GCLKC_8	I/O	8	bus4	DQ7	GCLKC_8	Comp_of_IOB95A	True	V20	pull up
IOB97A/D14	I/O	8	bus4	DQ7	D14	True_of_IOB97B	True	AB21	pull up
IOB97B/D15	I/O	8	bus4	DQ7	D15	Comp_of_IOB97A	True	AB22	pull up
IOB99A	I/O	8	bus4	DQS7		True_of_IOB99B	True	Y21	pull up
IOB99B/D13	I/O	8	bus4	DQS7	D13	Comp_of_IOB99A	True	Y22	pull up
BANK7 True LVDS Pair									
IOB102A/D11	I/O	7	bus4	DQ7	D11	True_of_IOB102B	True	AA20	pull up
IOB102B/D12	I/O	7	bus4	DQ7	D12	Comp_of_IOB102A	True	AA21	pull up
IOB104A/D09	I/O	7	bus4	DQ7	D09	True_of_IOB104B	True	W21	pull up
IOB104B/D10	I/O	7	bus4	DQ7	D10	Comp_of_IOB104A	True	W22	pull up
IOB106A	I/O	7	bus4	DQ7		True_of_IOB106B	True	AA16	pull up
IOB106B	I/O	7	bus4	DQ7		Comp_of_IOB106A	True	AA15	pull up
IOB108A	I/O	7	bus4	DQ7		True_of_IOB108B	True	AB17	pull up
IOB108B	I/O	7	bus4	DQ7		Comp_of_IOB108A	True	AB16	pull up
IOB111A/MCS_N	I/O	7	bus4	DQ6	MCS_N	True_of_IOB111B	True	T19	pull up
IOB111B/D08	I/O	7	bus4	DQ6	D08	Comp_of_IOB111A	True	T20	pull up
IOB113A/D06	I/O	7	bus4	DQ6	D06	True_of_IOB113B	True	P19	pull up
IOB113B/D07	I/O	7	bus4	DQ6	D07	Comp_of_IOB113A	True	R19	pull up
IOB115A/D04	I/O	7	bus4	DQ6	D04	True_of_IOB115B	True	T21	pull up
IOB115B/D05	I/O	7	bus4	DQ6	D05	Comp_of_IOB115A	True	U21	pull up
IOB117A/PUDC_B	I/O	7	bus4	DQS6	PUDC_B	True_of_IOB117B	True	U22	pull down
IOB117B/EMCCLK	I/O	7	bus4	DQS6	EMCCLK	Comp_of_IOB117A	True	V22	pull up
IOB120A/D02/MI2	I/O	7	bus4	DQ6	D02/MI2	True_of_IOB120B	True	P21	pull up
IOB120B/D03/MI3	I/O	7	bus4	DQ6	D03/MI3	Comp_of_IOB120A	True	R21	pull up
IOB122A	I/O	7	bus4	DQ6		True_of_IOB122B	True	Y17	pull up
IOB122B	I/O	7	bus4	DQ6		Comp_of_IOB122A	True	Y16	pull up
IOB124A	I/O	7	bus4	DQ6		True_of_IOB124B	True	P20	pull up
IOB124B	I/O	7	bus4	DQ6		Comp_of_IOB124A	True	V13	pull up
IOB126A	I/O	7	bus4	DQ6		True_of_IOB126B	True	W16	pull up
IOB126B	I/O	7	bus4	DQ6		Comp_of_IOB126A	True	W15	pull up
BANK6 True LVDS Pair									
IOB128A/D00/MOSI/MI0	I/O	6	bus4	DQ6	D00/MOSI/MI0	True_of_IOB128B	True	P22	pull up
IOB128B/D01/DIN/MISO/MI1	I/O	6	bus4	DQ6	D01/DIN/MISO/MI1	Comp_of_IOB128A	True	R22	pull up
IOB130A	I/O	6	bus4	none		True_of_IOB130B	True	V15	pull up
IOB130B	I/O	6	bus4	none		Comp_of_IOB130A	True	U15	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
I0B132A	I/O	6	bus4	none		True_of_I0B132B	True	T15	pull up
I0B132B	I/O	6	bus4	none		Comp_of_I0B132A	True	T14	pull up
I0B134A	I/O	6	bus4	none		True_of_I0B134B	True	U16	pull up
I0B134B	I/O	6	bus4	none		Comp_of_I0B134A	True	N15	pull up
I0B136A	I/O	6	bus4	none		True_of_I0B136B	True	P16	pull up
I0B136B	I/O	6	bus4	none		Comp_of_I0B136A	True	R17	pull up
I0B138A	I/O	6	bus4	none		True_of_I0B138B	True	N13	pull up
I0B138B	I/O	6	bus4	none		Comp_of_I0B138A	True	N14	pull up
I0B140A	I/O	6	bus4	none		True_of_I0B140B	True	N17	pull up
I0B140B	I/O	6	bus4	none		Comp_of_I0B140A	True	P17	pull up
I0B142A	I/O	6	bus4	none		True_of_I0B142B	True	P15	pull up
I0B142B	I/O	6	bus4	none		Comp_of_I0B142A	True	R16	pull up
I0B144A	I/O	6		none		True_of_I0B144B	True	R18	pull up
I0B144B	I/O	6		none		Comp_of_I0B144A	True	T18	pull up
I0B146A	I/O	6		none		True_of_I0B146B	True	P14	pull up
I0B146B	I/O	6		none		Comp_of_I0B146A	True	R14	pull up
BANK5 True LVDS Pair									
I0R39A/RPLL0_T_IN0/RPLL0_T_FB1	I/O	5	bus4	none	RPLL0_T_IN0/RPLL0_T_FB1	True_of_I0R39B	True	K17	pull up
I0R39B/RPLL0_C_IN0/RPLL0_C_FB1	I/O	5	bus4	none	RPLL0_C_IN0/RPLL0_C_FB1	Comp_of_I0R39A	True	J17	pull up
I0R41A/GCLKT_7	I/O	5	bus4	none	GCLKT_7	True_of_I0R41B	True	J19	pull up
I0R41B/GCLKC_7	I/O	5	bus4	none	GCLKC_7	Comp_of_I0R41A	True	H19	pull up
I0R43A	I/O	5		none		True_of_I0R43B	True	H20	pull up
I0R43B	I/O	5		none		Comp_of_I0R43A	True	G20	pull up
I0R45A	I/O	5	bus4	none		True_of_I0R45B	True	J22	pull up
I0R45B	I/O	5	bus4	none		Comp_of_I0R45A	True	H22	pull up
I0R48A	I/O	5	bus4	none		True_of_I0R48B	True	L14	pull up
I0R48B	I/O	5	bus4	none		Comp_of_I0R48A	True	L15	pull up
I0R50A	I/O	5	bus4	none		True_of_I0R50B	True	M13	pull up
I0R50B	I/O	5	bus4	none		Comp_of_I0R50A	True	L13	pull up
I0R52A	I/O	5	bus4	none		True_of_I0R52B	True	M15	pull up
I0R52B	I/O	5	bus4	none		Comp_of_I0R52A	True	M16	pull up
I0R54A/GCLKT_5	I/O	5	bus4	none	GCLKT_5	True_of_I0R54B	True	J20	pull up
I0R54B/GCLKC_5	I/O	5	bus4	none	GCLKC_5	Comp_of_I0R54A	True	J21	pull up
I0R57A/GCLKT_6	I/O	5	bus4	DQ5	GCLKT_6	True_of_I0R57B	True	K18	pull up
I0R57B/GCLKC_6	I/O	5	bus4	DQ5	GCLKC_6	Comp_of_I0R57A	True	K19	pull up
I0R59A	I/O	5	bus4	DQ5		True_of_I0R59B	True	K21	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
IOR59B	I/O	5	bus4	DQ5		Comp_of_IOR59A	True	K22	pull up
IOR61A/SDA/GCLKT_4/RPLL1_T_IN2/RPLL1_T_FB0	I/O	5	bus4	DQ5	SDA/GCLKT_4/RPLL1_T_IN2/RPLL1_T_FB0	True_of_IOR61B	True	L19	pull up
IOR61B/SCL/GCLKC_4/RPLL1_C_IN2/RPLL1_C_FB0	I/O	5	bus4	DQ5	SCL/GCLKC_4/RPLL1_C_IN2/RPLL1_C_FB0	Comp_of_IOR61A	True	L20	pull up
IOR63A/SI/SSIO	I/O	5		DQS5	SI/SSIO	True_of_IOR63B	True	N22	pull up
IOR63B/SSPI_CS_N	I/O	5		DQS5	SSPI_CS_N	Comp_of_IOR63A	True	M22	pull up
IOR66A/SO/SS11/RPLL1_T_IN1/RPLL1_T_FB1	I/O	5	bus4	DQ5	SO/SS11/RPLL1_T_IN1/RPLL1_T_FB1	True_of_IOR66B	True	M18	pull up
IOR66B/SSPI_CLK/RPLL1_C_IN1/RPLL1_C_FB1	I/O	5	bus4	DQ5	SSPI_CLK/RPLL1_C_IN1/RPLL1_C_FB1	Comp_of_IOR66A	True	L18	pull up
IOR68A	I/O	5	bus4	DQ5		True_of_IOR68B	True	M21	pull up
IOR68B	I/O	5	bus4	DQ5		Comp_of_IOR68A	True	L21	pull up
IOR70A/SSPI_WPN/SSI2	I/O	5		DQ5	SSPI_WPN/SSI2	True_of_IOR70B	True	N20	pull up
IOR70B/CLKHOLD_N/SSI3	I/O	5		DQ5	CLKHOLD_N/SSI3	Comp_of_IOR70A	True	M20	pull up
IOR72A/RPLL1_T_IN0	I/O	5		DQ5	RPLL1_T_IN0	True_of_IOR72B	True	N18	pull up
IOR72B/RPLL1_C_IN0	I/O	5		DQ5	RPLL1_C_IN0	Comp_of_IOR72A	True	N19	pull up
BANK4 True LVDS Pair									
IOR20A	I/O	4	bus4	DQ4		True_of_IOR20B	True	H13	pull up
IOR20B	I/O	4	bus4	DQ4		Comp_of_IOR20A	True	G13	pull up
IOR22A	I/O	4	bus4	DQ4		True_of_IOR22B	True	J15	pull up
IOR22B	I/O	4	bus4	DQ4		Comp_of_IOR22A	True	H15	pull up
IOR24A	I/O	4	bus4	DQ4		True_of_IOR24B	True	G15	pull up
IOR24B	I/O	4	bus4	DQ4		Comp_of_IOR24A	True	G16	pull up
IOR26A	I/O	4	bus4	DQS4		True_of_IOR26B	True	J14	pull up
IOR26B	I/O	4	bus4	DQS4		Comp_of_IOR26A	True	H14	pull up
IOR30A	I/O	4	bus4	DQ4		True_of_IOR30B	True	G17	pull up
IOR30B	I/O	4	bus4	DQ4		Comp_of_IOR30A	True	G18	pull up
IOR32A	I/O	4		DQ4		True_of_IOR32B	True	H17	pull up
IOR32B	I/O	4		DQ4		Comp_of_IOR32A	True	H18	pull up
IOR34A	I/O	4		DQ4		True_of_IOR34B	True	L16	pull up
IOR34B	I/O	4		DQ4		Comp_of_IOR34A	True	K16	pull up
IOR36A/RPLL0_T_IN1/RPLL0_C_FB0	I/O	4	bus4	DQ4	RPLL0_T_IN1/RPLL0_T_FB0	True_of_IOR36B	True	K13	pull up
IOR36B/RPLL0_C_IN1/RPLL0_C_FB0	I/O	4	bus4	DQ4	RPLL0_C_IN1/RPLL0_C_FB0	Comp_of_IOR36A	True	K14	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
BANK3 True LVDS Pair									
IOR11A/TPLL1_T_IN0/TPLL1_T_FB1	I/O	3	bus3	none	TPLL1_T_IN0/TPLL1_T_FB1	True_of_IOR11B	True	U8	pull up
IOR11B/MODE2/TPLL1_C_IN0/TPLL1_C_FB1	I/O	3	bus3	none	MODE2/TPLL1_C_IN0/TPLL1_C_FB1	Comp_of_IOR11A	True	U9	pull up
IOR5A/CCLK	I/O	3		none	CCLK	True_of_IOR5B	True	L12	pull up
IOR5B/READY	I/O	3		none	READY	Comp_of_IOR5A	True	U12	pull down
IOR7A/MODE1	I/O	3		none	MODE1	True_of_IOR7B	True	U10	pull up
IOR7B/MODE0	I/O	3		none	MODE0	Comp_of_IOR7A	True	U11	pull down
IOR9A/DONE	I/O	3		none	DONE	True_of_IOR9B	True	G11	pull down
IOR9B/RECONFIG_N	I/O	3		none	RECONFIG_N	Comp_of_IOR9A	True	N12	pull up
BANK2 True LVDS Pair									
IOT113A/SERDES_Q0CLKIN1	I/O	2	bus1	DQ2	SERDES_Q0CLKIN1	True_of_IOT113B	True	A18	pull up
IOT113B	I/O	2	bus1	DQ2		Comp_of_IOT113A	True	A19	pull up
IOT115A/GCLKT_3	I/O	2	bus1	DQ2	GCLKT_3	True_of_IOT115B	True	C18	pull up
IOT115B/GCLKC_3	I/O	2	bus1	DQ2	GCLKC_3	Comp_of_IOT115A	True	C19	pull up
IOT117A	I/O	2	bus1	DQS2		True_of_IOT117B	True	F18	pull up
IOT117B	I/O	2	bus1	DQS2		Comp_of_IOT117A	True	E18	pull up
IOT120A/GCLKT_2	I/O	2	bus1	DQ2	GCLKT_2	True_of_IOT120B	True	E19	pull up
IOT120B/GCLKC_2	I/O	2	bus1	DQ2	GCLKC_2	Comp_of_IOT120A	True	D19	pull up
IOT122A	I/O	2	bus1	DQ2		True_of_IOT122B	True	B20	pull up
IOT122B	I/O	2	bus1	DQ2		Comp_of_IOT122A	True	A20	pull up
IOT124A	I/O	2	bus1	DQ2		True_of_IOT124B	True	F19	pull up
IOT124B	I/O	2	bus1	DQ2		Comp_of_IOT124A	True	F20	pull up
IOT131A	I/O	2	bus0	DQ3		True_of_IOT131B	True	D20	pull up
IOT131B	I/O	2	bus0	DQ3		Comp_of_IOT131A	True	C20	pull up
IOT133A	I/O	2	bus0	DQ3		True_of_IOT133B	True	C22	pull up
IOT133B	I/O	2	bus0	DQ3		Comp_of_IOT133A	True	B22	pull up
IOT135A	I/O	2	bus0	DQS3		True_of_IOT135B	True	B21	pull up
IOT135B	I/O	2	bus0	DQS3		Comp_of_IOT135A	True	A21	pull up
IOT138A	I/O	2	bus0	DQ3		True_of_IOT138B	True	E21	pull up
IOT138B	I/O	2	bus0	DQ3		Comp_of_IOT138A	True	D21	pull up
IOT140A	I/O	2	bus0	DQ3		True_of_IOT140B	True	E22	pull up
IOT140B	I/O	2	bus0	DQ3		Comp_of_IOT140A	True	D22	pull up
IOT142A	I/O	2	bus0	DQ3		True_of_IOT142B	True	F21	pull up
IOT144A	I/O	2	bus0	DQ3		True_of_IOT144B	True	G21	pull up
IOT144B	I/O	2	bus0	DQ3		Comp_of_IOT144A	True	G22	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	PG484A	配置过程中的IO上下拉状态
IOT146A/TPLL1_T_IN1/TPLL1_T_FB0	I/O	2	bus0	DQ3	TPLL1_T_IN1/TPLL1_T_FB0	True_of_IOT146B	True	T16	pull up
BANK1 True LVDS Pair									
IOT104A/GCLKT_1	I/O	1	bus2	DQ1	GCLKT_1	True_of_IOT104B	True	B17	pull up
IOT104B/GCLKC_1	I/O	1	bus2	DQ1	GCLKC_1	Comp_of_IOT104A	True	B18	pull up
IOT106A/GCLKT_0	I/O	1	bus2	DQ1	GCLKT_0	True_of_IOT106B	True	D17	pull up
IOT106B/GCLKC_0	I/O	1	bus2	DQ1	GCLKC_0	Comp_of_IOT106A	True	C17	pull up
IOT78A	I/O	1	bus2	DQ0		True_of_IOT78B	True	E13	pull up
IOT78B	I/O	1	bus2	DQ0		Comp_of_IOT78A	True	E14	pull up
IOT80A/TPLL0_T_IN1/TPLL0_T_FB1	I/O	1	bus2	DQ0	TPLL0_T_IN1/TPLL0_T_FB1	True_of_IOT80B	True	F13	pull up
IOT80B/TPLL0_C_IN1/TPLL0_C_FB1	I/O	1	bus2	DQ0	TPLL0_C_IN1/TPLL0_C_FB1	Comp_of_IOT80A	True	F14	pull up
IOT82A/PCIE_HOST_RSTN/TPLL0_T_IN2/TPLL0_T_FB0	I/O	1	bus2	DQ0	PCIE_HOST_RSTN/TPLL0_T_IN2/TPLL0_T_FB0	True_of_IOT82B	True	D14	pull up
IOT82B/TPLL0_C_IN2/TPLL0_C_FB0	I/O	1	bus2	DQ0	TPLL0_C_IN2/TPLL0_C_FB0	Comp_of_IOT82A	True	D15	pull up
IOT85A	I/O	1		DQS0		True_of_IOT85B	True	C14	pull up
IOT85B	I/O	1		DQS0		Comp_of_IOT85A	True	C15	pull up
IOT87A	I/O	1	bus2	DQ0		True_of_IOT87B	True	E16	pull up
IOT87B	I/O	1	bus2	DQ0		Comp_of_IOT87A	True	D16	pull up
IOT89A/Q0REF_T_IN0	I/O	1	bus2	DQ0	Q0REF_T_IN0	True_of_IOT89B	True	F16	pull up
IOT89B/Q0REF_C_IN0	I/O	1	bus2	DQ0	Q0REF_C_IN0	Comp_of_IOT89A	True	E17	pull up
IOT91A	I/O	1	bus2	DQ1		True_of_IOT91B	True	F15	pull up
IOT93A	I/O	1	bus2	DQ1		True_of_IOT93B	True	C13	pull up
IOT93B	I/O	1	bus2	DQ1		Comp_of_IOT93A	True	B13	pull up
IOT95A	I/O	1	bus2	DQ1		True_of_IOT95B	True	A13	pull up
IOT95B	I/O	1	bus2	DQ1		Comp_of_IOT95A	True	A14	pull up
IOT97A	I/O	1	bus2	DQ1		True_of_IOT97B	True	B15	pull up
IOT97B	I/O	1	bus2	DQ1		Comp_of_IOT97A	True	B16	pull up
IOT99A	I/O	1	bus2	DQS1		True_of_IOT99B	True	A15	pull up
IOT99B	I/O	1	bus2	DQS1		Comp_of_IOT99A	True	A16	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225	配置过程中的IO上下拉状态
BANK11 True LVDS Pair									
IOL41A	I/O	11	bus2	none		True_of_IOL41B	True	F5	pull down
IOL41B	I/O	11	bus2	none		Comp_of_IOL41A	True	G5	pull down
IOL45A/GCLKT_18	I/O	11	bus2	none	GCLKT_18	True_of_IOL45B	True	K4	pull down
IOL45B/GCLKC_18	I/O	11	bus2	none	GCLKC_18	Comp_of_IOL45A	True	K3	pull down
BANK10 True LVDS Pair									
IOL48A/GCLKT_17	I/O	10	bus2	none	GCLKT_17	True_of_IOL48B	True	H5	pull down
IOL48B/GCLKC_17	I/O	10	bus2	none	GCLKC_17	Comp_of_IOL48A	True	H6	pull down
IOL50A/GCLKT_16	I/O	10	bus2	none	GCLKT_16	True_of_IOL50B	True	K5	pull down
IOL50B/GCLKC_16	I/O	10	bus2	none	GCLKC_16	Comp_of_IOL50A	True	J5	pull down
IOL66A/PUDC_B/LPLL1_T_IN1/LPLL1_T_FB0	I/O	10	bus2	none	PUDC_B/LPLL1_T_IN1/LPLL1_T_FB0	True_of_IOL66B	True	K1	pull down
BANK9 True LVDS Pair									
IOB12A	I/O	9		DQS12		True_of_IOB12B	True	R3	pull down
IOB12B	I/O	9		DQS12		Comp_of_IOB12A	True	P3	pull down
IOB14A	I/O	9		DQ12		True_of_IOB14B	True	N4	pull down
IOB14B	I/O	9		DQ12		Comp_of_IOB14A	True	M4	pull down
IOB16A	I/O	9	bus4	DQ12		True_of_IOB16B	True	P5	pull down
IOB16B	I/O	9	bus4	DQ12		Comp_of_IOB16A	True	N5	pull down
IOB18A/GCLKT_12	I/O	9	bus4	DQ11	GCLKT_12	True_of_IOB18B	True	M5	pull down
IOB18B/GCLKC_12	I/O	9	bus4	DQ11	GCLKC_12	Comp_of_IOB18A	True	L5	pull down
IOB1A	I/O	9	bus4	DQ12		True_of_IOB1B	True	L1	pull down
IOB1B	I/O	9	bus4	DQ12		Comp_of_IOB1A	True	L2	pull down
IOB20A	I/O	9	bus4	DQ11		True_of_IOB20B	True	R4	pull down
IOB20B	I/O	9	bus4	DQ11		Comp_of_IOB20A	True	R5	pull down
IOB22A	I/O	9	bus4	DQ11		True_of_IOB22B	True	R6	pull down
IOB22B	I/O	9	bus4	DQ11		Comp_of_IOB22A	True	R7	pull down
IOB24A	I/O	9	bus4	DQ11		True_of_IOB24B	True	N6	pull down
IOB24B	I/O	9	bus4	DQ11		Comp_of_IOB24A	True	M6	pull down
IOB26A	I/O	9	bus4	DQS11		True_of_IOB26B	True	R8	pull down
IOB26B	I/O	9	bus4	DQS11		Comp_of_IOB26A	True	R9	pull down
IOB29A	I/O	9	bus4	DQ11		True_of_IOB29B	True	P7	pull down
IOB29B	I/O	9	bus4	DQ11		Comp_of_IOB29A	True	N7	pull down
IOB31A/GCLKT_14	I/O	9	bus4	DQ11	GCLKT_14	True_of_IOB31B	True	N8	pull down
IOB31B/GCLKC_14	I/O	9	bus4	DQ11	GCLKC_14	Comp_of_IOB31A	True	M8	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225	配置过程中的IO上下拉状态
IOB33A/GCLKT_15	I/O	9	bus4	DQ11	GCLKT_15	True_of_IOB33B	True	P9	pull down
IOB33B/GCLKC_15	I/O	9	bus4	DQ11	GCLKC_15	Comp_of_IOB33A	True	N9	pull down
IOB35A	I/O	9	bus4	DQ10		True_of_IOB35B	True	L6	pull down
IOB35B	I/O	9	bus4	DQ11		Comp_of_IOB35A	True	L7	pull down
IOB38A/GCLKT_13	I/O	9	bus4	DQ10	GCLKT_13	True_of_IOB38B	True	M9	pull down
IOB38B/GCLKC_13	I/O	9	bus4	DQ10	GCLKC_13	Comp_of_IOB38A	True	L8	pull down
IOB3A/BPLL0_T_IN1/BPLL0_T_FB0	I/O	9	bus4	DQ12	BPLL0_T_IN1/BPLL0_T_FB0	True_of_IOB3B	True	M3	pull down
IOB3B/BPLL0_C_IN1/BPLL0_C_FB0	I/O	9	bus4	DQ12	BPLL0_C_IN1/BPLL0_C_FB0	Comp_of_IOB3A	True	L3	pull down
IOB5A/BPLL0_T_IN2/BPLL0_T_FB1	I/O	9	bus4	DQ12	BPLL0_T_IN2/BPLL0_T_FB1	True_of_IOB5B	True	M1	pull down
IOB5B/BPLL0_C_IN2/BPLL0_C_FB1	I/O	9	bus4	DQ12	BPLL0_C_IN2/BPLL0_C_FB1	Comp_of_IOB5A	True	N2	pull down
IOB7A	I/O	9	bus4	DQ12		True_of_IOB7B	True	N1	pull down
IOB7B	I/O	9	bus4	DQ12		Comp_of_IOB7A	True	P1	pull down
IOB9A	I/O	9	bus4	DQ12		True_of_IOB9B	True	R2	pull down
IOB9B	I/O	9	bus4	DQ12		Comp_of_IOB9A	True	P2	pull down
BANK8 True LVDS Pair									
IOB75A	I/O	8	bus4	DQ8		True_of_IOB75B	True	L9	pull down
IOB75B	I/O	8	bus4	DQ8		Comp_of_IOB75A	True	K8	pull down
IOB77A/BPLL1_T_IN2/BPLL1_T_FB0	I/O	8	bus4	DQ8	BPLL1_T_IN2/BPLL1_T_FB0	True_of_IOB77B	True	R10	pull down
IOB77B/BPLL1_C_IN2/BPLL1_C_FB0	I/O	8	bus4	DQ8	BPLL1_C_IN2/BPLL1_C_FB0	Comp_of_IOB77A	True	R11	pull down
IOB79A	I/O	8	bus4	DQ8		True_of_IOB79B	True	P11	pull down
IOB79B	I/O	8	bus4	DQ8		Comp_of_IOB79A	True	N11	pull down
IOB81A/GCLKT_11	I/O	8		DQ8	GCLKT_11	True_of_IOB81B	True	N10	pull down
IOB81B/GCLKC_11	I/O	8		DQ8	GCLKC_11	Comp_of_IOB81A	True	M10	pull down
IOB85A	I/O	8		DQS8		True_of_IOB85B	True	R13	pull down
IOB85B	I/O	8		DQS8		Comp_of_IOB85A	True	R12	pull down
IOB87A/GCLKT_10	I/O	8	bus4	DQ8	GCLKT_10	True_of_IOB87B	True	N12	pull down
IOB87B/GCLKC_10	I/O	8	bus4	DQ8	GCLKC_10	Comp_of_IOB87A	True	M11	pull down
IOB89A	I/O	8	bus4	DQ8		True_of_IOB89B	True	L10	pull down
IOB89B	I/O	8	bus4	DQ8		Comp_of_IOB89A	True	K10	pull down
IOB91A	I/O	8	bus4	DQ8		True_of_IOB91B	True	R14	pull down
IOB91B	I/O	8	bus4	DQ8		Comp_of_IOB91A	True	P13	pull down
BANK5 True LVDS Pair									
IOR41A/D14/GCLKT_7	I/O	5	bus4	none	D14/GCLKT_7	True_of_IOR41B	True	H11	pull down
IOR41B/D15/GCLKC_7	I/O	5	bus4	none	D15/GCLKC_7	Comp_of_IOR41A	True	H10	pull down
IOR57A/GCLKT_6	I/O	5	bus4	DQ5	GCLKT_6	True_of_IOR57B	True	J13	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225	配置过程中的IO上下拉状态
IOR57B/GCLKC_6	I/O	5	bus4	DQ5	GCLKC_6	Comp_of_IOR57A	True	J14	pull down
IOR59A	I/O	5	bus4	DQ5		True_of_IOR59B	True	J11	pull down
IOR59B	I/O	5	bus4	DQ5		Comp_of_IOR59A	True	K11	pull down
IOR61A/D07/SDA/RPLL1_T_IN2/RPLL1_T_FB0	I/O	5	bus4	DQ5	D07/SDA/RPLL1_T_IN2/RPLL1_T_FB0	True_of_IOR61B	True	K12	pull down
IOR61B/RDWR_B/SCL/RPLL1_C_IN2/RPLL1_C_FB0	I/O	5	bus4	DQ5	RDWR_B/SCL/RPLL1_C_IN2/RPLL1_C_FB0	Comp_of_IOR61A	True	K13	pull down
IOR63A/D03/SI/SSI0	I/O	5		DQS5	D03/SI/SSI0	True_of_IOR63B	True	L13	pull down
IOR63B/D04/SSPI_CS_N	I/O	5		DQS5	D04/SSPI_CS_N	Comp_of_IOR63A	True	L14	pull down
IOR66A/D05/SO/SSI1/RPLL1_T_IN1/RPLL1_T_FB1	I/O	5	bus4	DQ5	D05/SO/SSI1/RPLL1_T_IN1/RPLL1_T_FB1	True_of_IOR66B	True	L15	pull down
IOR66B/D06/SSPI_CLK/RPLL1_C_IN1/RPLL1_C_FB1	I/O	5	bus4	DQ5	D06/SSPI_CLK/RPLL1_C_IN1/RPLL1_C_FB1	Comp_of_IOR66A	True	M15	pull down
IOR68A	I/O	5	bus4	DQ5		True_of_IOR68B	True	L12	pull down
IOR68B	I/O	5	bus4	DQ5		Comp_of_IOR68A	True	M13	pull down
IOR70A/D08/SSPI_WPN/SSI2	I/O	5		DQ5	D08/SSPI_WPN/SSI2	True_of_IOR70B	True	N14	pull down
IOR70B/D09/CLKHOLD_N/SSI3	I/O	5		DQ5	D09/CLKHOLD_N/SSI3	Comp_of_IOR70A	True	N15	pull down
IOR72A/READY/RPLL1_T_IN0	I/O	5		DQ5	READY/RPLL1_T_IN0	True_of_IOR72B	True	P14	pull down
IOR72B/CSO_B/MCS_N/RPLL1_C_IN0	I/O	5		DQ5	CSO_B/MCS_N/RPLL1_C_IN0	Comp_of_IOR72A	True	P15	pull down
BANK4 True LVDS Pair									
IOR20A/D01/MI2	I/O	4	bus4	DQ4	D01/MI2	True_of_IOR20B	True	G13	pull down
IOR20B/D02/MI3	I/O	4	bus4	DQ4	D02/MI3	Comp_of_IOR20A	True	G14	pull down
IOR34A/D13	I/O	4		DQ4	D13	True_of_IOR34B	True	H15	pull down
IOR34B/EMCCLK	I/O	4		DQ4	EMCCLK	Comp_of_IOR34A	True	J15	pull down
IOR36A/GCLKT_5/RPLL0_T_IN1/RPLL0_C_FB0	I/O	4	bus4	DQ4	GCLKT_5/RPLL0_T_IN1/RPLL0_C_FB0	True_of_IOR36B	True	G12	pull down
IOR36B/GCLKC_5/RPLL0_C_IN1/RPLL0_C_FB0	I/O	4	bus4	DQ4	GCLKC_5/RPLL0_C_IN1/RPLL0_C_FB0	Comp_of_IOR36A	True	H12	pull down
BANK3 True LVDS Pair									
IOR15A/D11	I/O	3	bus3	none	D11	True_of_IOR15B	True	F11	pull down
IOR15B/D12	I/O	3	bus3	none	D12	Comp_of_IOR15A	True	G11	pull down
IOR17A/MODE1/GCLKT_4	I/O	3	bus3	none	MODE1/GCLKT_4	True_of_IOR17B	True	F15	pull up
IOR17B/D10/GCLKC_4	I/O	3	bus3	none	D10/GCLKC_4	Comp_of_IOR17A	True	G15	pull down
IOR5A/CCLK	I/O	3		none	CCLK	True_of_IOR5B	True	E13	pull down
IOR5B/MODE0	I/O	3		none	MODE0	Comp_of_IOR5A	True	E14	pull down
IOR9A/D00/DIN/MISO/MI1	I/O	3		none	D00/DIN/MISO/MI1	True_of_IOR9B	True	F12	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225	配置过程中的IO上下拉状态
IOR9B/CSI_B/MOSI/MIO	I/O	3		none	CSI_B/MOSI/MIO	Comp_of_IOR9A	True	F13	pull down
BANK2 True LVDS Pair									
IOT142A	I/O	2	bus0	DQ3		True_of_IOT142B	True	B13	pull down
IOT142B/DOUT	I/O	2	bus0	DQ3	DOUT	Comp_of_IOT142A	True	A13	pull up
IOT144A	I/O	2	bus0	DQ3		True_of_IOT144B	True	B14	pull down
IOT144B	I/O	2	bus0	DQ3		Comp_of_IOT144A	True	A14	pull down
IOT146A/TPLL1_T_IN1/TPLL1_T_FB0	I/O	2	bus0	DQ3	TPLL1_T_IN1/TPLL1_T_FB0	True_of_IOT146B	True	D13	pull down
IOT146B/TPLL1_C_IN1/TPLL1_C_FB0	I/O	2	bus0	DQ3	TPLL1_C_IN1/TPLL1_C_FB0	Comp_of_IOT146A	True	C14	pull down
BANK1 True LVDS Pair									
IOT74A	I/O	1	bus2	DQ0		True_of_IOT74B	True	E5	pull down
IOT74B	I/O	1	bus2	DQ0		Comp_of_IOT74A	True	E6	pull down
IOT76A/TPLL0_T_IN0	I/O	1	bus2	DQ0	TPLL0_T_IN0	True_of_IOT76B	True	E7	pull down
IOT76B/TPLL0_C_IN0	I/O	1	bus2	DQ0	TPLL0_C_IN0	Comp_of_IOT76A	True	E8	pull down
IOT78A	I/O	1	bus2	DQ0		True_of_IOT78B	True	F8	pull down
IOT78B	I/O	1	bus2	DQ0		Comp_of_IOT78A	True	E9	pull down
IOT89A/GCLKT_1/Q0REF_T_IN0	I/O	1	bus2	DQ0	GCLKT_1/Q0REF_T_IN0	True_of_IOT89B	True	F10	pull down
IOT89B/GCLKC_1/Q0REF_C_IN0	I/O	1	bus2	DQ0	GCLKC_1/Q0REF_C_IN0	Comp_of_IOT89A	True	E10	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225H	配置过程中的IO上下拉状态
BANK11 True LVDS Pair									
IOL41A	I/O	11	bus2	none		True_of_IOL41B	True	F5	pull down
IOL41B	I/O	11	bus2	none		Comp_of_IOL41A	True	G5	pull down
IOL45A/GCLKT_18	I/O	11	bus2	none	GCLKT_18	True_of_IOL45B	True	K4	pull down
IOL45B/GCLKC_18	I/O	11	bus2	none	GCLKC_18	Comp_of_IOL45A	True	K3	pull down
BANK10 True LVDS Pair									
IOL48A/GCLKT_17	I/O	10	bus2	none	GCLKT_17	True_of_IOL48B	True	H5	pull down
IOL48B/GCLKC_17	I/O	10	bus2	none	GCLKC_17	Comp_of_IOL48A	True	H6	pull down
IOL50A/GCLKT_16	I/O	10	bus2	none	GCLKT_16	True_of_IOL50B	True	K5	pull down
IOL50B/GCLKC_16	I/O	10	bus2	none	GCLKC_16	Comp_of_IOL50A	True	J5	pull down
IOL66A/PUDC_B/LPLL1_T_IN1/LPLL1_T_FB0	I/O	10	bus2	none	PUDC_B/LPLL1_T_IN1/LPLL1_T_FB0	True_of_IOL66B	True	K1	pull down
BANK9 True LVDS Pair									
IOB12A	I/O	9		DQS12		True_of_IOB12B	True	R3	pull down
IOB12B	I/O	9		DQS12		Comp_of_IOB12A	True	P3	pull down
IOB14A	I/O	9		DQ12		True_of_IOB14B	True	N4	pull down
IOB14B	I/O	9		DQ12		Comp_of_IOB14A	True	M4	pull down
IOB16A	I/O	9	bus4	DQ12		True_of_IOB16B	True	P5	pull down
IOB16B	I/O	9	bus4	DQ12		Comp_of_IOB16A	True	N5	pull down
IOB18A/GCLKT_12	I/O	9	bus4	DQ11	GCLKT_12	True_of_IOB18B	True	M5	pull down
IOB18B/GCLKC_12	I/O	9	bus4	DQ11	GCLKC_12	Comp_of_IOB18A	True	L5	pull down
IOB1A	I/O	9	bus4	DQ12		True_of_IOB1B	True	L1	pull down
IOB1B	I/O	9	bus4	DQ12		Comp_of_IOB1A	True	L2	pull down
IOB20A	I/O	9	bus4	DQ11		True_of_IOB20B	True	R4	pull down
IOB20B	I/O	9	bus4	DQ11		Comp_of_IOB20A	True	R5	pull down
IOB22A	I/O	9	bus4	DQ11		True_of_IOB22B	True	R6	pull down
IOB22B	I/O	9	bus4	DQ11		Comp_of_IOB22A	True	R7	pull down
IOB24A	I/O	9	bus4	DQ11		True_of_IOB24B	True	N6	pull down
IOB24B	I/O	9	bus4	DQ11		Comp_of_IOB24A	True	M6	pull down
IOB26A	I/O	9	bus4	DQS11		True_of_IOB26B	True	R8	pull down
IOB26B	I/O	9	bus4	DQS11		Comp_of_IOB26A	True	R9	pull down
IOB29A	I/O	9	bus4	DQ11		True_of_IOB29B	True	P7	pull down
IOB29B	I/O	9	bus4	DQ11		Comp_of_IOB29A	True	N7	pull down
IOB31A/GCLKT_14	I/O	9	bus4	DQ11	GCLKT_14	True_of_IOB31B	True	N8	pull down
IOB31B/GCLKC_14	I/O	9	bus4	DQ11	GCLKC_14	Comp_of_IOB31A	True	M8	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225H	配置过程中的IO上下拉状态
IOB33A/GCLKT_15	I/O	9	bus4	DQ11	GCLKT_15	True_of_IOB33B	True	P9	pull down
IOB33B/GCLKC_15	I/O	9	bus4	DQ11	GCLKC_15	Comp_of_IOB33A	True	N9	pull down
IOB35A	I/O	9	bus4	DQ10		True_of_IOB35B	True	L6	pull down
IOB35B	I/O	9	bus4	DQ11		Comp_of_IOB35A	True	L7	pull down
IOB38A/GCLKT_13	I/O	9	bus4	DQ10	GCLKT_13	True_of_IOB38B	True	M9	pull down
IOB38B/GCLKC_13	I/O	9	bus4	DQ10	GCLKC_13	Comp_of_IOB38A	True	L8	pull down
IOB3A/BPLL0_T_IN1/BPLL0_T_FB0	I/O	9	bus4	DQ12	BPLL0_T_IN1/BPLL0_T_FB0	True_of_IOB3B	True	M3	pull down
IOB3B/BPLL0_C_IN1/BPLL0_C_FB0	I/O	9	bus4	DQ12	BPLL0_C_IN1/BPLL0_C_FB0	Comp_of_IOB3A	True	L3	pull down
IOB5A/BPLL0_T_IN2/BPLL0_T_FB1	I/O	9	bus4	DQ12	BPLL0_T_IN2/BPLL0_T_FB1	True_of_IOB5B	True	M1	pull down
IOB5B/BPLL0_C_IN2/BPLL0_C_FB1	I/O	9	bus4	DQ12	BPLL0_C_IN2/BPLL0_C_FB1	Comp_of_IOB5A	True	N2	pull down
IOB7A	I/O	9	bus4	DQ12		True_of_IOB7B	True	N1	pull down
IOB7B	I/O	9	bus4	DQ12		Comp_of_IOB7A	True	P1	pull down
IOB9A	I/O	9	bus4	DQ12		True_of_IOB9B	True	R2	pull down
IOB9B	I/O	9	bus4	DQ12		Comp_of_IOB9A	True	P2	pull down
BANK8 True LVDS Pair									
IOB75A	I/O	8	bus4	DQ8		True_of_IOB75B	True	L9	pull down
IOB75B	I/O	8	bus4	DQ8		Comp_of_IOB75A	True	K8	pull down
IOB77A/BPLL1_T_IN2/BPLL1_T_FB0	I/O	8	bus4	DQ8	BPLL1_T_IN2/BPLL1_T_FB0	True_of_IOB77B	True	R10	pull down
IOB77B/BPLL1_C_IN2/BPLL1_C_FB0	I/O	8	bus4	DQ8	BPLL1_C_IN2/BPLL1_C_FB0	Comp_of_IOB77A	True	R11	pull down
IOB79A	I/O	8	bus4	DQ8		True_of_IOB79B	True	P11	pull down
IOB79B	I/O	8	bus4	DQ8		Comp_of_IOB79A	True	N11	pull down
IOB81A/GCLKT_11	I/O	8		DQ8	GCLKT_11	True_of_IOB81B	True	N10	pull down
IOB81B/GCLKC_11	I/O	8		DQ8	GCLKC_11	Comp_of_IOB81A	True	M10	pull down
IOB85A	I/O	8		DQS8		True_of_IOB85B	True	R13	pull down
IOB85B	I/O	8		DQS8		Comp_of_IOB85A	True	R12	pull down
IOB87A/GCLKT_10	I/O	8	bus4	DQ8	GCLKT_10	True_of_IOB87B	True	N12	pull down
IOB87B/GCLKC_10	I/O	8	bus4	DQ8	GCLKC_10	Comp_of_IOB87A	True	M11	pull down
IOB89A	I/O	8	bus4	DQ8		True_of_IOB89B	True	L10	pull down
IOB89B	I/O	8	bus4	DQ8		Comp_of_IOB89A	True	K10	pull down
IOB91A	I/O	8	bus4	DQ8		True_of_IOB91B	True	R14	pull down
IOB91B	I/O	8	bus4	DQ8		Comp_of_IOB91A	True	P13	pull down
BANK5 True LVDS Pair									
IOR41A/D14/GCLKT_7	I/O	5	bus4	none	D14/GCLKT_7	True_of_IOR41B	True	H11	pull down
IOR41B/D15/GCLKC_7	I/O	5	bus4	none	D15/GCLKC_7	Comp_of_IOR41A	True	H10	pull down
IOR57A/GCLKT_6	I/O	5	bus4	DQ5	GCLKT_6	True_of_IOR57B	True	J13	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225H	配置过程中的IO上下拉状态
IOR57B/GCLKC_6	I/O	5	bus4	DQ5	GCLKC_6	Comp_of_IOR57A	True	J14	pull down
IOR59A	I/O	5	bus4	DQ5		True_of_IOR59B	True	J11	pull down
IOR59B	I/O	5	bus4	DQ5		Comp_of_IOR59A	True	K11	pull down
IOR61A/D07/SDA/RPLL1_T_IN2/RPLL1_T_FB0	I/O	5	bus4	DQ5	D07/SDA/RPLL1_T_IN2/RPLL1_T_FB0	True_of_IOR61B	True	K12	pull down
IOR61B/RDWR_B/SCL/RPLL1_C_IN2/RPLL1_C_FB0	I/O	5	bus4	DQ5	RDWR_B/SCL/RPLL1_C_IN2/RPLL1_C_FB0	Comp_of_IOR61A	True	K13	pull down
IOR63A/D03/SI/SSI0	I/O	5		DQS5	D03/SI/SSI0	True_of_IOR63B	True	L13	pull down
IOR63B/D04/SSPI_CS_N	I/O	5		DQS5	D04/SSPI_CS_N	Comp_of_IOR63A	True	L14	pull down
IOR66A/D05/SO/SSI1/RPLL1_T_IN1/RPLL1_T_FB1	I/O	5	bus4	DQ5	D05/SO/SSI1/RPLL1_T_IN1/RPLL1_T_FB1	True_of_IOR66B	True	L15	pull down
IOR66B/D06/SSPI_CLK/RPLL1_C_IN1/RPLL1_C_FB1	I/O	5	bus4	DQ5	D06/SSPI_CLK/RPLL1_C_IN1/RPLL1_C_FB1	Comp_of_IOR66A	True	M15	pull down
IOR68A	I/O	5	bus4	DQ5		True_of_IOR68B	True	L12	pull down
IOR68B	I/O	5	bus4	DQ5		Comp_of_IOR68A	True	M13	pull down
IOR70A/D08/SSPI_WPN/SSI2	I/O	5		DQ5	D08/SSPI_WPN/SSI2	True_of_IOR70B	True	N14	pull down
IOR70B/D09/CLKHOLD_N/SSI3	I/O	5		DQ5	D09/CLKHOLD_N/SSI3	Comp_of_IOR70A	True	N15	pull down
IOR72A/READY/RPLL1_T_IN0	I/O	5		DQ5	READY/RPLL1_T_IN0	True_of_IOR72B	True	P14	pull down
IOR72B/CSO_B/MCS_N/RPLL1_C_IN0	I/O	5		DQ5	CSO_B/MCS_N/RPLL1_C_IN0	Comp_of_IOR72A	True	P15	pull down
BANK4 True LVDS Pair									
IOR20A/D01/MI2	I/O	4	bus4	DQ4	D01/MI2	True_of_IOR20B	True	G13	pull down
IOR20B/D02/MI3	I/O	4	bus4	DQ4	D02/MI3	Comp_of_IOR20A	True	G14	pull down
IOR34A/D13	I/O	4		DQ4	D13	True_of_IOR34B	True	H15	pull down
IOR34B/EMCCLK	I/O	4		DQ4	EMCCLK	Comp_of_IOR34A	True	J15	pull down
IOR36A/GCLKT_5/RPLL0_T_IN1/RPLL0_C_FB0	I/O	4	bus4	DQ4	GCLKT_5/RPLL0_T_IN1/RPLL0_C_FB0	True_of_IOR36B	True	G12	pull down
IOR36B/GCLKC_5/RPLL0_C_IN1/RPLL0_C_FB0	I/O	4	bus4	DQ4	GCLKC_5/RPLL0_C_IN1/RPLL0_C_FB0	Comp_of_IOR36A	True	H12	pull down
BANK3 True LVDS Pair									
IOR15A/D11	I/O	3	bus3	none	D11	True_of_IOR15B	True	F11	pull down
IOR15B/D12	I/O	3	bus3	none	D12	Comp_of_IOR15A	True	G11	pull down
IOR17A/MODE1/GCLKT_4	I/O	3	bus3	none	MODE1/GCLKT_4	True_of_IOR17B	True	F15	pull up
IOR17B/D10/GCLKC_4	I/O	3	bus3	none	D10/GCLKC_4	Comp_of_IOR17A	True	G15	pull down
IOR5A/CCLK	I/O	3		none	CCLK	True_of_IOR5B	True	E13	pull down
IOR5B/MODE0	I/O	3		none	MODE0	Comp_of_IOR5A	True	E14	pull down
IOR9A/D00/DIN/MISO/MI1	I/O	3		none	D00/DIN/MISO/MI1	True_of_IOR9B	True	F12	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG225H	配置过程中的IO上下拉状态
IOR9B/CSI_B/MOSI/MIO	I/O	3		none	CSI_B/MOSI/MIO	Comp_of_IOR9A	True	F13	pull down
BANK2 True LVDS Pair									
IOT142A	I/O	2	bus0	DQ3		True_of_IOT142B	True	B13	pull down
IOT142B/DOUT	I/O	2	bus0	DQ3	DOUT	Comp_of_IOT142A	True	A13	pull up
IOT144A	I/O	2	bus0	DQ3		True_of_IOT144B	True	B14	pull down
IOT144B	I/O	2	bus0	DQ3		Comp_of_IOT144A	True	A14	pull down
IOT146A/TPLL1_T_IN1/TPLL1_T_FB0	I/O	2	bus0	DQ3	TPLL1_T_IN1/TPLL1_T_FB0	True_of_IOT146B	True	D13	pull down
IOT146B/TPLL1_C_IN1/TPLL1_C_FB0	I/O	2	bus0	DQ3	TPLL1_C_IN1/TPLL1_C_FB0	Comp_of_IOT146A	True	C14	pull down
BANK1 True LVDS Pair									
IOT74A	I/O	1	bus2	DQ0		True_of_IOT74B	True	E5	pull down
IOT74B	I/O	1	bus2	DQ0		Comp_of_IOT74A	True	E6	pull down
IOT76A/TPLL0_T_IN0	I/O	1	bus2	DQ0	TPLL0_T_IN0	True_of_IOT76B	True	E7	pull down
IOT76B/TPLL0_C_IN0	I/O	1	bus2	DQ0	TPLL0_C_IN0	Comp_of_IOT76A	True	E8	pull down
IOT78A	I/O	1	bus2	DQ0		True_of_IOT78B	True	F8	pull down
IOT78B	I/O	1	bus2	DQ0		Comp_of_IOT78A	True	E9	pull down
IOT89A/GCLKT_1/Q0REF_T_IN0	I/O	1	bus2	DQ0	GCLKT_1/Q0REF_T_IN0	True_of_IOT89B	True	F10	pull down
IOT89B/GCLKC_1/Q0REF_C_IN0	I/O	1	bus2	DQ0	GCLKC_1/Q0REF_C_IN0	Comp_of_IOT89A	True	E10	pull down

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324A	配置过程中的IO上下拉状态
BANK9 True LVDS Pair									
IOB12A	I/O	9		DQS12		True_of_IOB12B	True	R5	Pull up&HighZ
IOB12B	I/O	9		DQS12		Comp_of_IOB12A	True	T5	Pull up&HighZ
IOB14A	I/O	9		DQ12		True_of_IOB14B	True	U6	Pull up&HighZ
IOB14B	I/O	9		DQ12		Comp_of_IOB14A	True	U5	Pull up&HighZ
IOB16A	I/O	9	bus4	DQ12		True_of_IOB16B	True	P6	Pull up&HighZ
IOB16B	I/O	9	bus4	DQ12		Comp_of_IOB16A	True	P5	Pull up&HighZ
IOB18A	I/O	9	bus4	DQ11		True_of_IOB18B	True	U4	Pull up&HighZ
IOB18B	I/O	9	bus4	DQ11		Comp_of_IOB18A	True	V4	Pull up&HighZ
IOB1A	I/O	9	bus4	DQ12		True_of_IOB1B	True	V8	Pull up&HighZ
IOB1B	I/O	9	bus4	DQ12		Comp_of_IOB1A	True	V7	Pull up&HighZ
IOB20A	I/O	9	bus4	DQ11		True_of_IOB20B	True	T4	Pull up&HighZ
IOB20B	I/O	9	bus4	DQ11		Comp_of_IOB20A	True	T3	Pull up&HighZ
IOB22A	I/O	9	bus4	DQ11		True_of_IOB22B	True	V3	Pull up&HighZ
IOB22B	I/O	9	bus4	DQ11		Comp_of_IOB22A	True	V2	Pull up&HighZ
IOB26A	I/O	9	bus4	DQS11		True_of_IOB26B	True	U2	Pull up&HighZ
IOB26B	I/O	9	bus4	DQS11		Comp_of_IOB26A	True	U1	Pull up&HighZ
IOB31A/GCLKT_14	I/O	9	bus4	DQ11	GCLKT_14	True_of_IOB31B	True	R3	Pull up&HighZ
IOB31B/GCLKC_14	I/O	9	bus4	DQ11	GCLKC_14	Comp_of_IOB31A	True	T2	Pull up&HighZ
IOB33A/GCLKT_15	I/O	9	bus4	DQ11	GCLKT_15	True_of_IOB33B	True	R2	Pull up&HighZ
IOB33B/GCLKC_15	I/O	9	bus4	DQ11	GCLKC_15	Comp_of_IOB33A	True	R1	Pull up&HighZ
IOB38A/GCLKT_13	I/O	9	bus4	DQ10	GCLKT_13	True_of_IOB38B	True	P4	Pull up&HighZ
IOB38B/GCLKC_13	I/O	9	bus4	DQ10	GCLKC_13	Comp_of_IOB38A	True	P3	Pull up&HighZ
IOB3A/BPLL0_T_IN3/BPLL0_T_FB4	I/O	9	bus4	DQ12	BPLL0_T_IN3/BPLL0_T_FB4	True_of_IOB3B	True	U7	Pull up&HighZ
IOB3B/BPLL0_C_IN3/BPLL0_C_FB4	I/O	9	bus4	DQ12	BPLL0_C_IN3/BPLL0_C_FB4	Comp_of_IOB3A	True	V6	Pull up&HighZ
IOB40A/GCLKT_12	I/O	9	bus4	DQ10	GCLKT_12	True_of_IOB40B	True	N3	Pull up&HighZ
IOB40B/GCLKC_12	I/O	9	bus4	DQ10	GCLKC_12	Comp_of_IOB40A	True	N2	Pull up&HighZ
IOB42A	I/O	9	bus4	DQ10		True_of_IOB42B	True	M4	Pull up&HighZ
IOB42B	I/O	9	bus4	DQ10		Comp_of_IOB42A	True	N4	Pull up&HighZ
IOB44A	I/O	9	bus4	DQS10		True_of_IOB44B	True	N1	Pull up&HighZ
IOB44B	I/O	9	bus4	DQS10		Comp_of_IOB44A	True	P1	Pull up&HighZ
IOB50A	I/O	9	bus4	DQ10/D QS_910		True_of_IOB50B	True	M6	Pull up&HighZ
IOB50B	I/O	9	bus4	DQ10/D QS_910		Comp_of_IOB50A	True	N6	Pull up&HighZ
IOB52A	I/O	9	bus4	DQ10		True_of_IOB52B	True	M2	Pull up&HighZ

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324A	配置过程中的IO上下拉状态
IOB52B	I/O	9	bus4	DQ10		Comp_of_IOB52A	True	M1	Pull up&HighZ
IOB58A	I/O	9	bus4	DQ9		True_of_IOB58B	True	L5	Pull up&HighZ
IOB58B	I/O	9	bus4	DQ9		Comp_of_IOB58A	True	M5	Pull up&HighZ
IOB5A/BPLL0_T_IN4/BPLL0_T_FB3	I/O	9	bus4	DQ12	BPLL0_T_IN4/BPLL0_T_FB3	True_of_IOB5B	True	R7	Pull up&HighZ
IOB5B/BPLL0_C_IN4/BPLL0_C_FB3	I/O	9	bus4	DQ12	BPLL0_C_IN4/BPLL0_C_FB3	Comp_of_IOB5A	True	T7	Pull up&HighZ
IOB60A	I/O	9	bus4	DQ9		True_of_IOB60B	True	L4	Pull up&HighZ
IOB60B	I/O	9	bus4	DQ9		Comp_of_IOB60A	True	L3	Pull up&HighZ
IOB62A	I/O	9	bus4	DQS9		True_of_IOB62B	True	K2	Pull up&HighZ
IOB62B	I/O	9	bus4	DQS9		Comp_of_IOB62A	True	K1	Pull up&HighZ
IOB65A	I/O	9	bus4	DQ9		True_of_IOB65B	True	K3	Pull up&HighZ
IOB65B	I/O	9	bus4	DQ9		Comp_of_IOB65A	True	L2	Pull up&HighZ
IOB67A/BPLL1_T_IN2	I/O	9	bus4	DQ9	BPLL1_T_IN2	True_of_IOB67B	True	J5	Pull up&HighZ
IOB67B/BPLL1_C_IN2	I/O	9	bus4	DQ9	BPLL1_C_IN2	Comp_of_IOB67A	True	J4	Pull up&HighZ
IOB69B	I/O	9	bus4	DQ9		Comp_of_IOB69A	True	J6	Pull up&HighZ
IOB71A/BPLL1_T_IN3/BPLL1_T_FB4	I/O	9	bus4	DQ9	BPLL1_T_IN3/BPLL1_T_FB4	True_of_IOB71B	True	K6	Pull up&HighZ
IOB71B/BPLL1_C_IN3/BPLL1_C_FB4	I/O	9	bus4	DQ9	BPLL1_C_IN3/BPLL1_C_FB4	Comp_of_IOB71A	True	K5	Pull up&HighZ
IOB7A	I/O	9	bus4	DQ12		True_of_IOB7B	True	R6	Pull up&HighZ
BANK8 True LVDS Pair									
IOB75A	I/O	8	bus4	DQ8		True_of_IOB75B	True	V16	Pull up&HighZ
IOB75B	I/O	8	bus4	DQ8		Comp_of_IOB75A	True	V17	Pull up&HighZ
IOB77A/BPLL1_T_IN4/BPLL1_T_FB3	I/O	8	bus4	DQ8	BPLL1_T_IN4/BPLL1_T_FB3	True_of_IOB77B	True	U15	Pull up&HighZ
IOB77B/BPLL1_C_IN4/BPLL1_C_FB3	I/O	8	bus4	DQ8	BPLL1_C_IN4/BPLL1_C_FB3	Comp_of_IOB77A	True	U16	Pull up&HighZ
IOB79A/CSI_B	I/O	8	bus4	DQ8	CSI_B	True_of_IOB79B	True	T17	Pull up&HighZ
IOB79B	I/O	8	bus4	DQ8		Comp_of_IOB79A	True	U17	Pull up&HighZ
IOB81A/GCLKT_11	I/O	8		DQ8	GCLKT_11	True_of_IOB81B	True	R16	Pull up&HighZ
IOB81B/GCLKC_11	I/O	8		DQ8	GCLKC_11	Comp_of_IOB81A	True	R17	Pull up&HighZ
IOB85A/RDWR_B	I/O	8		DQS8	RDWR_B	True_of_IOB85B	True	R18	Pull up&HighZ
IOB85B/CSO_B/DOUT	I/O	8		DQS8	CSO_B/DOUT	Comp_of_IOB85A	True	T18	Pull up
IOB87A/GCLKT_10	I/O	8	bus4	DQ8	GCLKT_10	True_of_IOB87B	True	T14	Pull up&HighZ
IOB87B/GCLKC_10	I/O	8	bus4	DQ8	GCLKC_10	Comp_of_IOB87A	True	T15	Pull up&HighZ
IOB93A/GCLKT_9	I/O	8	bus4	DQ7	GCLKT_9	True_of_IOB93B	True	P14	Pull up&HighZ
IOB93B/GCLKC_9	I/O	8	bus4	DQ7	GCLKC_9	Comp_of_IOB93A	True	R15	Pull up&HighZ
IOB95A/GCLKT_8	I/O	8	bus4	DQ7	GCLKT_8	True_of_IOB95B	True	P15	Pull up&HighZ
IOB95B/GCLKC_8	I/O	8	bus4	DQ7	GCLKC_8	Comp_of_IOB95A	True	P16	Pull up&HighZ
IOB97A/D14	I/O	8	bus4	DQ7	D14	True_of_IOB97B	True	N18	Pull up&HighZ
IOB97B/D15	I/O	8	bus4	DQ7	D15	Comp_of_IOB97A	True	P18	Pull up&HighZ

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324A	配置过程中的IO上下拉状态
I0B99A	I/O	8	bus4	DQS7		True_of_I0B99B	True	N16	Pull up&HighZ
I0B99B/D13	I/O	8	bus4	DQS7	D13	Comp_of_I0B99A	True	N17	Pull up&HighZ
BANK7 True LVDS Pair									
I0B102A/D11	I/O	7	bus4	DQ7	D11	True_of_I0B102B	True	M14	Pull up&HighZ
I0B102B/D12	I/O	7	bus4	DQ7	D12	Comp_of_I0B102A	True	N14	Pull up&HighZ
I0B104A/D09	I/O	7	bus4	DQ7	D09	True_of_I0B104B	True	M16	Pull up&HighZ
I0B104B/D10	I/O	7	bus4	DQ7	D10	Comp_of_I0B104A	True	M17	Pull up&HighZ
I0B111A/MCS_N	I/O	7	bus4	DQ6	MCS_N	True_of_I0B111B	True	L15	Pull up&HighZ
I0B111B/D08	I/O	7	bus4	DQ6	D08	Comp_of_I0B111A	True	M15	Pull up&HighZ
I0B113A/D06	I/O	7	bus4	DQ6	D06	True_of_I0B113B	True	J14	Pull up&HighZ
I0B113B/D07	I/O	7	bus4	DQ6	D07	Comp_of_I0B113A	True	K15	Pull up&HighZ
I0B115A/D04	I/O	7	bus4	DQ6	D04	True_of_I0B115B	True	K17	Pull up&HighZ
I0B115B/D05	I/O	7	bus4	DQ6	D05	Comp_of_I0B115A	True	L18	Pull up&HighZ
I0B117A/PUDC_B	I/O	7	bus4	DQS6	PUDC_B	True_of_I0B117B	True	J18	Pull down
I0B117B/EMCCLK	I/O	7	bus4	DQS6	EMCCLK	Comp_of_I0B117A	True	K18	Pull up&HighZ
I0B120A/D02/MI2	I/O	7	bus4	DQ6	D02/MI2	True_of_I0B120B	True	J15	Pull up&HighZ
I0B120B/D03/MI3	I/O	7	bus4	DQ6	D03/MI3	Comp_of_I0B120A	True	J16	Pull up&HighZ
BANK6 True LVDS Pair									
I0B128A/D00/MOSI/MI0	I/O	6	bus4	DQ6	D00/MOSI/MI0	True_of_I0B128B	True	K16	Pull up&HighZ
I0B128B/D01/DIN/MISO/MI1	I/O	6	bus4	DQ6	D01/DIN/MISO/MI1	Comp_of_I0B128A	True	L17	Pull up&HighZ
I0B136A	I/O	6	bus4	none		True_of_I0B136B	True	U9	Pull up&HighZ
I0B136B	I/O	6	bus4	none		Comp_of_I0B136A	True	V9	Pull up&HighZ
I0B138A	I/O	6	bus4	none		True_of_I0B138B	True	U11	Pull up&HighZ
I0B138B	I/O	6	bus4	none		Comp_of_I0B138A	True	V11	Pull up&HighZ
I0B140A	I/O	6	bus4	none		True_of_I0B140B	True	T12	Pull up&HighZ
I0B140B	I/O	6	bus4	none		Comp_of_I0B140A	True	U12	Pull up&HighZ
I0B142A	I/O	6	bus4	none		True_of_I0B142B	True	V12	Pull up&HighZ
I0B142B	I/O	6	bus4	none		Comp_of_I0B142A	True	V13	Pull up&HighZ
I0B144A	I/O	6		none		True_of_I0B144B	True	U14	Pull up&HighZ
I0B144B	I/O	6		none		Comp_of_I0B144A	True	V14	Pull up&HighZ
I0B146A	I/O	6		none		True_of_I0B146B	True	R13	Pull up&HighZ
I0B146B	I/O	6		none		Comp_of_I0B146A	True	T13	Pull up&HighZ

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324A	配置过程中的IO上下拉状态
BANK3 True LVDS Pair									
IOR11A/TPLL1_T_IN3/TPLL1_T_FB4	I/O	3	bus3	none	TPLL1_T_IN3/TPLL1_T_FB4	True_of_IOR11B	True	E12	Pull up&HighZ
IOR11B/MODE2/TPLL1_C_IN3/TPLL1_C_FB4	I/O	3	bus3	none	MODE2/TPLL1_C_IN3/TPLL1_C_FB4	Comp_of_IOR11A	True	F13	pull up
IOR5A/CCLK	I/O	3		none	CCLK	True_of_IOR5B	True	E8	Pull up&HighZ
IOR5B/READY	I/O	3		none	READY	Comp_of_IOR5A	True	T10	Pull down
IOR7A/MODE1	I/O	3		none	MODE1	True_of_IOR7B	True	R11	Pull up
IOR7B/MODE0	I/O	3		none	MODE0	Comp_of_IOR7A	True	R12	Pull down
IOR9A/DONE	I/O	3		none	DONE	True_of_IOR9B	True	F12	Pull down
IOR9B/RECONFIG_N	I/O	3		none	RECONFIG_N	Comp_of_IOR9A	True	P10	Pull up
BANK2 True LVDS Pair									
IOT113A/GCLKT_2/Q0REF_T_IN1	I/O	2	bus1	DQ2	GCLKT_2/Q0REF_T_IN1	True_of_IOT113B	True	E15	Pull up&HighZ
IOT113B/GCLKC_2/Q0REF_C_IN1	I/O	2	bus1	DQ2	GCLKC_2/Q0REF_C_IN1	Comp_of_IOT113A	True	D15	Pull up&HighZ
IOT115A/GCLKT_3	I/O	2	bus1	DQ2	GCLKT_3	True_of_IOT115B	True	E16	Pull up&HighZ
IOT115B/GCLKC_3	I/O	2	bus1	DQ2	GCLKC_3	Comp_of_IOT115A	True	D16	Pull up&HighZ
IOT117A	I/O	2	bus1	DQS2		True_of_IOT117B	True	B16	Pull up&HighZ
IOT117B	I/O	2	bus1	DQS2		Comp_of_IOT117A	True	A17	Pull up&HighZ
IOT120A	I/O	2	bus1	DQ2		True_of_IOT120B	True	C16	Pull up&HighZ
IOT120B	I/O	2	bus1	DQ2		Comp_of_IOT120A	True	B17	Pull up&HighZ
IOT122A	I/O	2	bus1	DQ2		True_of_IOT122B	True	E17	Pull up&HighZ
IOT122B	I/O	2	bus1	DQ2		Comp_of_IOT122A	True	D18	Pull up&HighZ
IOT124A	I/O	2	bus1	DQ2		True_of_IOT124B	True	C17	Pull up&HighZ
IOT124B	I/O	2	bus1	DQ2		Comp_of_IOT124A	True	C18	Pull up&HighZ
IOT129A	I/O	2	bus0	DQ3		True_of_IOT129B	True	G17	Pull up&HighZ
IOT129B	I/O	2	bus0	DQ2		Comp_of_IOT129A	True	F18	Pull up&HighZ
IOT131A	I/O	2	bus0	DQ3		True_of_IOT131B	True	H16	Pull up&HighZ
IOT131B	I/O	2	bus0	DQ3		Comp_of_IOT131A	True	G16	Pull up&HighZ
IOT135A	I/O	2	bus0	DQS3		True_of_IOT135B	True	G15	Pull up&HighZ
IOT135B	I/O	2	bus0	DQS3		Comp_of_IOT135A	True	F15	Pull up&HighZ
IOT138A	I/O	2	bus0	DQ3		True_of_IOT138B	True	G14	Pull up&HighZ
IOT138B	I/O	2	bus0	DQ3		Comp_of_IOT138A	True	F14	Pull up&HighZ
IOT142A	I/O	2	bus0	DQ3		True_of_IOT142B	True	H17	Pull up&HighZ
IOT142B	I/O	2	bus0	DQ3		Comp_of_IOT142A	True	H18	Pull up&HighZ
IOT146A/TPLL1_T_IN4/TPLL1_T_FB3	I/O	2	bus0	DQ3	TPLL1_T_IN4/TPLL1_T_FB3	True_of_IOT146B	True	F17	Pull up&HighZ
IOT146B/TPLL1_C_IN4/TPLL1_C_FB3	I/O	2	bus0	DQ3	TPLL1_C_IN4/TPLL1_C_FB3	Comp_of_IOT146A	True	E18	Pull up&HighZ

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324A	配置过程中的IO上下拉状态
BANK1 True LVDS Pair									
IOT102A	I/O	1	bus2	DQ1		True_of_IOT102B	True	B14	Pull up&HighZ
IOT102B	I/O	1	bus2	DQ1		Comp_of_IOT102A	True	A15	Pull up&HighZ
IOT104A/GCLKT_1	I/O	1	bus2	DQ1	GCLKT_1	True_of_IOT104B	True	E13	Pull up&HighZ
IOT104B/GCLKC_1	I/O	1	bus2	DQ1	GCLKC_1	Comp_of_IOT104A	True	D14	Pull up&HighZ
IOT106A/GCLKT_0	I/O	1	bus2	DQ1	GCLKT_0	True_of_IOT106B	True	D13	Pull up&HighZ
IOT106B/GCLKC_0	I/O	1	bus2	DQ1	GCLKC_0	Comp_of_IOT106A	True	C13	Pull up&HighZ
IOT76A/TPLL0_T_IN2	I/O	1	bus2	DQ0	TPLL0_T_IN2	True_of_IOT76B	True	D8	Pull up&HighZ
IOT76B/TPLL0_C_IN2	I/O	1	bus2	DQ0	TPLL0_C_IN2	Comp_of_IOT76A	True	C8	Pull up&HighZ
IOT80A/TPLL0_T_IN3/TPLL0_T_FB4	I/O	1	bus2	DQ0	TPLL0_T_IN3/TPLL0_T_FB4	True_of_IOT80B	True	D9	Pull up&HighZ
IOT80B/TPLL0_C_IN3/TPLL0_C_FB4	I/O	1	bus2	DQ0	TPLL0_C_IN3/TPLL0_C_FB4	Comp_of_IOT80A	True	C9	Pull up&HighZ
IOT82A/TPLL0_T_IN4/TPLL0_T_FB3/P CIE_HOST_RSTN	I/O	1	bus2	DQ0	TPLL0_T_IN4/TPLL0_T_FB3/P CIE_HOST_RSTN	True_of_IOT82B	True	B10	Pull up&HighZ
IOT82B/TPLL0_C_IN4/TPLL0_C_FB3/P CIE_HOST_RSTN	I/O	1	bus2	DQ0	TPLL0_C_IN4/TPLL0_C_FB3/P CIE_HOST_RSTN	Comp_of_IOT82A	True	A10	Pull up&HighZ
IOT85A	I/O	1		DQS0		True_of_IOT85B	True	B9	Pull up&HighZ
IOT85B	I/O	1		DQS0		Comp_of_IOT85A	True	A9	Pull up&HighZ
IOT87A	I/O	1	bus2	DQ0		True_of_IOT87B	True	C11	Pull up&HighZ
IOT87B	I/O	1	bus2	DQ0		Comp_of_IOT87A	True	B11	Pull up&HighZ
IOT89A/Q0REF_T_IN0	I/O	1	bus2	DQ0	Q0REF_T_IN0	True_of_IOT89B	True	D11	Pull up&HighZ
IOT89B/Q0REF_C_IN0	I/O	1	bus2	DQ0	Q0REF_C_IN0	Comp_of_IOT89A	True	C12	Pull up&HighZ
IOT95A	I/O	1	bus2	DQ1		True_of_IOT95B	True	B12	Pull up&HighZ
IOT95B	I/O	1	bus2	DQ1		Comp_of_IOT95A	True	A12	Pull up&HighZ
IOT97A	I/O	1	bus2	DQ1		True_of_IOT97B	True	A13	Pull up&HighZ
IOT97B	I/O	1	bus2	DQ1		Comp_of_IOT97A	True	A14	Pull up&HighZ
IOT99A	I/O	1	bus2	DQS1		True_of_IOT99B	True	C14	Pull up&HighZ
IOT99B	I/O	1	bus2	DQS1		Comp_of_IOT99A	True	B15	Pull up&HighZ

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
BANK11 True LVDS Pair									
IOL37A	I/O	11		none		True_of_IOL37B	True	E14	pull up
IOL37B	I/O	11		none		Comp_of_IOL37A	True	D15	pull up
IOL39A	I/O	11	bus2	none		True_of_IOL39B	True	B16	pull up
IOL39B	I/O	11	bus2	none		Comp_of_IOL39A	True	A16	pull up
IOL41A	I/O	11	bus2	none		True_of_IOL41B	True	C15	pull up
IOL41B	I/O	11	bus2	none		Comp_of_IOL41A	True	A15	pull up
IOL43A/GCLKT_19/LPLL0_T_IN3/LPLL0_T_FB4	I/O	11	bus2	none	GCLKT_19/LPLL0_T_IN3/LPLL0_T_FB4	True_of_IOL43B	True	F12	pull up
IOL43B/GCLKC_19/LPLL0_C_IN3/LPLL0_C_FB4	I/O	11	bus2	none	GCLKC_19/LPLL0_C_IN3/LPLL0_C_FB4	Comp_of_IOL43A	True	E12	pull up
IOL45A/GCLKT_18	I/O	11	bus2	none	GCLKT_18	True_of_IOL45B	True	G9	pull up
IOL45B/GCLKC_18	I/O	11	bus2	none	GCLKC_18	Comp_of_IOL45A	True	G11	pull up
BANK10 True LVDS Pair									
IOL48A/GCLKT_17	I/O	10	bus2	none	GCLKT_17	True_of_IOL48B	True	G8	pull up
IOL48B/GCLKC_17	I/O	10	bus2	none	GCLKC_17	Comp_of_IOL48A	True	E8	pull up
IOL50A/GCLKT_16	I/O	10	bus2	none	GCLKT_16	True_of_IOL50B	True	E6	pull up
IOL50B/GCLKC_16	I/O	10	bus2	none	GCLKC_16	Comp_of_IOL50A	True	F7	pull up
IOL52A	I/O	10	bus2	none		True_of_IOL52B	True	B3	pull up
IOL52B	I/O	10	bus2	none		Comp_of_IOL52A	True	A3	pull up
IOL66A/LPLL1_T_IN3/LPLL1_T_FB4/PUDC_B	I/O	10	bus2	none	LPLL1_T_IN3/LPLL1_T_FB4/PUDC_B	True_of_IOL66B	True	B2	pull down
IOL66B/LPLL1_C_IN3/LPLL1_C_FB4	I/O	10	bus2	none	LPLL1_C_IN3/LPLL1_C_FB4	Comp_of_IOL66A	True	A2	pull up
BANK9 True LVDS Pair									
IOB12A	I/O	9		DQS12		True_of_IOB12B	True	E3	pull up
IOB12B	I/O	9		DQS12		Comp_of_IOB12A	True	E1	pull up
IOB14A	I/O	9		DQ12		True_of_IOB14B	True	F2	pull up
IOB14B	I/O	9		DQ12		Comp_of_IOB14A	True	F1	pull up
IOB16A	I/O	9	bus4	DQ12		True_of_IOB16B	True	G3	pull up
IOB16B	I/O	9	bus4	DQ12		Comp_of_IOB16A	True	G1	pull up
IOB18A/GCLKT_12	I/O	9	bus4	DQ11	GCLKT_12	True_of_IOB18B	True	H2	pull up
IOB18B/GCLKC_12	I/O	9	bus4	DQ11	GCLKC_12	Comp_of_IOB18A	True	H1	pull up
IOB1A	I/O	9	bus4	DQ12		True_of_IOB1B	True	C2	pull up
IOB1B	I/O	9	bus4	DQ12		Comp_of_IOB1A	True	C1	pull up
IOB20A/GCLKT_15	I/O	9	bus4	DQ11	GCLKT_15	True_of_IOB20B	True	H4	pull up
IOB20B/GCLKC_15	I/O	9	bus4	DQ11	GCLKC_15	Comp_of_IOB20A	True	H3	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
I0B22A	I/O	9	bus4	DQ11		True_of_I0B22B	True	H7	pull up
I0B22B	I/O	9	bus4	DQ11		Comp_of_I0B22A	True	G6	pull up
I0B24A	I/O	9	bus4	DQ11		True_of_I0B24B	True	H6	pull up
I0B24B	I/O	9	bus4	DQ11		Comp_of_I0B24A	True	H5	pull up
I0B26A	I/O	9	bus4	DQS11		True_of_I0B26B	True	L4	pull up
I0B26B	I/O	9	bus4	DQS11		Comp_of_I0B26A	True	L3	pull up
I0B29A	I/O	9	bus4	DQ11		True_of_I0B29B	True	J3	pull up
I0B29B	I/O	9	bus4	DQ11		Comp_of_I0B29A	True	J1	pull up
I0B31A	I/O	9	bus4	DQ11		True_of_I0B31B	True	K2	pull up
I0B31B	I/O	9	bus4	DQ11		Comp_of_I0B31A	True	K1	pull up
I0B33A	I/O	9	bus4	DQ11		True_of_I0B33B	True	L2	pull up
I0B33B	I/O	9	bus4	DQ11		Comp_of_I0B33A	True	L1	pull up
I0B35A/GCLKT_14	I/O	9	bus4	DQ10	GCLKT_14	True_of_I0B35B	True	K4	pull up
I0B35B/GCLKC_14	I/O	9	bus4	DQ11	GCLKC_14	Comp_of_I0B35A	True	K3	pull up
I0B38A/GCLKT_13	I/O	9	bus4	DQ10	GCLKT_13	True_of_I0B38B	True	L5	pull up
I0B38B/GCLKC_13	I/O	9	bus4	DQ10	GCLKC_13	Comp_of_I0B38A	True	K5	pull up
I0B3A/BPLL0_T_IN3/BPLL0_T_FB4	I/O	9	bus4	DQ12	BPLL0_T_IN3/BPLL0_T_FB4	True_of_I0B3B	True	F6	pull up
I0B3B/BPLL0_C_IN3/BPLL0_C_FB4	I/O	9	bus4	DQ12	BPLL0_C_IN3/BPLL0_C_FB4	Comp_of_I0B3A	True	F5	pull up
I0B40A	I/O	9	bus4	DQ10		True_of_I0B40B	True	M3	pull up
I0B40B	I/O	9	bus4	DQ10		Comp_of_I0B40A	True	M1	pull up
I0B42A	I/O	9	bus4	DQ10		True_of_I0B42B	True	N2	pull up
I0B42B	I/O	9	bus4	DQ10		Comp_of_I0B42A	True	N1	pull up
I0B44A	I/O	9	bus4	DQS10		True_of_I0B44B	True	P2	pull up
I0B44B	I/O	9	bus4	DQS10		Comp_of_I0B44A	True	P1	pull up
I0B48A	I/O	9	bus4	DQ10		True_of_I0B48B	True	T2	pull up
I0B48B	I/O	9	bus4	DQ10		Comp_of_I0B48A	True	T1	pull up
I0B50A	I/O	9	bus4	DQ10/D QS_910		True_of_I0B50B	True	U2	pull up
I0B50B	I/O	9	bus4	DQ10/D QS_910		Comp_of_I0B50A	True	U1	pull up
I0B5A/BPLL0_T_IN4/BPLL0_T_FB3	I/O	9	bus4	DQ12	BPLL0_T_IN4/BPLL0_T_FB3	True_of_I0B5B	True	E4	pull up
I0B5B/BPLL0_C_IN4/BPLL0_C_FB3	I/O	9	bus4	DQ12	BPLL0_C_IN4/BPLL0_C_FB3	Comp_of_I0B5A	True	D3	pull up
I0B62A	I/O	9	bus4	DQS9		True_of_I0B62B	True	J7	pull up
I0B62B	I/O	9	bus4	DQS9		Comp_of_I0B62A	True	J6	pull up
I0B65A	I/O	9	bus4	DQ9		True_of_I0B65B	True	N4	pull up
I0B65B	I/O	9	bus4	DQ9		Comp_of_I0B65A	True	N3	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
I0B67A/BPLL1_T_IN2	I/O	9	bus4	DQ9	BPLL1_T_IN2	True_of_I0B67B	True	P4	pull up
I0B67B/BPLL1_C_IN2	I/O	9	bus4	DQ9	BPLL1_C_IN2	Comp_of_I0B67A	True	P3	pull up
I0B69A	I/O	9	bus4	DQ9		True_of_I0B69B	True	L6	pull up
I0B69B	I/O	9	bus4	DQ9		Comp_of_I0B69A	True	M5	pull up
I0B71A/BPLL1_T_IN3/BPLL1_T_FB4	I/O	9	bus4	DQ9	BPLL1_T_IN3/BPLL1_T_FB4	True_of_I0B71B	True	L7	pull up
I0B71B/BPLL1_C_IN3/BPLL1_C_FB4	I/O	9	bus4	DQ9	BPLL1_C_IN3/BPLL1_C_FB4	Comp_of_I0B71A	True	K6	pull up
I0B7A	I/O	9	bus4	DQ12		True_of_I0B7B	True	D2	pull up
I0B7B	I/O	9	bus4	DQ12		Comp_of_I0B7A	True	D1	pull up
I0B9A	I/O	9	bus4	DQ12		True_of_I0B9B	True	F4	pull up
I0B9B	I/O	9	bus4	DQ12		Comp_of_I0B9A	True	F3	pull up
BANK5 True LVDS Pair									
I0R39A/GCLKT_6/RPLL0_T_IN3/RPLL0_T_FB4	I/O	5	bus4	none	GCLKT_6/RPLL0_T_IN3/RPLL0_T_FB4	True_of_I0R39B	True	T9	pull up
I0R39B/GCLKC_6/RPLL0_C_IN3/RPLL0_C_FB4	I/O	5	bus4	none	GCLKC_6/RPLL0_C_IN3/RPLL0_C_FB4	Comp_of_I0R39A	True	V9	pull up
I0R41A/GCLKT_7/D14	I/O	5	bus4	none	GCLKT_7/D14	True_of_I0R41B	True	R8	pull up
I0R41B/GCLKC_7/D15	I/O	5	bus4	none	GCLKC_7/D15	Comp_of_I0R41A	True	T8	pull up
I0R43A	I/O	5		none		True_of_I0R43B	True	M10	pull up
I0R43B	I/O	5		none		Comp_of_I0R43A	True	N9	pull up
I0R45A	I/O	5	bus4	none		True_of_I0R45B	True	M8	pull up
I0R45B	I/O	5	bus4	none		Comp_of_I0R45A	True	N8	pull up
I0R48A	I/O	5	bus4	none		True_of_I0R48B	True	U8	pull up
I0R48B	I/O	5	bus4	none		Comp_of_I0R48A	True	V8	pull up
I0R50A	I/O	5	bus4	none		True_of_I0R50B	True	N7	pull up
I0R50B	I/O	5	bus4	none		Comp_of_I0R50A	True	P8	pull up
I0R52A	I/O	5	bus4	none		True_of_I0R52B	True	U7	pull up
I0R52B	I/O	5	bus4	none		Comp_of_I0R52A	True	V7	pull up
I0R54A	I/O	5	bus4	none		True_of_I0R54B	True	R7	pull up
I0R54B	I/O	5	bus4	none		Comp_of_I0R54A	True	T7	pull up
I0R57A	I/O	5	bus4	DQ5		True_of_I0R57B	True	T6	pull up
I0R57B	I/O	5	bus4	DQ5		Comp_of_I0R57A	True	V6	pull up
I0R59A	I/O	5	bus4	DQ5		True_of_I0R59B	True	N6	pull up
I0R59B	I/O	5	bus4	DQ5		Comp_of_I0R59A	True	P7	pull up
I0R61A/RPLL1_T_IN4/RPLL1_T_FB3/D07/SDA	I/O	5	bus4	DQ5	RPLL1_T_IN4/RPLL1_T_FB3/D07/SDA	True_of_I0R61B	True	R5	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
IOR61B/RPLL1_C_IN4/RPLL1_C_FB3/RDWR_B/SCL	I/O	5	bus4	DQ5	RPLL1_C_IN4/RPLL1_C_FB3/RDWR_B/SCL	Comp_of_IOR61A	True	T5	pull up
IOR63A/D03/SI/SSIO	I/O	5		DQS5	D03/SI/SSIO	True_of_IOR63B	True	U5	pull up
IOR63B/D04/SSPI_CS_N	I/O	5		DQS5	D04/SSPI_CS_N	Comp_of_IOR63A	True	V5	pull up
IOR66A/RPLL1_T_IN3/RPLL1_T_FB4/D05/SO/SS1	I/O	5	bus4	DQ5	RPLL1_T_IN3/RPLL1_T_FB4/D05/SO/SS1	True_of_IOR66B	True	R3	pull up
IOR66B/RPLL1_C_IN3/RPLL1_C_FB4/D06/SSPI_CLK	I/O	5	bus4	DQ5	RPLL1_C_IN3/RPLL1_C_FB4/D06/SSPI_CLK	Comp_of_IOR66A	True	T3	pull up
IOR68A	I/O	5	bus4	DQ5		True_of_IOR68B	True	T4	pull up
IOR68B	I/O	5	bus4	DQ5		Comp_of_IOR68A	True	V4	pull up
IOR70A/D08/SSPI_WPN/SSI2	I/O	5		DQ5	D08/SSPI_WPN/SSI2	True_of_IOR70B	True	N5	pull up
IOR70B/D09/CLKHOLD_N/SSI3	I/O	5		DQ5	D09/CLKHOLD_N/SSI3	Comp_of_IOR70A	True	P6	pull up
IOR72A/RPLL1_T_IN2/READY	I/O	5		DQ5	RPLL1_T_IN2/READY	True_of_IOR72B	True	U3	pull down
IOR72B/RPLL1_C_IN2/CSO_B/MCS_N	I/O	5		DQ5	RPLL1_C_IN2/CSO_B/MCS_N	Comp_of_IOR72A	True	V3	pull up
BANK4 True LVDS Pair									
IOR20A/D01/MI2	I/O	4	bus4	DQ4	D01/MI2	True_of_IOR20B	True	T14	pull up
IOR20B/D02/MI3	I/O	4	bus4	DQ4	D02/MI3	Comp_of_IOR20A	True	V14	pull up
IOR22A	I/O	4	bus4	DQ4		True_of_IOR22B	True	T12	pull up
IOR22B	I/O	4	bus4	DQ4		Comp_of_IOR22A	True	V12	pull up
IOR24A	I/O	4	bus4	DQ4		True_of_IOR24B	True	M11	pull up
IOR24B	I/O	4	bus4	DQ4		Comp_of_IOR24A	True	N11	pull up
IOR26A	I/O	4	bus4	DQS4		True_of_IOR26B	True	N10	pull up
IOR26B	I/O	4	bus4	DQS4		Comp_of_IOR26A	True	P11	pull up
IOR30A	I/O	4	bus4	DQ4		True_of_IOR30B	True	R11	pull up
IOR30B	I/O	4	bus4	DQ4		Comp_of_IOR30A	True	T11	pull up
IOR32A	I/O	4		DQ4		True_of_IOR32B	True	U11	pull up
IOR32B	I/O	4		DQ4		Comp_of_IOR32A	True	V11	pull up
IOR34A/GCLKT_4/D13	I/O	4		DQ4	GCLKT_4/D13	True_of_IOR34B	True	U10	pull up
IOR34B/GCLKC_4/EMCCLK	I/O	4		DQ4	GCLKC_4/EMCCLK	Comp_of_IOR34A	True	V10	pull up
IOR36A/GCLKT_5/RPLL0_T_IN4/RPLL0_T_FB3	I/O	4	bus4	DQ4	GCLKT_5/RPLL0_T_IN4/RPLL0_T_FB3	True_of_IOR36B	True	R10	pull up
IOR36B/GCLKC_5/RPLL0_C_IN4/RPLL0_C_FB3	I/O	4	bus4	DQ4	GCLKC_5/RPLL0_C_IN4/RPLL0_C_FB3	Comp_of_IOR36A	True	T10	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
BANK3 True LVDS Pair									
IOR11A/TPLL01_T_IN3/TPLL01_T_FB4	I/O	3	bus3	none	TPLL01_T_IN3/TPLL01_T_FB4	True_of_IOR11B	True	U16	pull up
IOR11B/TPLL01_C_IN3/TPLL01_C_FB4	I/O	3	bus3	none	TPLL01_C_IN3/TPLL01_C_FB4	Comp_of_IOR11A	True	V16	pull up
IOR13A	I/O	3	bus3	none		True_of_IOR13B	True	U15	pull up
IOR13B	I/O	3	bus3	none		Comp_of_IOR13A	True	V15	pull up
IOR15A/D11	I/O	3	bus3	none	D11	True_of_IOR15B	True	U13	pull up
IOR15B/D12	I/O	3	bus3	none	D12	Comp_of_IOR15A	True	V13	pull up
IOR17A/MODE1	I/O	3	bus3	none	MODE1	True_of_IOR17B	True	N12	pull up
IOR17B/D10	I/O	3	bus3	none	D10	Comp_of_IOR17A	True	P12	pull up
IOR5A/CCLK	I/O	3		none	CCLK	True_of_IOR5B	True	R15	pull up
IOR5B/MODE0	I/O	3		none	MODE0	Comp_of_IOR5A	True	T15	pull down
IOR7A	I/O	3		none		True_of_IOR7B	True	P13	pull up
IOR7B	I/O	3		none		Comp_of_IOR7A	True	R16	pull up
IOR9A/D00/DIN/MISO/MI1	I/O	3		none	D00/DIN/MISO/MI1	True_of_IOR9B	True	R13	pull up
IOR9B/CSI_B/MOSI/MI0	I/O	3		none	CSI_B/MOSI/MI0	Comp_of_IOR9A	True	T13	pull up
BANK2 True LVDS Pair									
IOT111A	I/O	2	bus1	DQ2		True_of_IOT111B	True	J13	pull up
IOT111B	I/O	2	bus1	DQ2		Comp_of_IOT111A	True	K14	pull up
IOT113A/GCLKT_2/Q0REF_T_IN1	I/O	2	bus1	DQ2	GCLKT_2/Q0REF_T_IN1	True_of_IOT113B	True	K15	pull up
IOT113B/GCLKC_2/Q0REF_C_IN1	I/O	2	bus1	DQ2	GCLKC_2/Q0REF_C_IN1	Comp_of_IOT113A	True	K16	pull up
IOT115A/GCLKT_3	I/O	2	bus1	DQ2	GCLKT_3	True_of_IOT115B	True	L12	pull up
IOT115B/GCLKC_3	I/O	2	bus1	DQ2	GCLKC_3	Comp_of_IOT115A	True	L13	pull up
IOT117A	I/O	2	bus1	DQS2		True_of_IOT117B	True	N15	pull up
IOT117B	I/O	2	bus1	DQS2		Comp_of_IOT117A	True	N16	pull up
IOT120A	I/O	2	bus1	DQ2		True_of_IOT120B	True	N17	pull up
IOT120B	I/O	2	bus1	DQ2		Comp_of_IOT120A	True	N18	pull up
IOT122A	I/O	2	bus1	DQ2		True_of_IOT122B	True	P17	pull up
IOT122B	I/O	2	bus1	DQ2		Comp_of_IOT122A	True	P18	pull up
IOT124A	I/O	2	bus1	DQ2		True_of_IOT124B	True	T17	pull up
IOT124B	I/O	2	bus1	DQ2		Comp_of_IOT124A	True	T18	pull up
IOT126A	I/O	2	bus1	DQ2		True_of_IOT126B	True	U17	pull up
IOT126B	I/O	2	bus1	DQ2		Comp_of_IOT126A	True	U18	pull up
IOT129A	I/O	2	bus0	DQ3		True_of_IOT129B	True	L14	pull up
IOT129B	I/O	2	bus0	DQ2		Comp_of_IOT129A	True	M13	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
IOT131A	I/O	2	bus0	DQ3		True_of_IOT131B	True	M14	pull up
IOT131B	I/O	2	bus0	DQ3		Comp_of_IOT131A	True	N14	pull up
IOT142A	I/O	2	bus0	DQ3		True_of_IOT142B	True	P15	pull up
IOT142B/DOUT	I/O	2	bus0	DQ3	DOUT	Comp_of_IOT142A	True	P16	pull up
BANK1 True LVDS Pair									
IOT102A	I/O	1	bus2	DQ1		True_of_IOT102B	True	J16	pull up
IOT102B	I/O	1	bus2	DQ1		Comp_of_IOT102A	True	J18	pull up
IOT104A	I/O	1	bus2	DQ1		True_of_IOT104B	True	L17	pull up
IOT104B	I/O	1	bus2	DQ1		Comp_of_IOT104A	True	L18	pull up
IOT106A	I/O	1	bus2	DQ1		True_of_IOT106B	True	M16	pull up
IOT106B	I/O	1	bus2	DQ1		Comp_of_IOT106A	True	M18	pull up
IOT108A/GCLKT_1	I/O	1	bus2	DQ2	GCLKT_1	True_of_IOT108B	True	L15	pull up
IOT108B/GCLKC_1	I/O	1	bus2	DQ1	GCLKC_1	Comp_of_IOT108A	True	L16	pull up
IOT74A	I/O	1	bus2	DQ0		True_of_IOT74B	True	H12	pull up
IOT74B	I/O	1	bus2	DQ0		Comp_of_IOT74A	True	G13	pull up
IOT76A/TPLL0_T_IN2	I/O	1	bus2	DQ0	TPLL0_T_IN2	True_of_IOT76B	True	F14	pull up
IOT76B/TPLL0_C_IN2	I/O	1	bus2	DQ0	TPLL0_C_IN2	Comp_of_IOT76A	True	G14	pull up
IOT78A	I/O	1	bus2	DQ0		True_of_IOT78B	True	C17	pull up
IOT78B	I/O	1	bus2	DQ0		Comp_of_IOT78A	True	C18	pull up
IOT80A/TPLL0_T_IN3/TPLL0_T_FB4	I/O	1	bus2	DQ0	TPLL0_T_IN3/TPLL0_T_FB4	True_of_IOT80B	True	F15	pull up
IOT80B/TPLL0_C_IN3/TPLL0_C_FB4	I/O	1	bus2	DQ0	TPLL0_C_IN3/TPLL0_C_FB4	Comp_of_IOT80A	True	F16	pull up
IOT82A/TPLL0_T_IN4/TPLL0_T_FB3/P CIE_HOST_RSTN	I/O	1	bus2	DQ0	TPLL0_T_IN4/TPLL0_T_FB3/P CIE_HOST_RSTN	True_of_IOT82B	True	H13	pull up
IOT82B/TPLL0_C_IN4/TPLL0_C_FB3/P CIE_HOST_RSTN	I/O	1	bus2	DQ0	TPLL0_C_IN4/TPLL0_C_FB3/P CIE_HOST_RSTN	Comp_of_IOT82A	True	H14	pull up
IOT85A	I/O	1		DQS0		True_of_IOT85B	True	D17	pull up
IOT85B	I/O	1		DQS0		Comp_of_IOT85A	True	D18	pull up
IOT87A	I/O	1	bus2	DQ0		True_of_IOT87B	True	E16	pull up
IOT87B	I/O	1	bus2	DQ0		Comp_of_IOT87A	True	E18	pull up
IOT89A/Q0REF_T_IN0	I/O	1	bus2	DQ0	Q0REF_T_IN0	True_of_IOT89B	True	F17	pull up
IOT89B/Q0REF_C_IN0	I/O	1	bus2	DQ0	Q0REF_C_IN0	Comp_of_IOT89A	True	F18	pull up
IOT91A	I/O	1	bus2	DQ1		True_of_IOT91B	True	K12	pull up
IOT91B	I/O	1	bus2	DQ1		Comp_of_IOT91A	True	K13	pull up
IOT93A	I/O	1	bus2	DQ1		True_of_IOT93B	True	H15	pull up
IOT93B	I/O	1	bus2	DQ1		Comp_of_IOT93A	True	H16	pull up
IOT95A	I/O	1	bus2	DQ1		True_of_IOT95B	True	G16	pull up

管脚名称	功能	BANK	ADC_INPUT	DQS	配置功能	差分Pair	LVDS	UG324S	配置过程中的IO上下拉状态
IOT95B	I/O	1	bus2	DQ1		Comp_of_IOT95A	True	G18	pull up
IOT97A/GCLKT_0	I/O	1	bus2	DQ1	GCLKT_0	True_of_IOT97B	True	H17	pull up
IOT97B/GCLKC_0	I/O	1	bus2	DQ1	GCLKC_0	Comp_of_IOT97A	True	H18	pull up
IOT99A	I/O	1	bus2	DQS1		True_of_IOT99B	True	K17	pull up
IOT99B	I/O	1	bus2	DQS1		Comp_of_IOT99A	True	K18	pull up